

# 74AUP1GU04

## Low-power unbuffered inverter

Rev. 6 — 10 February 2022

Product data sheet

## 1. General description

The 74AUP1GU04 is a single unbuffered inverter. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

## 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- CMOS low power dissipation
- Low static power consumption;  $I_{CC} = 0.9 \mu A$  (maximum)
- Low noise overshoot and undershoot < 10% of  $V_{CC}$
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Overvoltage tolerant inputs to 3.6 V
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AUP1GU04GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1GU04GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
74AUP1GU04GF	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1 × 0.5 mm	SOT891
74AUP1GU04GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115
74AUP1GU04GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202
74AUP1GU04GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.35 mm	SOT1226

4. Marking

Table 2. Marking

Type number	Marking code [1]
74AUP1GU04GW	pD
74AUP1GU04GM	pD
74AUP1GU04GF	pD
74AUP1GU04GN	pD
74AUP1GU04GS	pD
74AUP1GU04GX	pD

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram

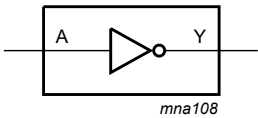


Fig. 1. Logic symbol

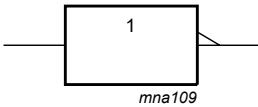


Fig. 2. IEC logic symbol

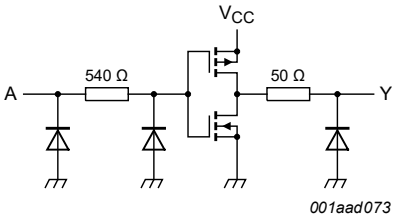
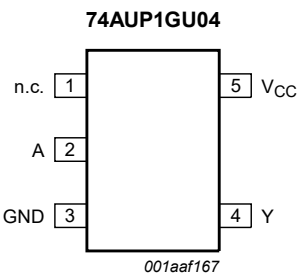


Fig. 3. Logic diagram

6. Pinning information

6.1. Pinning



**74AUP1GU04**

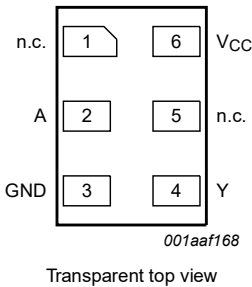
n.c. 1 5 V<sub>CC</sub>

A 2

GND 3 4 Y

001aaf167

**Fig. 4. Pin configuration SOT353-1 (TSSOP5)**



**74AUP1GU04**

n.c. 1 6 V<sub>CC</sub>

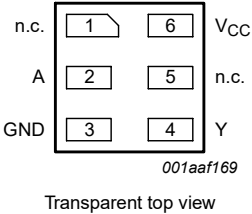
A 2 5 n.c.

GND 3 4 Y

001aaf168

Transparent top view

**Fig. 5. Pin configuration SOT886 (XSON6)**



**74AUP1GU04**

n.c. 1 6 V<sub>CC</sub>

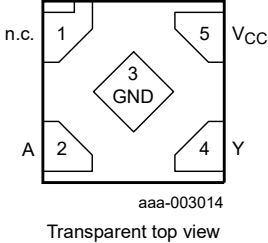
A 2 5 n.c.

GND 3 4 Y

001aaf169

Transparent top view

**Fig. 6. Pin configuration SOT891, SOT1115 and SOT1202 (XSON6)**



**74AUP1GU04**

n.c. 1 5 V<sub>CC</sub>

A 2 4 Y

3 GND

aaa-003014

Transparent top view

**Fig. 7. Pin configuration SOT1226 (X2SON5)**

6.2. Pin description

Table 3. Pin description

Symbol	Pin		Description
	TSSOP5 and X2SON5	XSON6	
n.c.	1	1	not connected
A	2	2	data input
GND	3	3	ground (0 V)
Y	4	4	data output
n.c.	-	5	not connected
V <sub>CC</sub>	5	6	supply voltage

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output
A	Y
L	H
H	L

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		-0.5	+4.6	V
$I_{IK}$	input clamping current	$V_I < 0$ V	-50	-	mA
$V_I$	input voltage	[1]	-0.5	+4.6	V
$I_{OK}$	output clamping current	$V_O < 0$ V	-50	-	mA
$V_O$	output voltage	[1]	-0.5	$V_{CC} + 0.5$	V
$I_O$	output current	$V_O = 0$ V to $V_{CC}$	-	$\pm 20$	mA
$I_{CC}$	supply current		-	+50	mA
$I_{GND}$	ground current		-50	-	mA
$T_{stg}$	storage temperature		-65	+150	°C
$P_{tot}$	total power dissipation	$T_{amb} = -40$ °C to +125 °C [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT886 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT891 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package:  $P_{tot}$  derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1226 (X2SON5) package:  $P_{tot}$  derates linearly with 3.0 mW/K above 67 °C.

## 9. Recommended operating conditions

**Table 6. Recommended operating conditions**

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		0.8	3.6	V
$V_I$	input voltage		0	3.6	V
$V_O$	output voltage		0	$V_{CC}$	V
$T_{amb}$	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 0.8$ V to 3.6 V	0	200	ns/V

## 10. Static characteristics

**Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = 25 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	0.75 × V <sub>CC</sub>	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.25 × V <sub>CC</sub>	V
V <sub>OH</sub>	HIGH-level output voltage	I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.1	µA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.5	µA
C <sub>I</sub>	input capacitance	V <sub>CC</sub> = 0 V to 3.6 V; V <sub>I</sub> = GND or V <sub>CC</sub>	-	1.5	-	pF
C <sub>O</sub>	output capacitance	V <sub>O</sub> = GND; V <sub>CC</sub> = 0 V	-	1.8	-	pF
<b>T<sub>amb</sub> = -40 °C to +85 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	0.75 × V <sub>CC</sub>	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.25 × V <sub>CC</sub>	V
V <sub>OH</sub>	HIGH-level output voltage	I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>OL</sub>	LOW-level output voltage	I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.5	µA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.9	µA
<b>T<sub>amb</sub> = -40 °C to +125 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	0.75 × V <sub>CC</sub>	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.25 × V <sub>CC</sub>	V
V <sub>OH</sub>	HIGH-level output voltage	I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.75	µA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	1.4	µA

## 11. Dynamic characteristics

**Table 8. Dynamic characteristics**

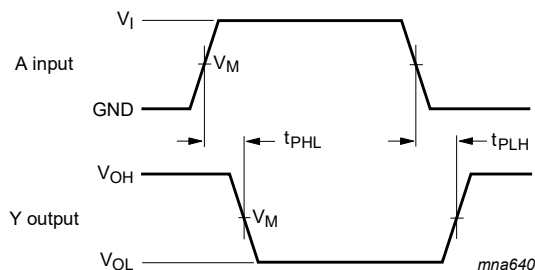
Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 pF										
t <sub>pd</sub>	propagation delay	A to Y; see <a href="#">Fig. 8</a> [2]								
		V <sub>CC</sub> = 0.8 V	-	6.2	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	0.9	2.3	4.4	0.9	4.8	0.9	5.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	0.7	1.7	3.1	0.6	3.4	0.6	3.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	0.5	1.4	2.6	0.5	2.9	0.5	3.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.4	1.1	2.0	0.4	2.3	0.4	2.6	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.3	1.0	1.8	0.3	2.1	0.3	2.4	ns
C <sub>L</sub> = 10 pF										
t <sub>pd</sub>	propagation delay	A to Y; see <a href="#">Fig. 8</a> [2]								
		V <sub>CC</sub> = 0.8 V	-	9.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.2	3.1	6.1	1.2	6.8	1.2	7.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.0	2.3	4.0	0.9	4.6	0.9	5.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	0.8	1.9	3.3	0.7	3.8	0.7	4.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.6	1.5	2.7	0.6	3.1	0.6	3.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.5	1.3	2.4	0.5	2.7	0.5	3.0	ns
C <sub>L</sub> = 15 pF										
t <sub>pd</sub>	propagation delay	A to Y; see <a href="#">Fig. 8</a> [2]								
		V <sub>CC</sub> = 0.8 V	-	13.0	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.6	3.8	7.9	1.4	8.8	1.4	9.7	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.3	2.8	4.9	1.1	5.7	1.1	6.3	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.0	2.3	4.0	0.9	4.7	0.9	5.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.8	1.9	3.2	0.8	3.7	0.8	4.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.7	1.6	2.9	0.7	3.3	0.7	3.7	ns
C <sub>L</sub> = 30 pF										
t <sub>pd</sub>	propagation delay	A to Y; see <a href="#">Fig. 8</a> [2]								
		V <sub>CC</sub> = 0.8 V	-	23.2	-	-	-	-	-	-
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.4	6.0	13.1	2.2	14.8	2.2	16.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.0	4.2	7.6	1.8	9.0	1.8	9.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.7	3.6	6.1	1.5	7.2	1.5	8.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	2.9	4.8	1.3	5.7	1.3	6.3	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.2	2.5	4.3	1.1	5.1	1.1	5.7	ns

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 pF, 10 pF, 15 pF and 30 pF										
C <sub>PD</sub>	power dissipation capacitance	f = 1 MHz; V <sub>I</sub> = GND to V <sub>CC</sub> [3]								
		V <sub>CC</sub> = 0.8 V	-	1.2	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	1.1	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	1.2	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	1.4	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	2.8	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.4	-	-	-	-	-	pF

- [1] All typical values are measured at nominal  $V_{CC}$ .
- [2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .
- [3]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu\text{W}$ ).  
 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$  where:  
 $f_i$  = input frequency in MHz;  
 $f_o$  = output frequency in MHz;  
 $C_L$  = output load capacitance in pF;  
 $V_{CC}$  = supply voltage in V;  
 $N$  = number of inputs switching;  
 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

## 11.1. Waveforms



Measurement points are given in [Table 9](#).

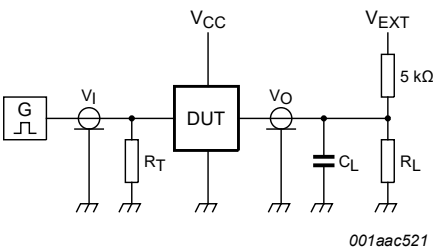
Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage drop that occur with the output load.

**Fig. 8. The data input (A) to output (Y) propagation delays**

**Table 9. Measurement points**

Supply voltage	Output	Input		
$V_{CC}$	$V_M$	$V_M$	$V_I$	$t_r = t_f$
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{CC}$	$\leq 3.0 \text{ ns}$





Test data is given in [Table 10](#).  
Definitions for test circuit:  
 $R_L$  = Load resistance;  
 $C_L$  = Load capacitance including jig and probe capacitance;  
 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator;  
 $V_{EXT}$  = External voltage for measuring switching times.

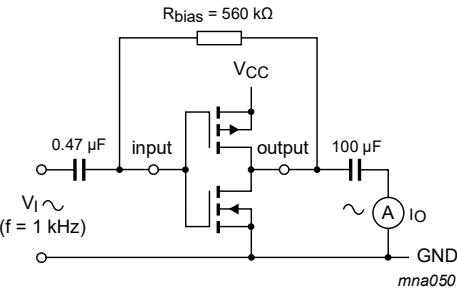
Fig. 9. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		$V_{EXT}$		
$V_{CC}$	$C_L$	$R_L$ [1]	$t_{PLH}$ , $t_{PHL}$	$t_{PZH}$ , $t_{PHZ}$	$t_{PZL}$ , $t_{PLZ}$
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times  $R_L = 5 \text{ k}\Omega$ .  
For measuring propagation delays, setup and hold times and pulse width  $R_L = 1 \text{ M}\Omega$ .

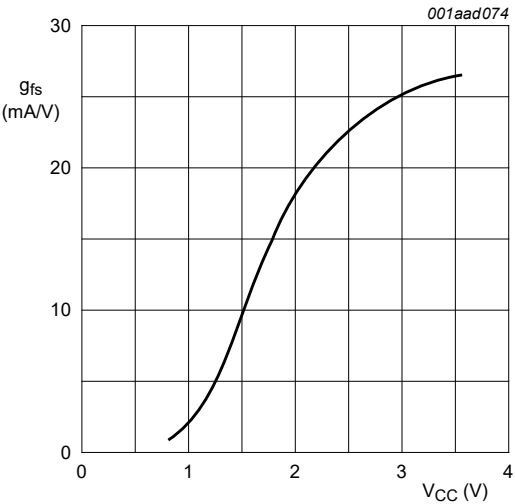
12. Additional characteristics



$$g_{fs} = \frac{\Delta I_O}{\Delta V_i}$$

$V_O$  is constant.

Fig. 10. Test set-up for measuring forward transconductance



$T_{amb} = 25 \text{ }^\circ\text{C}$ .

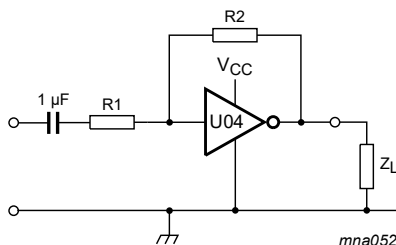
Fig. 11. Typical forward transconductance as a function of supply voltage

## 13. Application information

Some applications for the 74AUP1GU04 are:

- Linear amplifier (see [Fig. 12](#))
- Crystal oscillator (see [Fig. 13](#)).

**Remark:** All values given are typical values unless otherwise specified.



$$Z_L > 10 \text{ k}\Omega.$$

$$R1 \geq 3 \text{ k}\Omega.$$

$$R2 \leq 1 \text{ M}\Omega.$$

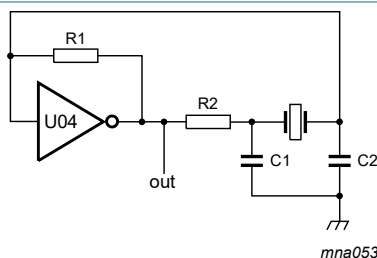
$$\text{Open loop gain: } G_{OL} = 20.$$

$$\text{Voltage amplification: } A_V = - \frac{G_{OL}}{1 + \frac{R1}{R2} (1 + G_{OL})}.$$

$$V_{o(p-p)} = V_{CC} - 1.5 \text{ V centered at } 0.5 \times V_{CC}.$$

$$\text{Unity gain bandwidth product is 5 MHz.}$$

**Fig. 12. Linear amplifier application**



$$C1 = 47 \text{ pF.}$$

$$C2 = 22 \text{ pF.}$$

$$R1 = 1 \text{ M}\Omega \text{ to } 10 \text{ M}\Omega.$$

R2 optimum value depends on the frequency and required stability against changes in  $V_{CC}$  or average minimum  $I_{CC}$  ( $I_{CC} = 2 \text{ mA}$  at  $V_{CC} = 3.3 \text{ V}$  and  $f = 10 \text{ MHz}$ ).

**Fig. 13. Crystal oscillator application**

14. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm SOT353-1

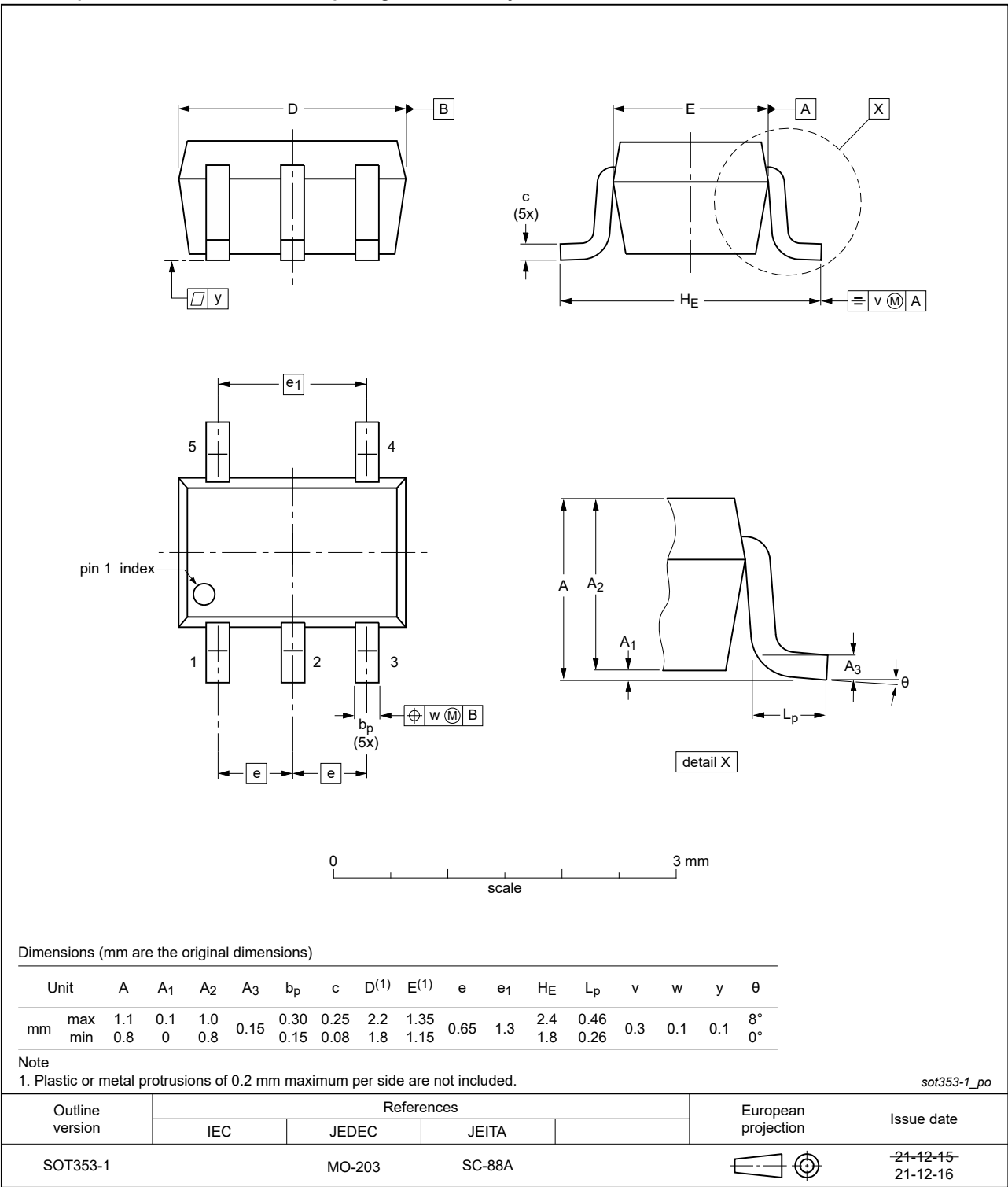


Fig. 14. Package outline SOT353-1 (TSSOP5)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

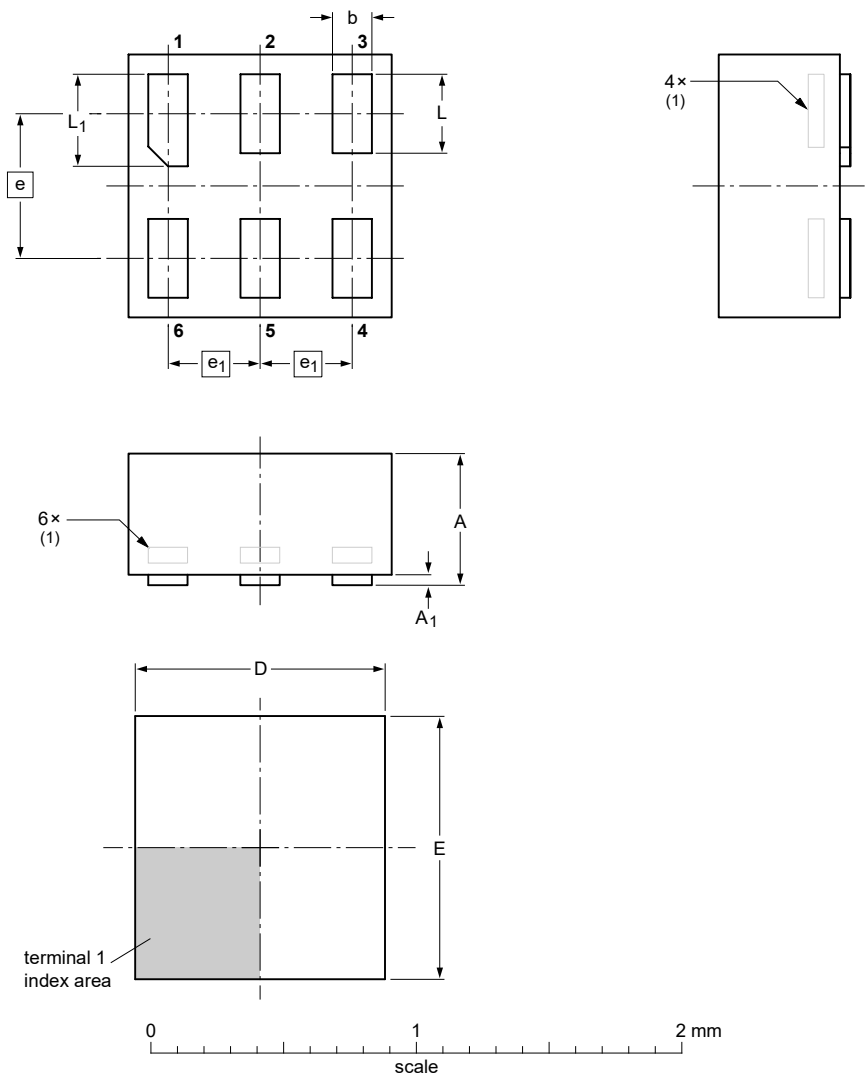
SOT886



Fig. 15. Package outline SOT886 (XSON6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm

SOT891



DIMENSIONS (mm are the original dimensions)

UNIT	A max	A <sub>1</sub> max	b	D	E	e	e <sub>1</sub>	L	L <sub>1</sub>
mm	0.5	0.04	0.20 0.12	1.05 0.95	1.05 0.95	0.55	0.35	0.35 0.27	0.40 0.32

Note  
1. Can be visible in some manufacturing processes.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT891						-05-04-06 07-05-15

Fig. 16. Package outline SOT891 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 0.9 x 1.0 x 0.35 mm

SOT1115

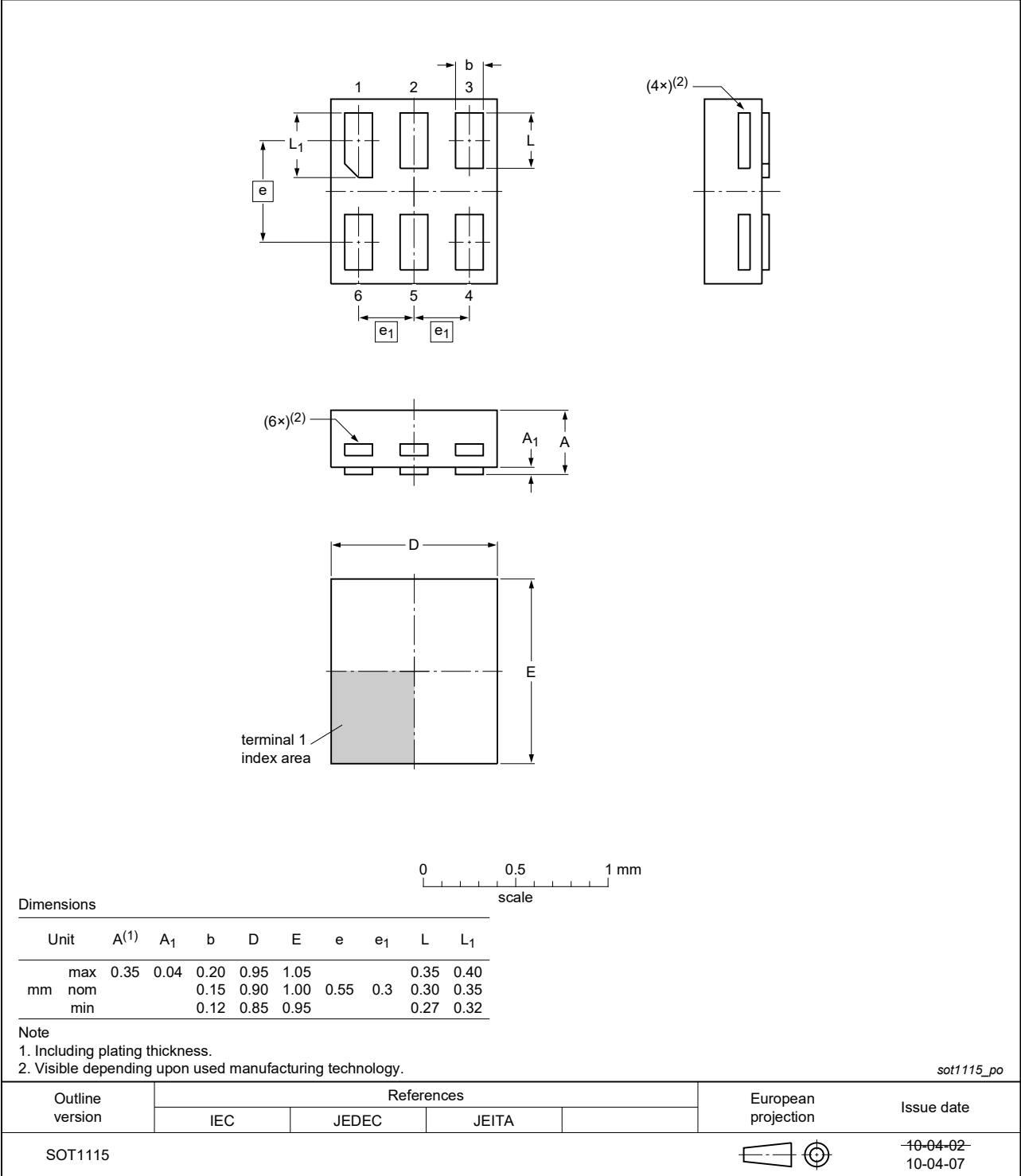


Fig. 17. Package outline SOT1115 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 1.0 x 1.0 x 0.35 mm

SOT1202

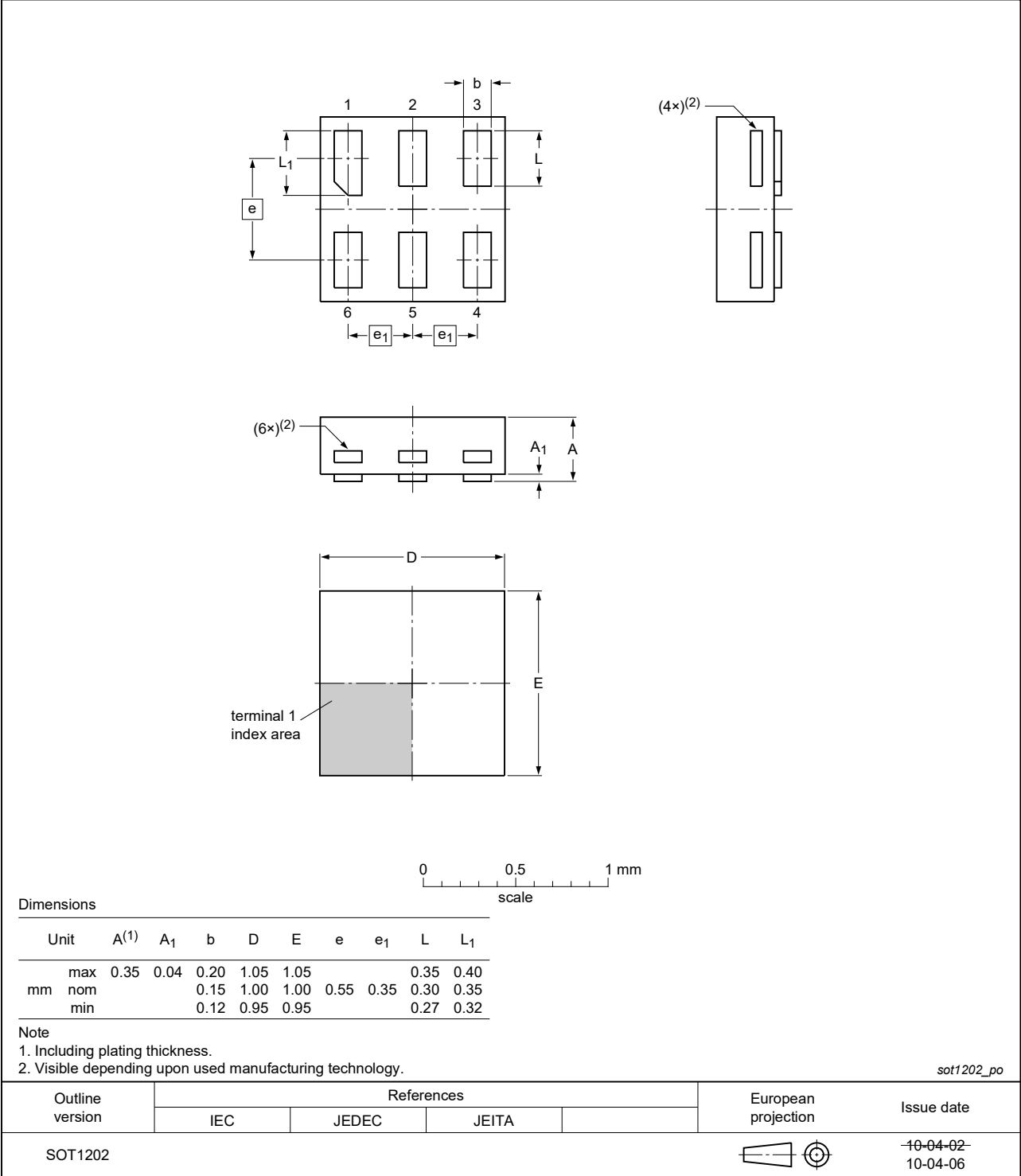


Fig. 18. Package outline SOT1202 (XSON6)

X2SON5: plastic thermal enhanced extremely thin small outline package; no leads;  
5 terminals; body 0.8 x 0.8 x 0.35 mm

SOT1226

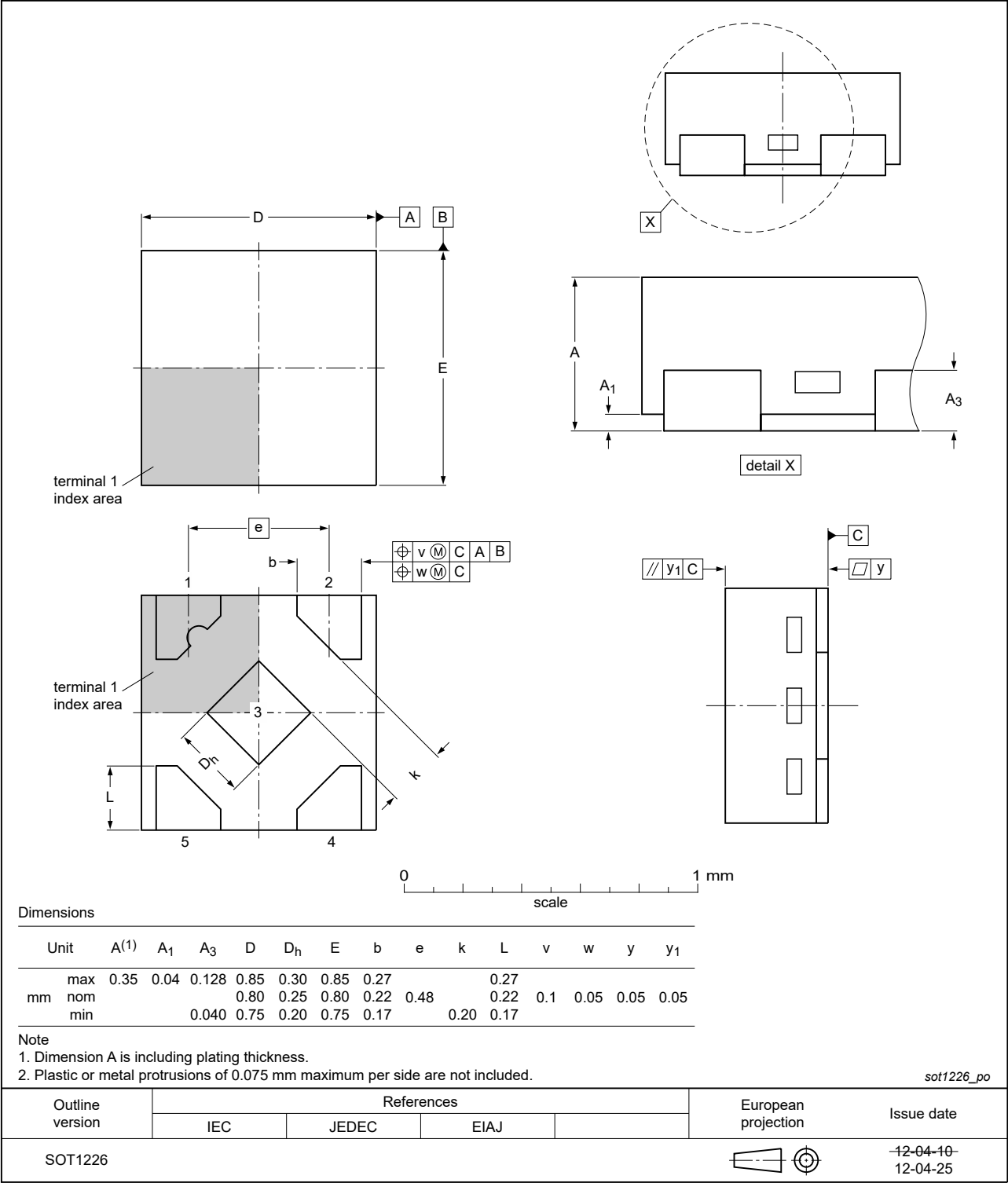


Fig. 19. Package outline SOT1226 (X2SON5)



## 15. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

## 16. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1GU04 v.6	20220210	Product data sheet	-	74AUP1GU04 v.5
Modifications:	<ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li><a href="#">Fig. 14</a>: Package outline drawing for SOT353-1 (TSSOP5) has changed.</li> <li><a href="#">Table 5</a>: Derating values for <math>P_{tot}</math> total power dissipation updated.</li> <li><a href="#">Section 1</a> and <a href="#">Section 2</a> updated.</li> </ul>			
74AUP1GU04 v.5	20120629	Product data sheet	-	74AUP1GU04 v.4
Modifications:	<ul style="list-style-type: none"> <li>Added type number 74AUP1GU04GX (SOT1226).</li> <li>Package outline drawing of SOT886 (<a href="#">Fig. 15</a>) modified.</li> </ul>			
74AUP1GU04 v.4	20111116	Product data sheet	-	74AUP1GU04 v.3
Modifications:	<ul style="list-style-type: none"> <li>Legal pages updated.</li> <li>Package outline drawing SOT363 replaced by SOT353-1.</li> </ul>			
74AUP1GU04 v.3	20100721	Product data sheet	-	74AUP1GU04 v.2
74AUP1GU04 v.2	20060803	Product data sheet	-	74AUP1GU04 v.1
74AUP1GU04 v.1	20050810	Product data sheet	-	-

## 17. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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## Contents

1. General description.....	1
2. Features and benefits.....	1
3. Ordering information.....	2
4. Marking.....	2
5. Functional diagram.....	2
6. Pinning information.....	3
6.1. Pinning.....	3
6.2. Pin description.....	3
7. Functional description.....	3
8. Limiting values.....	4
9. Recommended operating conditions.....	4
10. Static characteristics.....	5
11. Dynamic characteristics.....	7
11.1. Waveforms.....	8
12. Additional characteristics.....	9
13. Application information.....	10
14. Package outline.....	11
15. Abbreviations.....	17
16. Revision history.....	17
17. Legal information.....	18

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