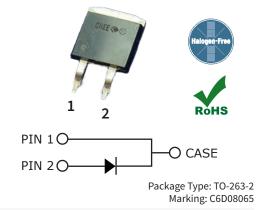


C6D08065G

6th Generation 650 V, 8 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Features

- Low Forward Voltage (V_r) Drop with Positive **Temperature Coefficient**
- Zero Reverse Recovery Current / Forward **Recovery Voltage**
- Temperature-Independent Switching Behavior
- Low Leakage Current (I_P) •

Applications

- Industrial Power Supplies
- Switch Mode Power Supplies
- Server / Telecom Power Supplies
- **Power Factor Correction**
- Solar Inverter
- Uninterruptible Power Supply

Maximum Ratings ($T_c = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	V _{RRM}	650	N			
DC Blocking Voltage	V _{DC}	650	- V			
Continuous Forward Current	- I _F	30	- - - - A	T _J = 25 °C		
		15		T _j = 125 °C	Fig. 3	
	-	8		T _j = 155 °C		
Repetitive Peak Forward Surge Current	I - FRM -	31		T _c = 25 °C, t _p = 10 ms, Half Sine Wave		
		17		$T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$		
Non-Repetitive Peak Forward Surge Current	I - FSM -	56		$T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	Fig. 8	
		48		$T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$		
	I _{F, Max} -	650	-	$T_{c} = 25 \text{ °C}, t_{p} = 10 \mu\text{s}, \text{ Pulse}$		
		590	-	T _c = 110 °C, t _p = 10 μs, Pulse		
Power Dissipation	P _{tot} -	92	- W	T _J = 25 °C	– Fig. 4	
		40		T ₁ = 110 °C		



Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Units	Test Conditions	Note	
Drain-Source Voltage	V _F	1.27	1.40	— v	I _F = 8 A, T _J = 25 °C	- Fig. 1	
		1.37	1.50	— v	I _F = 8 A, T _J = 175 °C		
Reverse Current	I _R	2	20		$V_{R} = 650 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$	- Fig. 2	
		15	200	— μΑ	V _R = 650 V, T _J = 175 °C		
Total Capacitive Charge	Q _c	29		nC	$V_{R} = 400 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	Fig. 5	
		518			$V_{R} = 0 V, T_{J} = 25 °C, f = 1 MHz$		
Total Capacitance	С	56		pF	$V_{R} = 200 \text{ V}, \text{ T}_{J} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$	– Fig. 6 –	
		45			$V_{R} = 400 \text{ V}, \text{ T}_{J} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$		
Capacitance Stored Energy	E _c	4.4		μJ	V _R =400 V	Fig. 7	

Note:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Тур.	Units	Note	
Thermal Resistance, Junction to Case	R _{θ, JC}	1.62	°C / W		
Operating Junction & Storage Temperature	T_{J},T_{stg}	-55 to +175	°C	Fig. 9	

Typical Performance

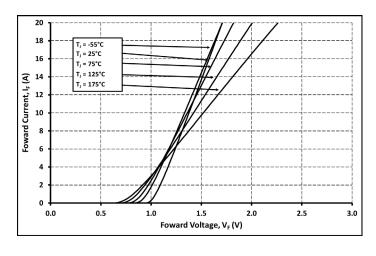


Figure 1. Forward Characteristics

Figure 2. Reverse Characteristics

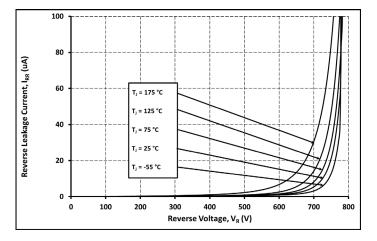
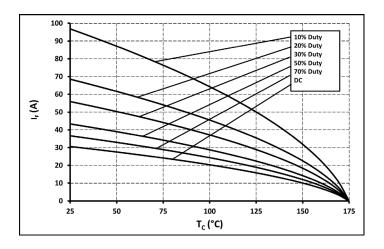


Figure 3. Current Derating





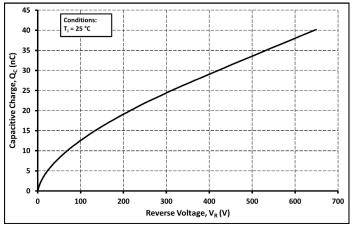


Figure 4. Power Derating

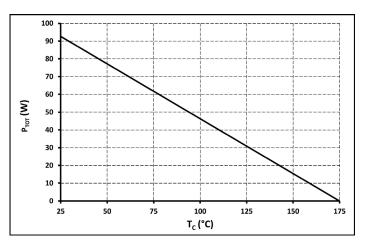
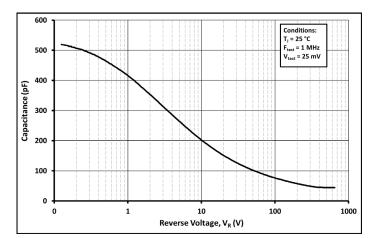


Figure 6. Capacitance vs. Reverse Voltage



Typical Performance

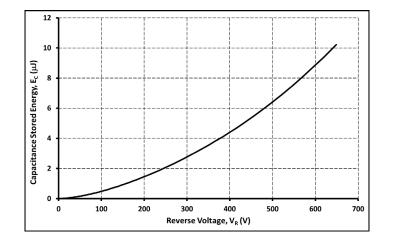
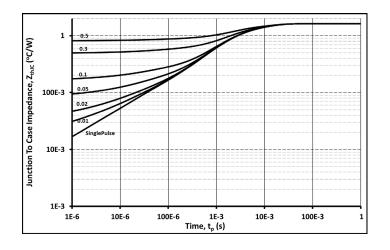


Figure 7. Capacitance Stored Energy

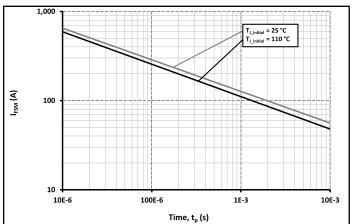
Figure 9. Transient Thermal Impedance



Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Class
Human Body Model	НВМ	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

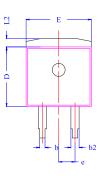


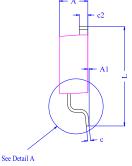


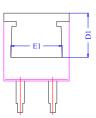
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Package Dimensions

Package: TO-263-2 All dimensions in mm.









A1		0.20	0.25		
b	0.71	0.825	0.94		
b2	1.15	1.275	1.4		
c	0.356	0.4955	0.635		
c2	1.22	1.31	1.4		
D	8.89	9.145	9.4		
D1	6.48	6.78	6.88		
Е	10.04	10.16	10.28		
E1	7.535	7.980	8.425		
е	2.54				
L	14.73	15.24	15.75		
L1	2.29	2.54	2.79		
L2	1.15	1.27	1.39		
θ	0°	4°	8°		

Тур

4.445

Max

4.57

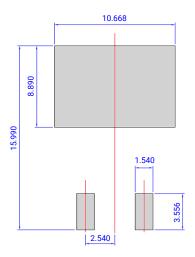
Dim

А

Min

4.32

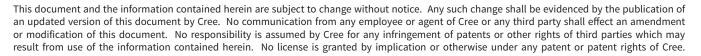
Recommended Solder Pad Layout



Learn more about recommended soldering profiles in this application note.

C6D08065G





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The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of www.cree.com.

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REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact your Cree representative to ensure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

For more information, please contact:

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