

C3M0160120J

Silicon Carbide Power MOSFET

C3M™ MOSFET Technology

N-Channel Enhancement Mode

Features

- 3rd generation SiC MOSFET technology
- Low impedance package with driver source pin
- 7mm of creepage distance between drain and source
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery (Q_{rr})
- Halogen free, RoHS compliant

Benefits

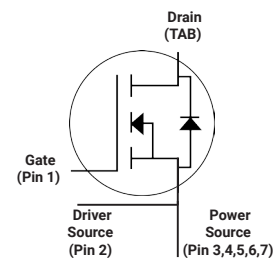
- Reduce switching losses and minimize gate ringing
- Higher system efficiency
- Reduce cooling requirements
- Increase power density
- Increase system switching frequency

Applications

- Renewable energy
- High voltage DC/DC converters
- Switch Mode Power Supplies
- UPS

V_{DS}	1200 V
$I_D @ 25^\circ\text{C}$	17 A
$R_{DS(on)}$	160 mΩ

Package



Part Number	Package	Marking
C3M0160120J	T0-263-7	C3M0160120J

Maximum Ratings ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{DSmax}	Drain - Source Voltage	1200	V	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	
V_{GSmax}	Gate - Source Voltage (dynamic)	-8/+19	V	AC ($f > 1\text{ Hz}$)	Note: 1
V_{GSop}	Gate - Source Voltage (static)	-4/+15	V	Static	Note: 2
I_D	Continuous Drain Current	17	A	$V_{GS} = 15\text{ V}, T_C = 25^\circ\text{C}$	Fig. 19
		12		$V_{GS} = 15\text{ V}, T_C = 100^\circ\text{C}$	
$I_{D(pulse)}$	Pulsed Drain Current	34	A	Pulse width t_p limited by T_{jmax}	Fig. 22
P_D	Power Dissipation	90	W	$T_C = 25^\circ\text{C}, T_J = 150^\circ\text{C}$	Fig. 20
T_J, T_{stg}	Operating Junction and Storage Temperature	-55 to +150	$^\circ\text{C}$		
T_L	Solder Temperature	260	$^\circ\text{C}$	1.6mm (0.063") from case for 10s	

Note (1): When using MOSFET Body Diode $V_{GSmax} = -4\text{V}/+19\text{V}$

Note (2): MOSFET can also safely operate at $0/+15\text{ V}$

Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	1200			V	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	
$V_{GS(th)}$	Gate Threshold Voltage	1.8	2.8	3.6	V	$V_{DS} = V_{GS}, I_D = 2.33\text{ mA}$	Fig. 11
			2.2		V	$V_{DS} = V_{GS}, I_D = 2.33\text{ mA}, T_J = 150^\circ\text{C}$	
I_{DSS}	Zero Gate Voltage Drain Current		1	50	μA	$V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}$	
I_{GSS}	Gate-Source Leakage Current		10	250	nA	$V_{GS} = 15\text{ V}, V_{DS} = 0\text{ V}$	
$R_{DS(on)}$	Drain-Source On-State Resistance		160	208	m Ω	$V_{GS} = 15\text{ V}, I_D = 8.5\text{ A}$	Fig. 4, 5, 6
			256			$V_{GS} = 15\text{ V}, I_D = 8.5\text{ A}, T_J = 150^\circ\text{C}$	
g_{fs}	Transconductance		5.2		S	$V_{DS} = 20\text{ V}, I_{DS} = 8.5\text{ A}$	Fig. 7
			4.9			$V_{DS} = 20\text{ V}, I_{DS} = 8.5\text{ A}, T_J = 150^\circ\text{C}$	
C_{iss}	Input Capacitance		632		pF	$V_{GS} = 0\text{ V}, V_{DS} = 1000\text{ V}$ $f = 1\text{ MHz}$ $V_{AC} = 25\text{ mV}$	Fig. 17, 18
C_{oss}	Output Capacitance		39				
C_{rss}	Reverse Transfer Capacitance		3				
E_{oss}	C_{oss} Stored Energy		22.5		μJ		Fig. 16
E_{ON}	Turn-On Switching Energy (Body Diode FWD)		64		μJ	$V_{DS} = 800\text{ V}, V_{GS} = -4\text{ V}/15\text{ V}, I_D = 8.5\text{ A},$ $R_{G(ext)} = 2.5\text{ }\Omega, L = 336\text{ }\mu\text{H}$	Fig. 26, 29
E_{OFF}	Turn-Off Switching Energy (Body Diode FWD)		13				
$t_{d(on)}$	Turn-On Delay Time		11		ns	$V_{DD} = 800\text{ V}, V_{GS} = -4\text{ V}/15\text{ V}$ $I_D = 8.5\text{ A}, R_{G(ext)} = 0\text{ }\Omega,$ Timing relative to V_{DS} Inductive load	Fig. 27, 28, 29
t_r	Rise Time		8				
$t_{d(off)}$	Turn-Off Delay Time		14				
t_f	Fall Time		8				
$R_{G(int)}$	Internal Gate Resistance		8		Ω	$f = 1\text{ MHz}, V_{AC} = 25\text{ mV}$	
Q_{gs}	Gate to Source Charge		11		nC	$V_{DS} = 800\text{ V}, V_{GS} = -4\text{ V}/15\text{ V}$ $I_D = 8.5\text{ A}$ Per IEC60747-8-4 pg 21	Fig. 12
Q_{gd}	Gate to Drain Charge		5				
Q_g	Total Gate Charge		24				

Reverse Diode Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V_{SD}	Diode Forward Voltage	4.4		V	$V_{GS} = -4\text{ V}, I_{SD} = 3\text{ A}$	Fig. 8, 9, 10
		4.0		V	$V_{GS} = -4\text{ V}, I_{SD} = 3\text{ A}, T_J = 150^\circ\text{C}$	
I_S	Continuous Diode Forward Current		17	A	$V_{GS} = -4\text{ V}$	Note 1
$I_{S, pulse}$	Diode pulse Current		34	A	$V_{GS} = -4\text{ V},$ pulse width t_p limited by T_{jmax}	Note 1
t_{rr}	Reverse Recover time	5		ns	$V_{GS} = -4\text{ V}, I_{SD} = 8.5\text{ A}, V_R = 800\text{ V}$ $\text{dif}/\text{dt} = 8925\text{ A}/\mu\text{s}, T_J = 25^\circ\text{C}$	Note 1, Fig. 29
Q_{rr}	Reverse Recovery Charge	65		nC		
I_{rrm}	Peak Reverse Recovery Current	19		A		
t_{rr}	Reverse Recover time	7		ns	$V_{GS} = -4\text{ V}, I_{SD} = 8.5\text{ A}, V_R = 800\text{ V}$ $\text{dif}/\text{dt} = 2020\text{ A}/\mu\text{s}, T_J = 25^\circ\text{C}$	Note 1, Fig. 29
Q_{rr}	Reverse Recovery Charge	32		nC		
I_{rrm}	Peak Reverse Recovery Current	8		A		

Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Test Conditions	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.38	$^\circ\text{C}/\text{W}$		Fig. 21

Typical Performance

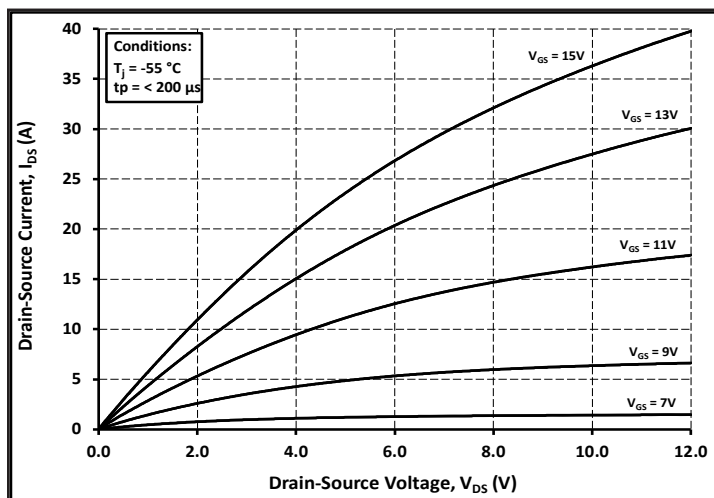


Figure 1. Output Characteristics $T_J = -55\text{ }^{\circ}\text{C}$

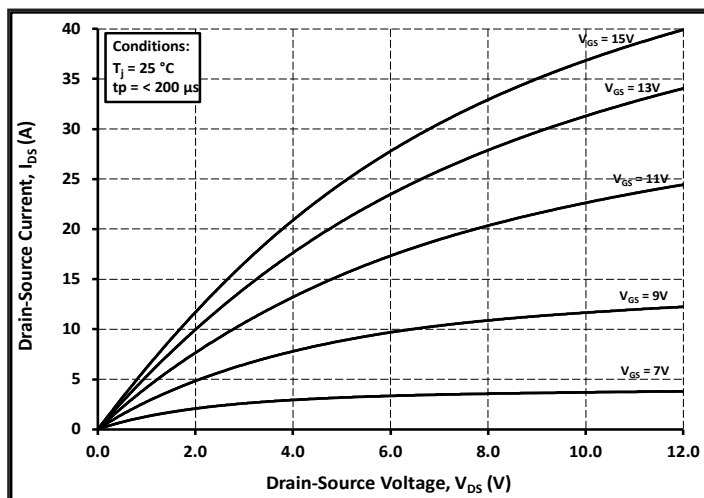


Figure 2. Output Characteristics $T_J = 25\text{ }^{\circ}\text{C}$

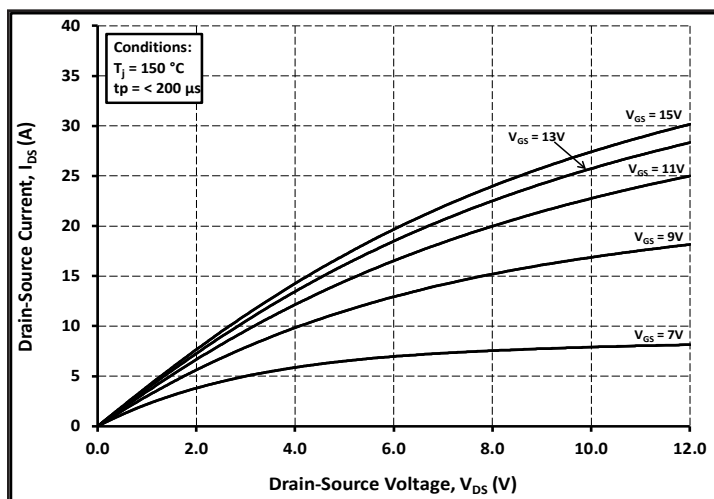


Figure 3. Output Characteristics $T_J = 150\text{ }^{\circ}\text{C}$

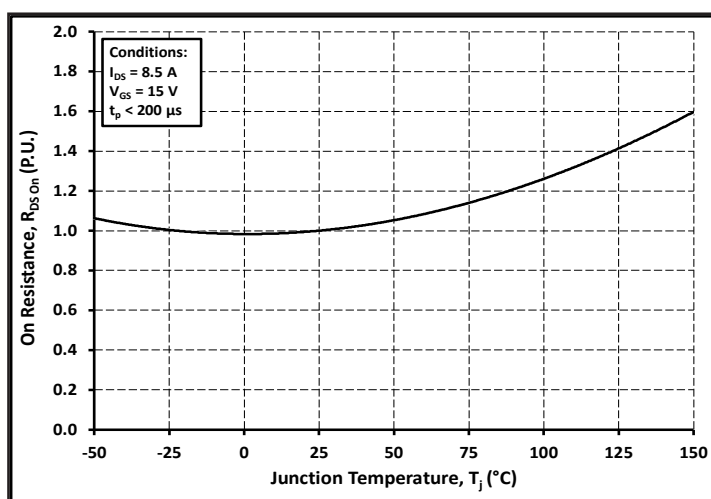


Figure 4. Normalized On-Resistance vs. Temperature

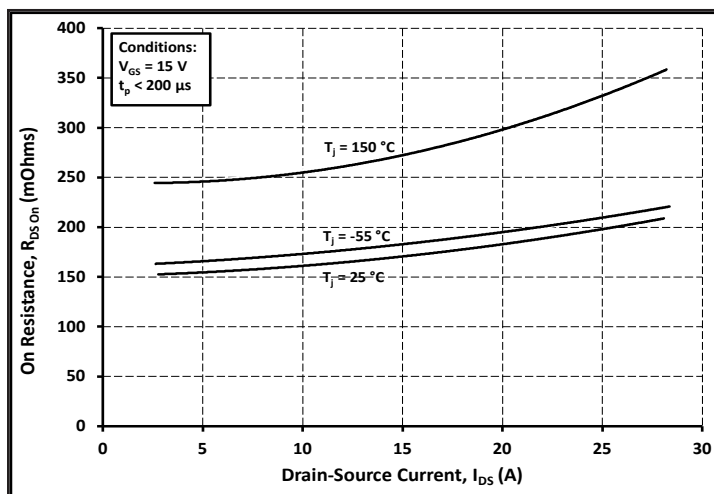


Figure 5. On-Resistance vs. Drain Current
For Various Temperatures

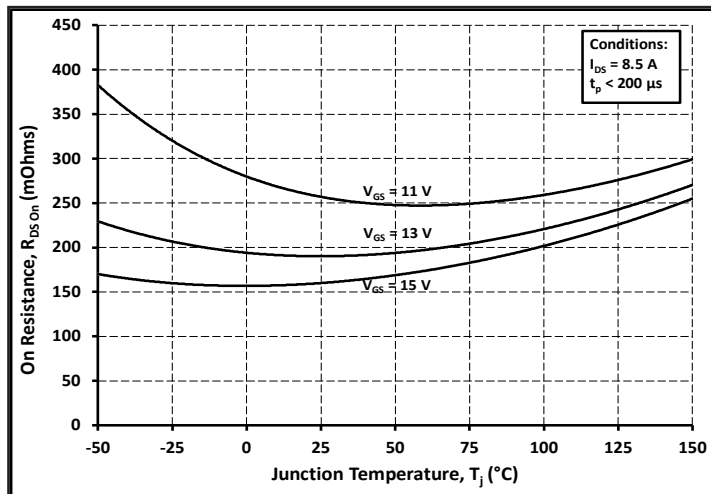


Figure 6. On-Resistance vs. Temperature
For Various Gate Voltage

Typical Performance

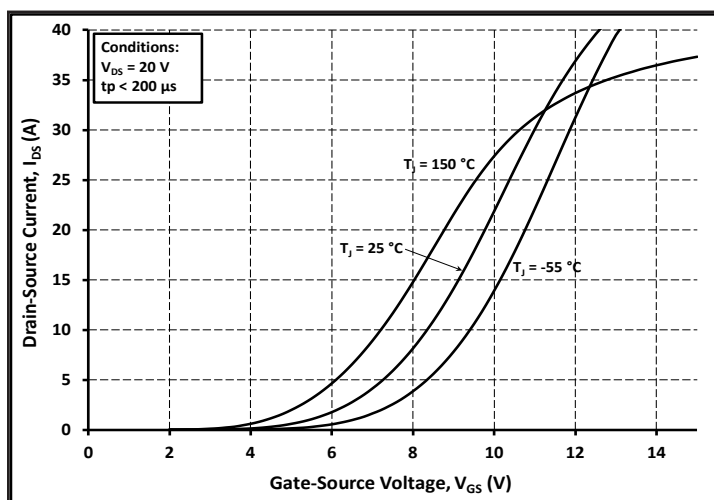


Figure 7. Transfer Characteristic for Various Junction Temperatures

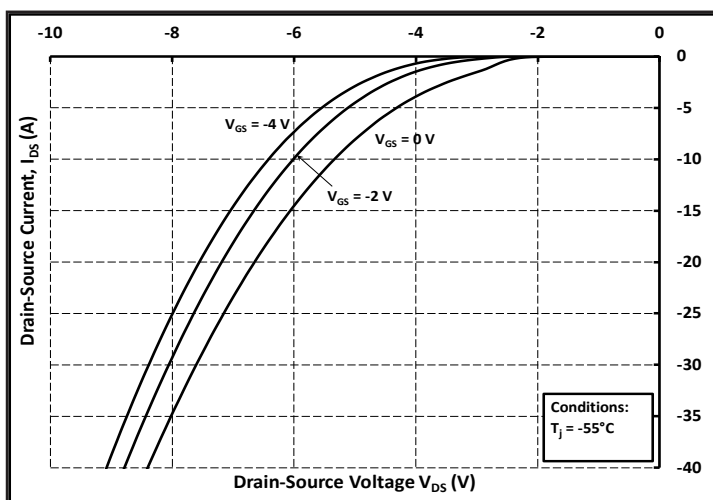


Figure 8. Body Diode Characteristic at $-55\text{ }^{\circ}\text{C}$

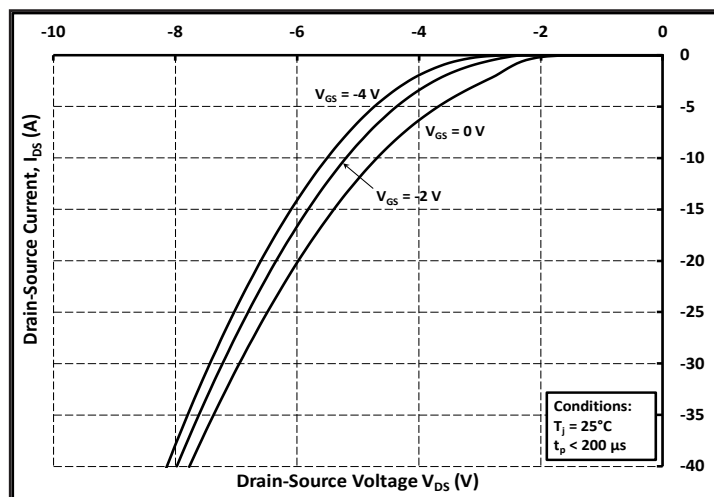


Figure 9. Body Diode Characteristic at $25\text{ }^{\circ}\text{C}$

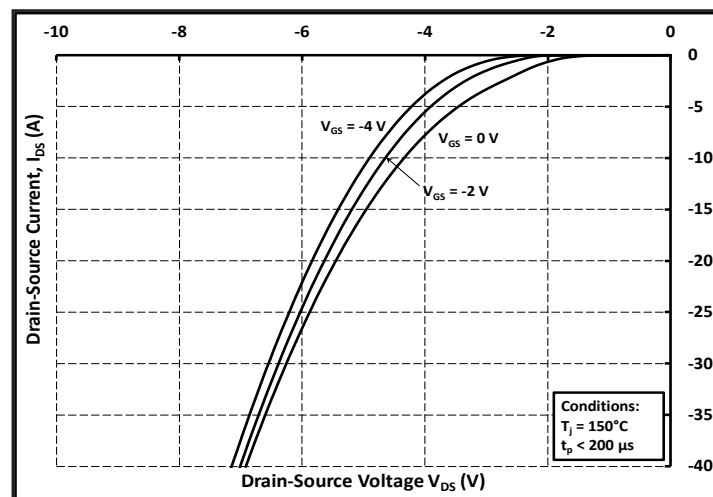


Figure 10. Body Diode Characteristic at $150\text{ }^{\circ}\text{C}$

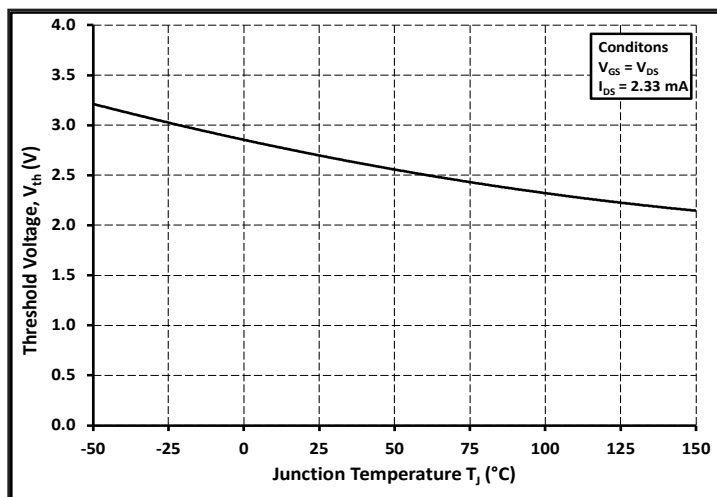


Figure 11. Threshold Voltage vs. Temperature

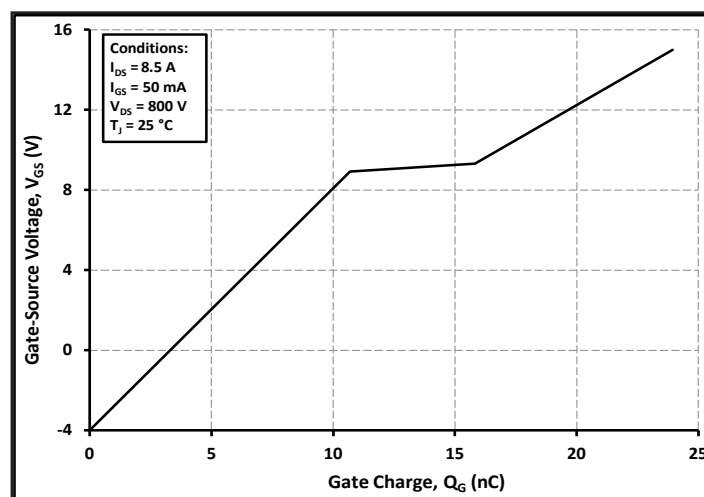


Figure 12. Gate Charge Characteristics

Typical Performance

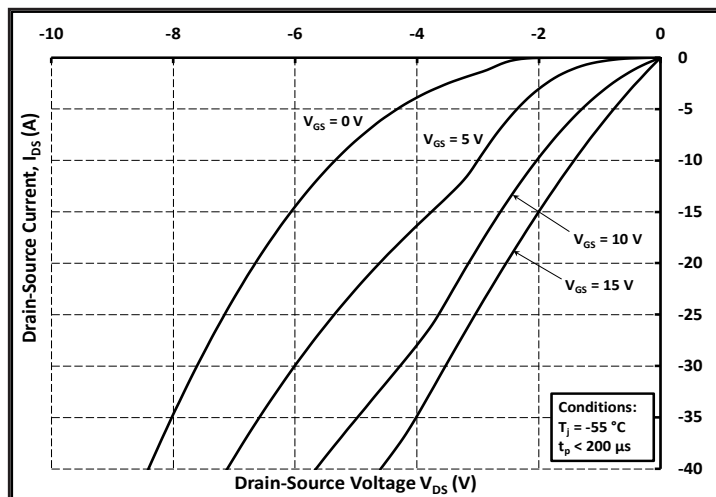


Figure 13. 3rd Quadrant Characteristic at -55°C

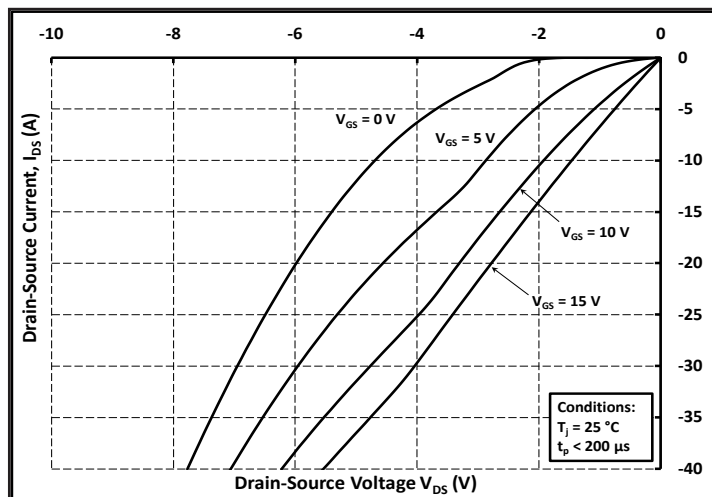


Figure 14. 3rd Quadrant Characteristic at 25°C

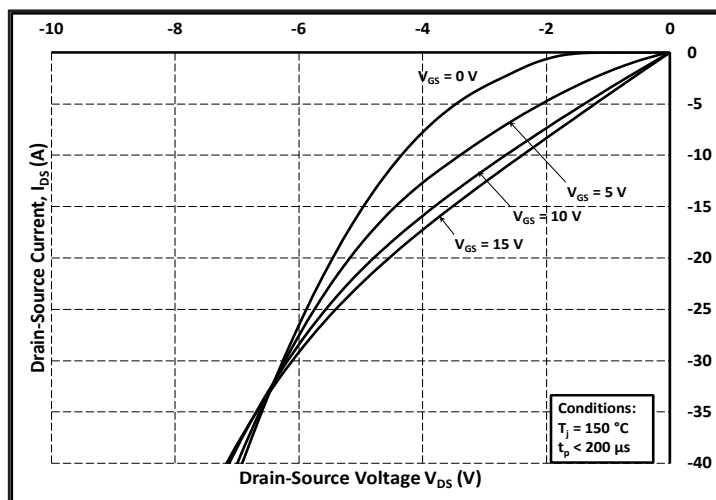


Figure 15. 3rd Quadrant Characteristic at 150°C

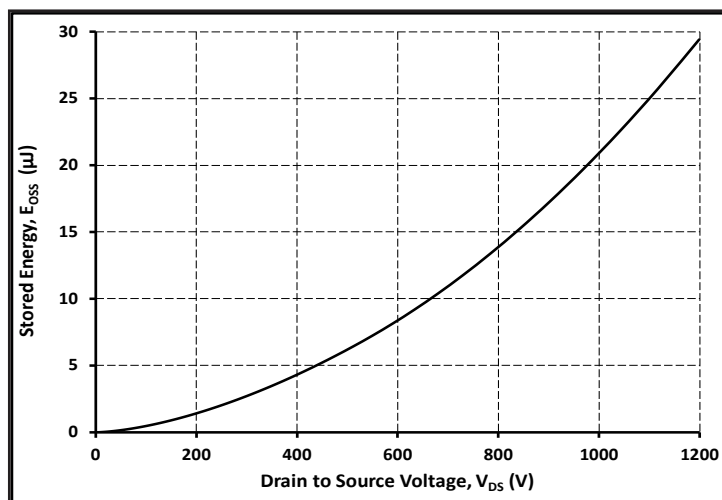


Figure 16. Output Capacitor Stored Energy

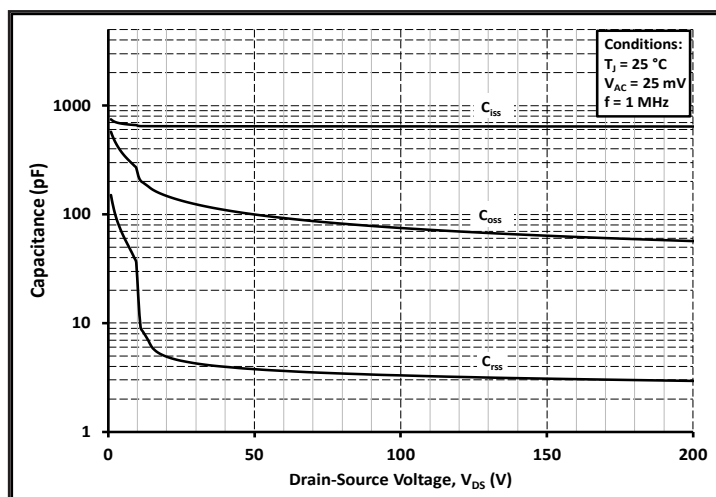


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

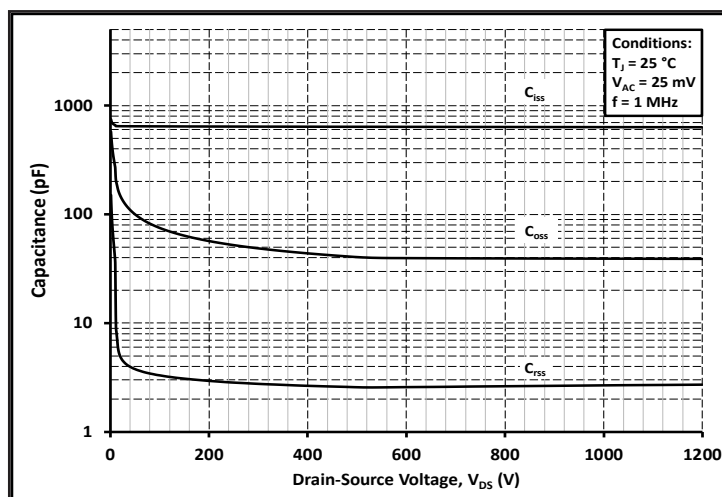


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1200V)

Typical Performance

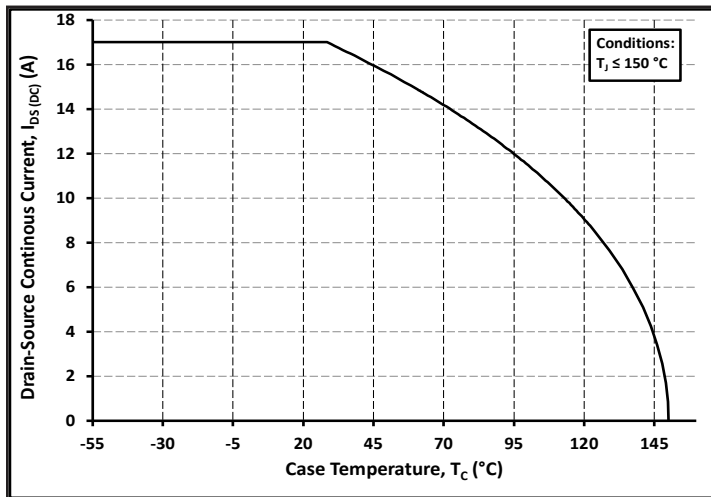


Figure 19. Continuous Drain Current Derating vs. Case Temperature

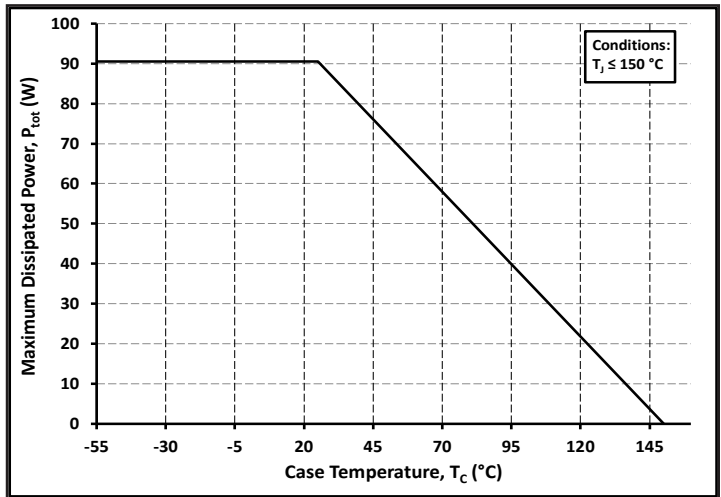


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

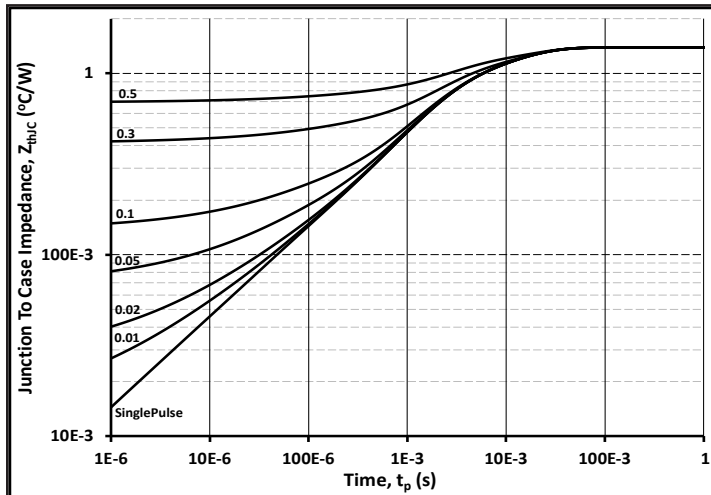


Figure 21. Transient Thermal Impedance (Junction - Case)

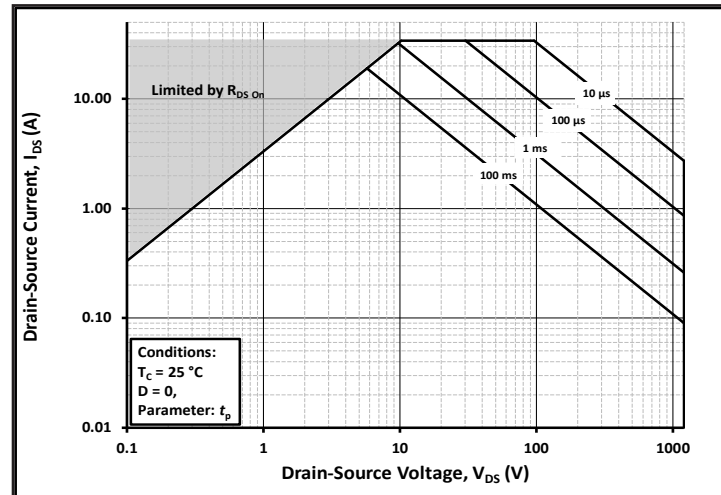


Figure 22. Safe Operating Area

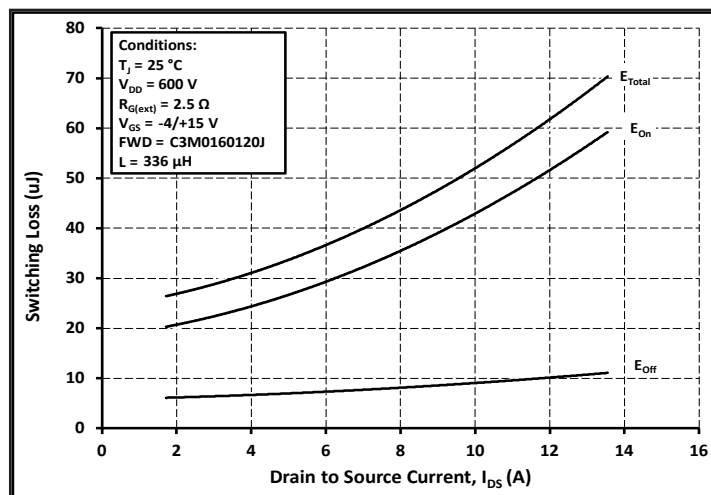


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600V$)

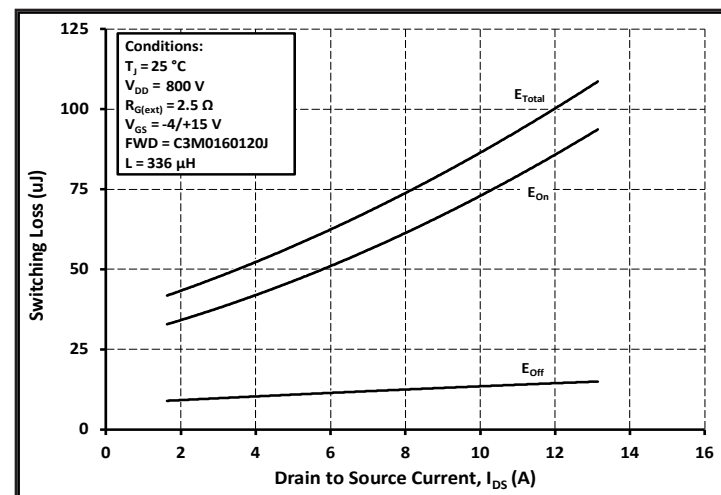


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 800V$)

Typical Performance

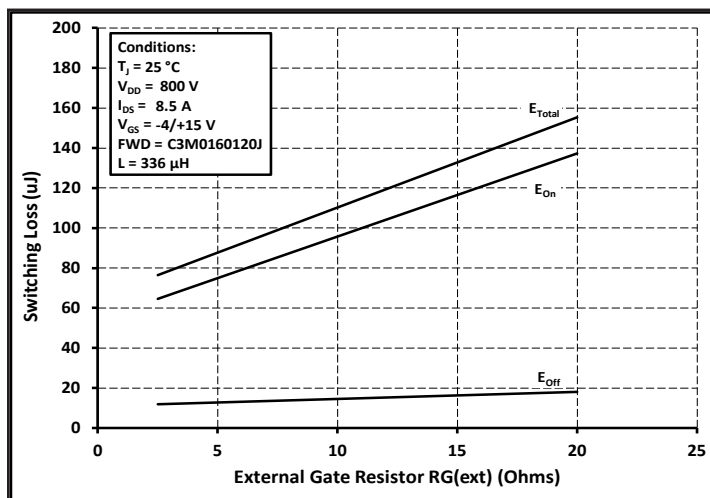


Figure 25. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

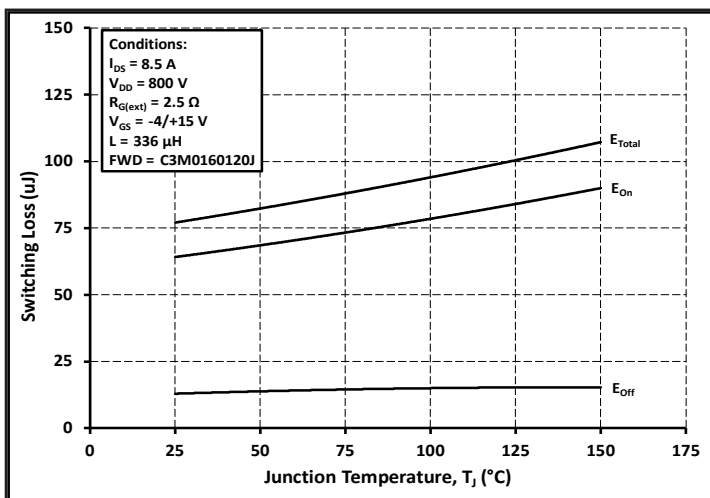


Figure 26. Clamped Inductive Switching Energy vs. Temperature

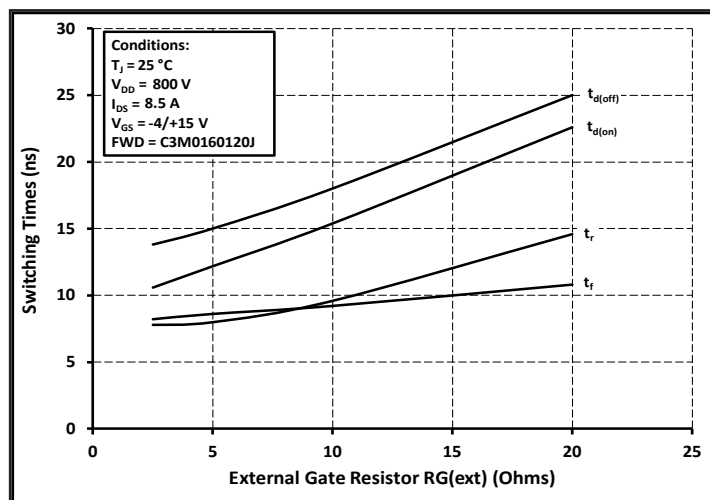


Figure 27. Switching Times vs. $R_{G(ext)}$

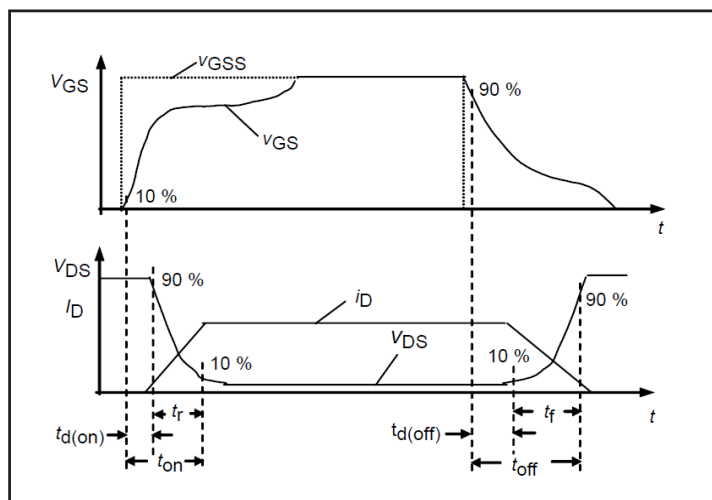


Figure 28. Switching Times Definition

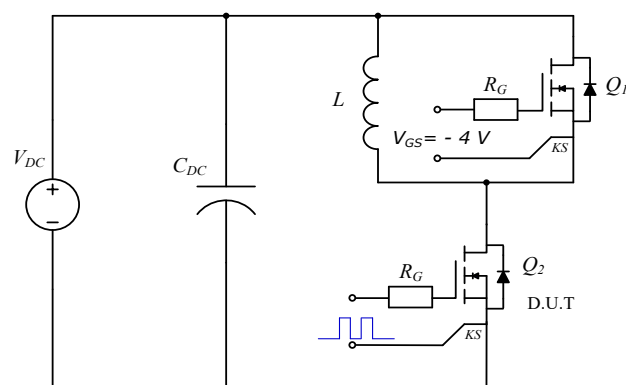
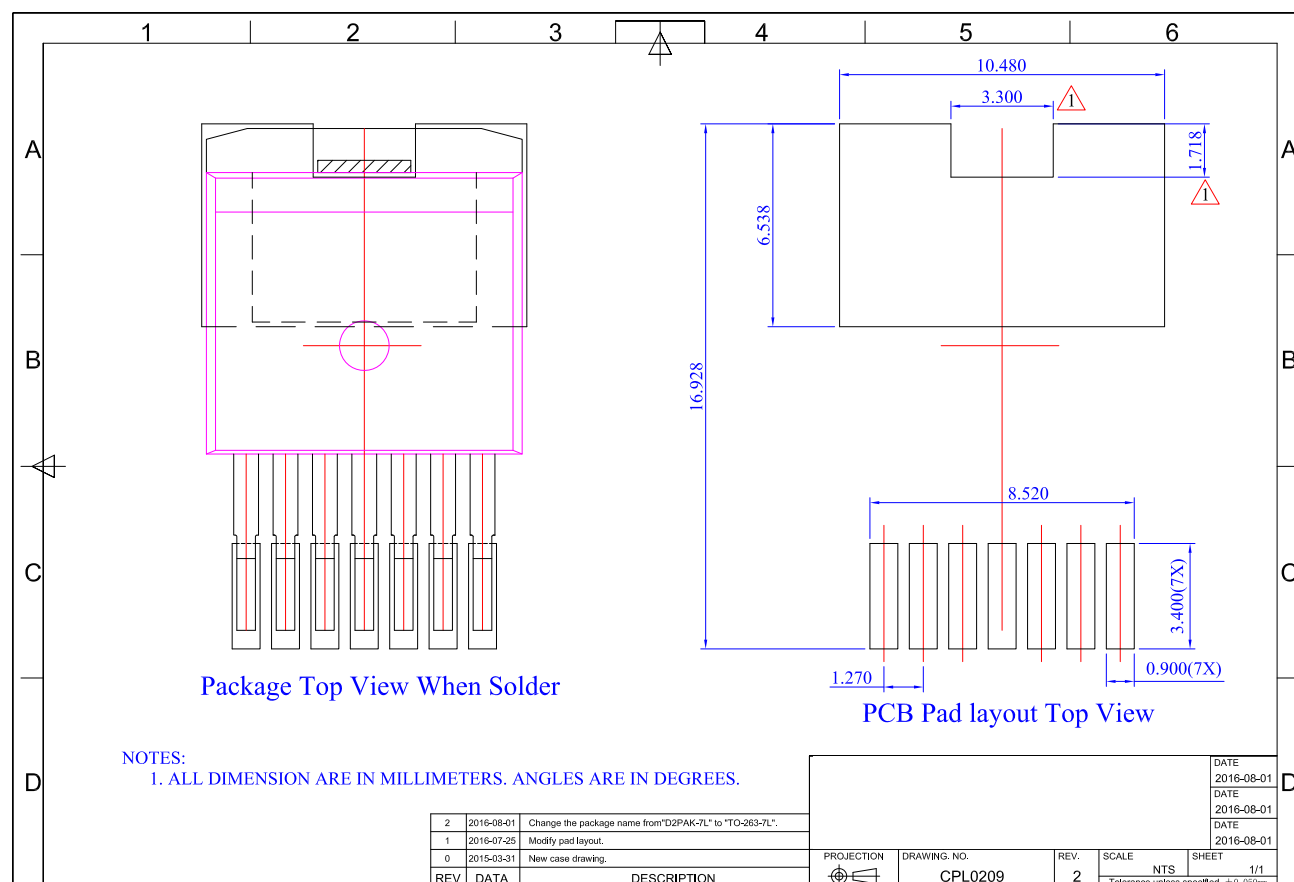
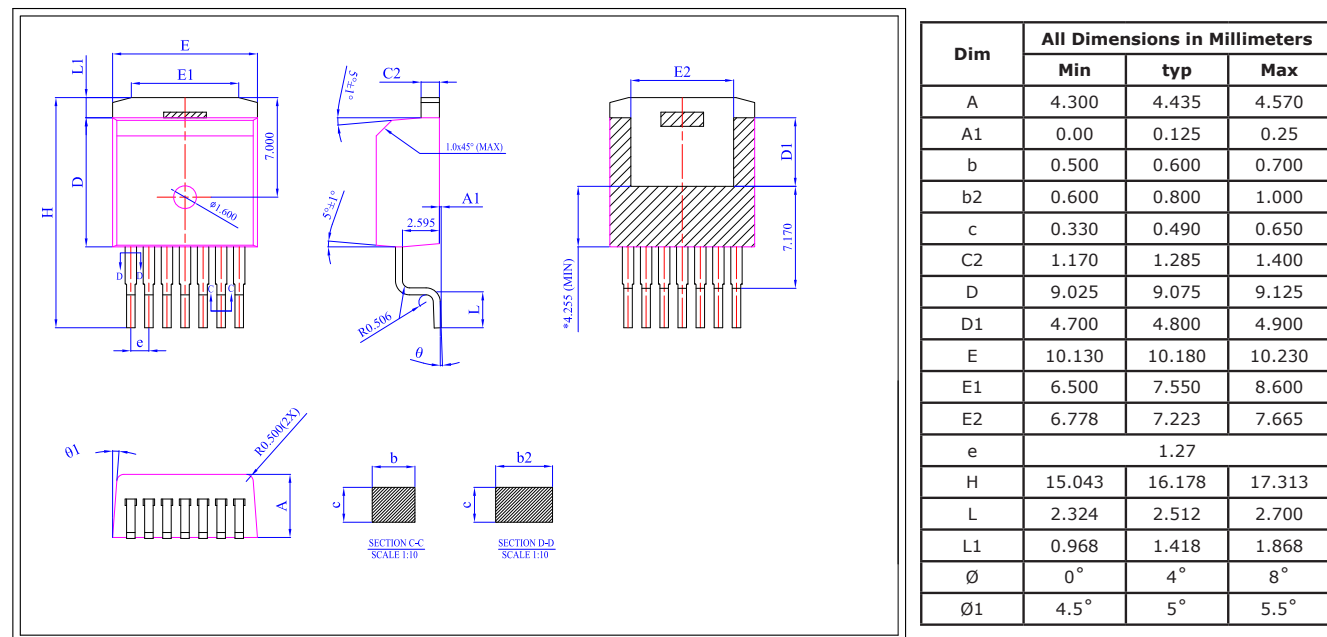


Figure 29. Clamped Inductive Switching
Waveform Test Circuit

Note (3): Turn-off and Turn-on switching energy and timing values measured using SiC MOSFET Body Diode as shown above.

Package Dimensions

Package 7L D2PAK



Notes

- **RoHS Compliance**
The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of www.cree.com.
- **REACH Compliance**
REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.
- This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, air traffic control systems.

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- **SPICE Models:** <http://wolfspeed.com/power/tools-and-support>
- **SiC MOSFET Isolated Gate Driver reference design:** <http://wolfspeed.com/power/tools-and-support>
- **SiC MOSFET Evaluation Board:** <http://wolfspeed.com/power/tools-and-support>

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