

ISP1510

Smart Ultra Wide Band and Bluetooth Low Energy Module with MCU and Antennas

This highly miniaturized LGA module, 9 x 16 x 1.9 mm, is based on the DW1000 UWB transceiver and nRF52832 BLE chip. Using a simple user interface via the SPI connection and integrating a Cortex™ M4 CPU, flash and RAM memory combined with optimized antennas, ISP1510 offers the perfect stand-alone ranging module solution for security bubble applications. For longer range applications, ISP1510 can be used in conjunction with an external UWB antenna. Alternatively, it can also be connected to an external MCU.



Key Features

- IEEE802.15.4-2011 UWB compliant
- Single Mode BLE 5 Ready
- NFC-A Tag for OOB pairing
- Spatial resolution better than 10 cm
- Fully integrated UWB & BLE matching and Antennas
- Integrated UWB 38.4 MHz and BLE 32 MHz & 32.768 kHz Clocks
- DC/DC converters
- UWB section based on DecaWave DW1000
- BLE section based on Nordic Semi nRF52
- Externally Controlled or using embedded 32-bit ARM Cortex M4 CPU
- 512 kB Flash and 64 kB SRAM
- Analog and Digital peripherals
- SPI interface
- Supply Voltage 2.8V to 3.6V
- Very small size 9.0 x 16.0 x 1.9 mm
- Temperature -40 to +85 °C
- Ultra-small size 9.0 x 16.0 x 1.9 mm



Applications

- Precision Real Time Location Systems (RTLS) for Healthcare, Sport and Wellness, Consumer, Industrial ...
- Security bubble
- Access control
- Indoor positioning

Pending Certifications

- Fully FCC certified module
- Fully CE certified module
- Fully IC certified module
- Bluetooth SIG certified QDL listing
- RoHS compliant

Preliminary Data Sheet

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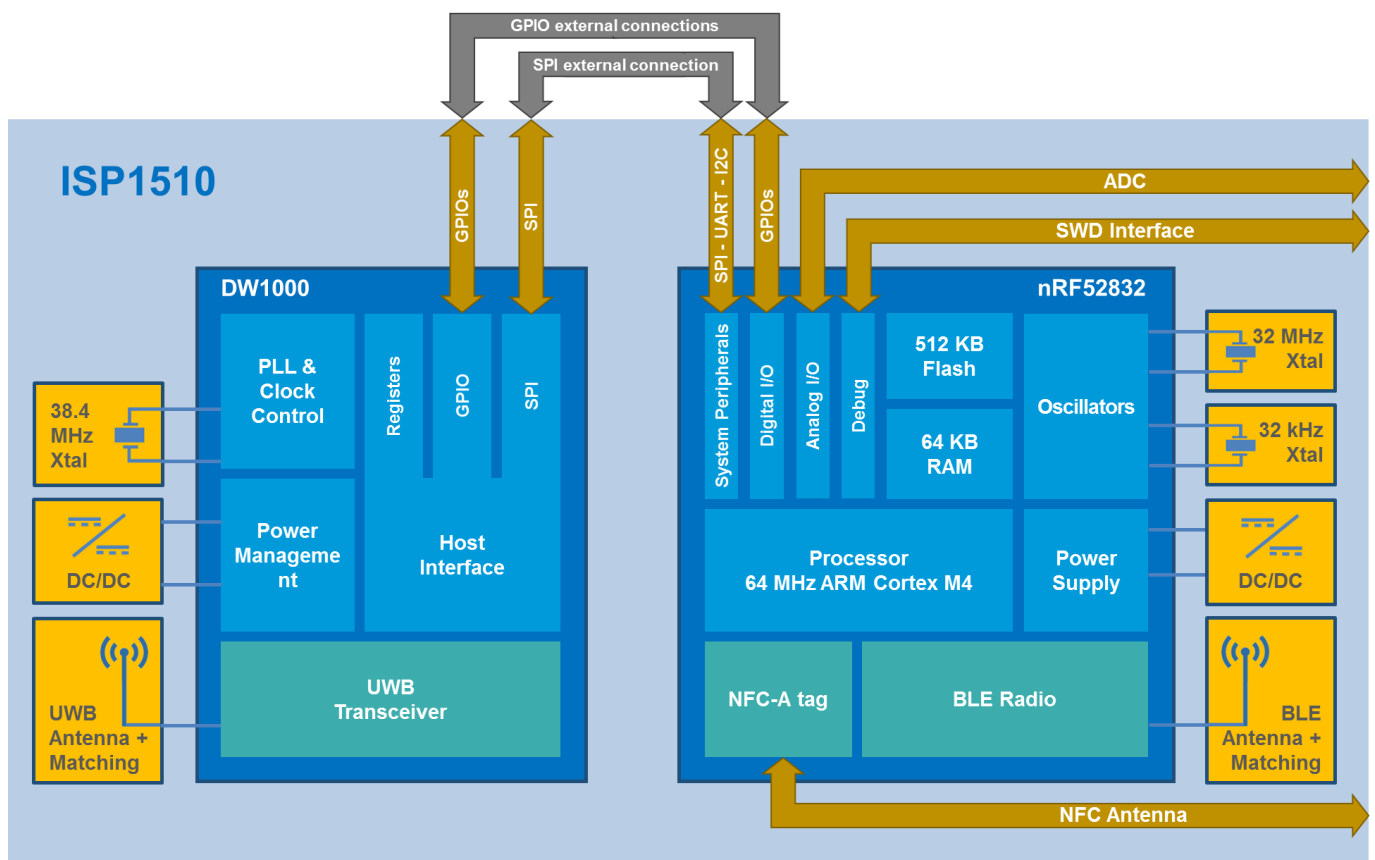
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1. Block Diagram

This module is based on DecaWave DW1000 single-chip UWB transceiver and nRF52832 Nordic Semiconductor 2.4GHz wireless System on Chip (SoC). It integrates a 32-bit ARM Cortex™ M4 CPU, 512 kB flash memory, 64 kB RAM as well as analog and digital peripherals. Despite the small size of 9.0 x 16.0 x 1.9 mm, the module integrates decoupling capacitors, 38.4 MHz crystal for UWB, 32 MHz and 32.768 kHz crystals for BLE, DC-DC converters, RF matching circuits and two antennas in addition to the wireless SoCs. Low power consumption and advanced power management enables battery lifetimes up to several months on a coin cell battery.

UWB communication is compliant to IEEE802.15.4. Using a simple user interface controlled over the air by Bluetooth or by SPI, ISP1510 offers the perfect stand-alone ranging module solution for security bubble or maximum 15 meters range applications. For longer range applications with typically 50 meters distance, ISP1510 can be used in conjunction with an external UWB antenna. Alternatively, it can be connected to an external microprocessor for more complex positioning algorithms, in which case all the processing is carried out externally to the module.

BLE connectivity compliant to Bluetooth V4.2. ISP1510 BLE section can be used either in Peripheral or Central roles for BLE. Fully qualified BLE stacks for nRF52832 are implemented in the S132 SoftDevice which can be freely downloaded.



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2. Specifications

The specifications of the module follow those of the nRF52832 and DW1000. The following high level parameters are given for the module.

2.1. Absolute Maximum Ratings

| Parameter | Min | Typ | Max | Unit |
|---|------|-----|-------|--------|
| Supply Voltage VCC_BLE | -0.3 | | 3.9 | V |
| Supply Voltage VDD_UWB, VDDAON, VDDIO | -0.3 | | 4.0 | V |
| IO Pins P0.xx, GPIOx, SPICLK, SPIMISO, SPIMOSI, SPICS, SRESET, WAKEUP | -0.3 | | 3.6 | V |
| UWB RF Input Level | | | 0 | dBm |
| BLE RF Input Level | | | 10 | dBm |
| NFC Antenna pin current | | | 80 | mA |
| Storage Temperature | -40 | | +85 | °C |
| Moisture Sensitivity Level | | | 5 | - |
| Flash Endurance | | | 10000 | cycles |



ATTENTION

CONSERVE PRECAUTION FOR HANDLING
ELECTROSTATIC SENSITIVE DEVICES
Human Body Model Class 3A

2.2. Operating Conditions

| Parameter | Min | Typ | Max | Unit |
|--|------|-----|---------|------|
| Operating Supply Voltage VCC_BLE, VDD_UWB, VDDAON | 2.8 | 3.0 | 3.6 | V |
| Operating Supply Voltage VDDIO (used during programming of calibration settings in DW1000 OTP) | 3.7 | 3.8 | 3.9 | V |
| Operating Input Voltage IO Pins P0.xx | -0.3 | | VCC_BLE | V |
| Operating Input Voltage IO Pins GPIOx, SPICLK, SPIMISO, SPIMOSI, SPICS, SRESET, WAKEUP | -0.3 | | VDD_UWB | V |
| Operating Temperature Range | -30 | +25 | +85 | °C |

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2.3. Current Consumption

| Parameter | Min | Typ | Max | Unit |
|---|-----|-----|-----|------|
| UWB receiver | | | 180 | mA |
| UWB transmitter | | | 140 | mA |
| UWB idle mode | | 18 | | mA |
| UWB sleep mode | | 1 | | μA |
| UWB deep sleep mode | | 100 | | nA |
| BLE Peak current, Receiver active ⁽¹⁾ | | 6.1 | | mA |
| BLE Peak current, Transmitter active +4 dBm Output Power ⁽²⁾ | | 7.9 | | mA |
| BLE Peak current, Transmitter active 0 dBm Output Power ⁽²⁾ | | 5.4 | | mA |
| CPU System OFF current, no RAM retention | | 0.7 | | μA |
| CPU System ON base current, full RAM retention | | 1.5 | | μA |
| CPU Additional RAM retention current per 4 KB block | | 40 | | nA |

(1) DC-DC enabled, Power supply 3V, 1 Msps

(2) DC-DC enabled, Power supply 3V

2.4. Clock Sources

| Parameter | Min | Typ | Max | Unit |
|--|-----|-----|-------|------|
| Internal High Frequency Clock for RF Stability: 38.4 MHz Crystal Frequency Tolerance ⁽¹⁾ | | | ± 30 | ppm |
| Internal High Frequency Clock for RF Stability: 32 MHz Crystal Frequency Tolerance ⁽¹⁾ | | | ± 40 | ppm |
| Internal Low Frequency Clock for BLE Synchronization: 32.768 kHz Crystal Frequency Tolerance ⁽¹⁾ | | | ± 40 | ppm |
| Internal Low Frequency Clock for BLE Synchronization: RC Oscillator ⁽²⁾ | | | ± 250 | ppm |
| RF Frequency tolerance: For UWB operation Channel 5 | | | ± 30 | ppm |
| RF Frequency tolerance: For BLE operation Channels 0 to 39 | | | ± 40 | ppm |

(1) Including initial tolerance, drift, aging, and frequency pulling

(2) Frequency tolerance after calibration

Preliminary Data Sheet

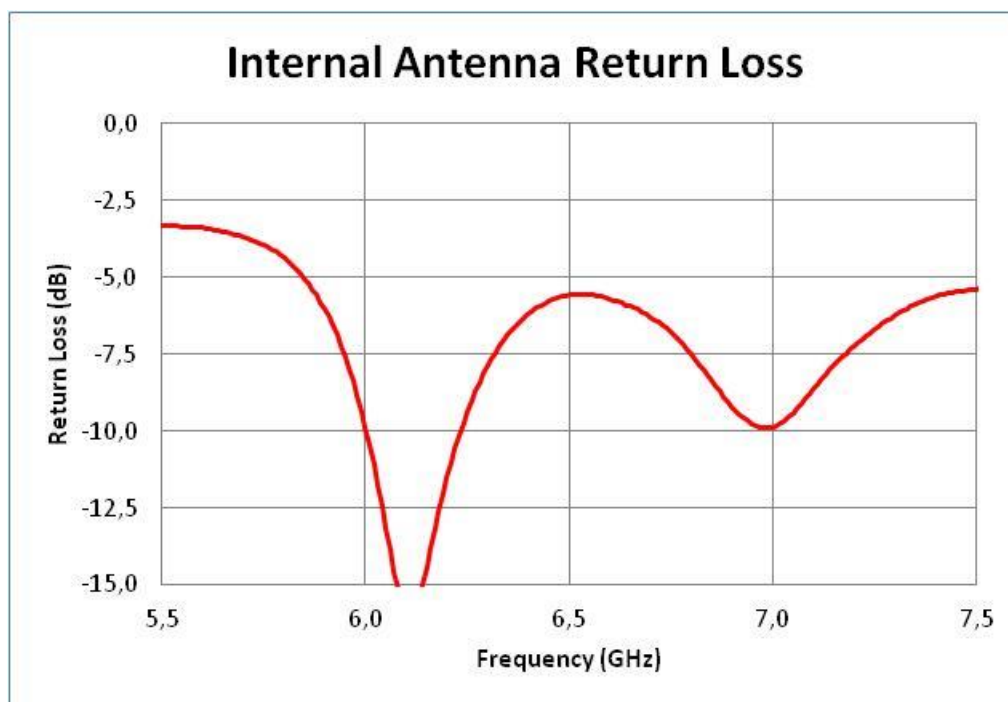
2.5. Radio Specifications

| Parameter | Min | Typ | Max | Unit |
|---|-------|--------|------|----------|
| UWB channel 5 center frequency (1) (2) | | 6489.6 | | MHz |
| UWB channel 3 center frequency (2) | | 4492.8 | | MHz |
| UWB channel bandwidth | | 499.2 | | MHz |
| UWB Input at 1dB compression point | | -39 | | dBm |
| UWB Rx sensitivity Level for BER <1% (3) | | -93 | | dBm |
| UWB Output Power spectral density | | -39 | -35 | dBm/MHz |
| UWB Antenna Gain channel 5 (1) (4) | | -2 | | dBi |
| UWB Load impedance | | 50 | | Ω |
| UWB Range Open field @ 1 m height (1) (3) (4) | | 15 | | m |
| UWB Range Open field @ 1 m height (2) (3) | | 50 | | m |
| BLE Frequency Range | 2402 | | 2480 | MHz |
| BLE Channel 0 to 39 Spacing | | 2 | | MHz |
| BLE Output Power Channels 0 to 39 | -20 | | +4 | dBm |
| BLE Rx sensitivity Level for BER <0,1% ideal Tx | -96 | | | dBm |
| BLE Antenna Gain | | 0.6 | | dBi |
| BLE EIRP | -19.4 | | 4.6 | dBm |
| BLE Range Open field @1m height (5) | | 70 | | m |

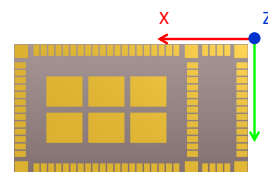
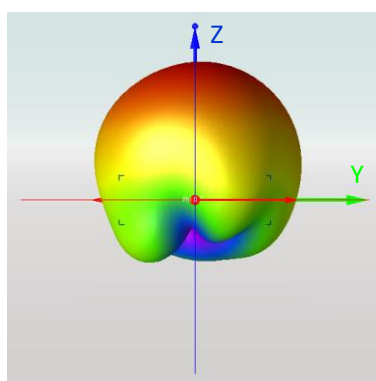
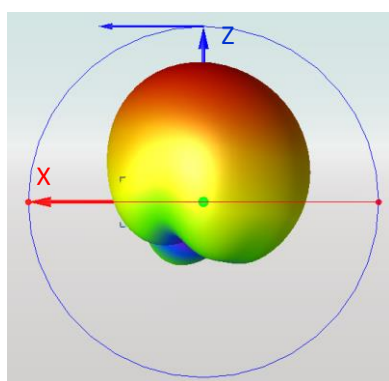
- (1) With internal UWB antenna / Channel 5 only
- (2) With external UWB antenna
- (3) 6.8 Mbps / Preamble 256
- (4) Measured Front side of ISP1510
- (5) Data Rate 1000 / 2000 Kbps

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Typical UWB Antenna Return Loss



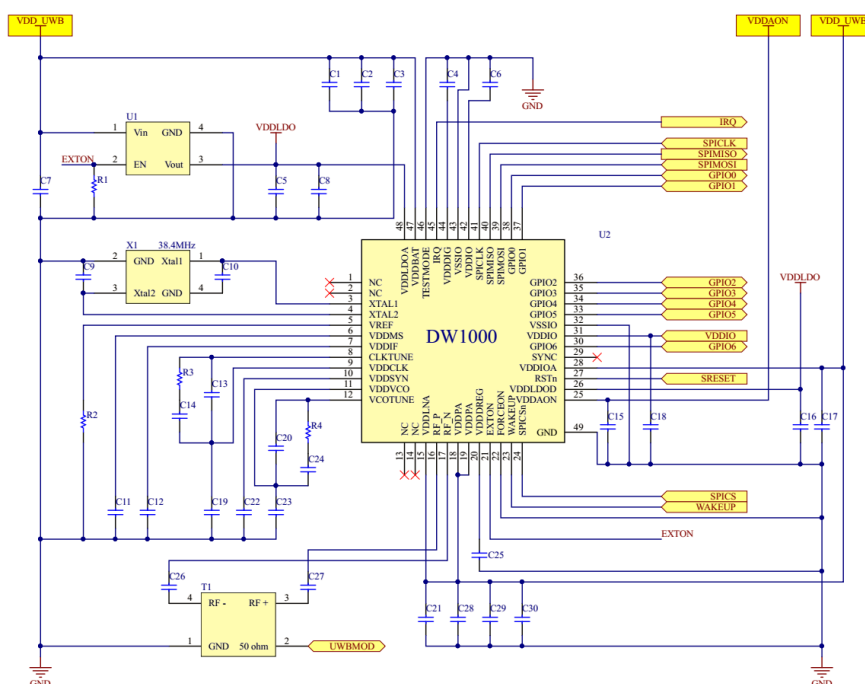
Radiation Patterns



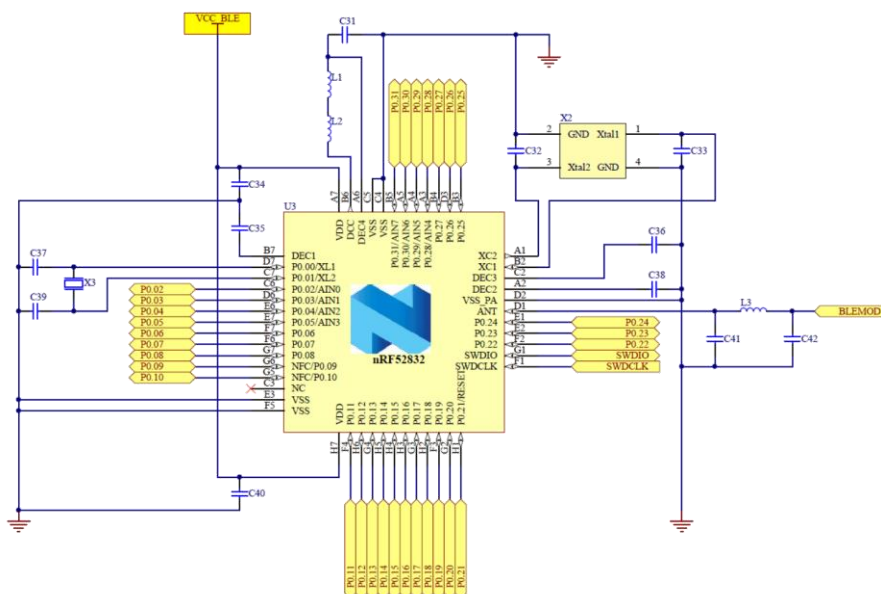
Preliminary Data Sheet

2.6. Electrical Schematic

Details of DW1000 connections



Details of nRF52832 connections



Preliminary Data Sheet

3. Pin Description

The module uses an LGA format on a 0.50 mm pitch. The pad layout follows the QFN Jedec standard for LGA parts. The NC pads are to be connected to isolated metal pads on the application PCB for mechanical stability and reliability (drop test).

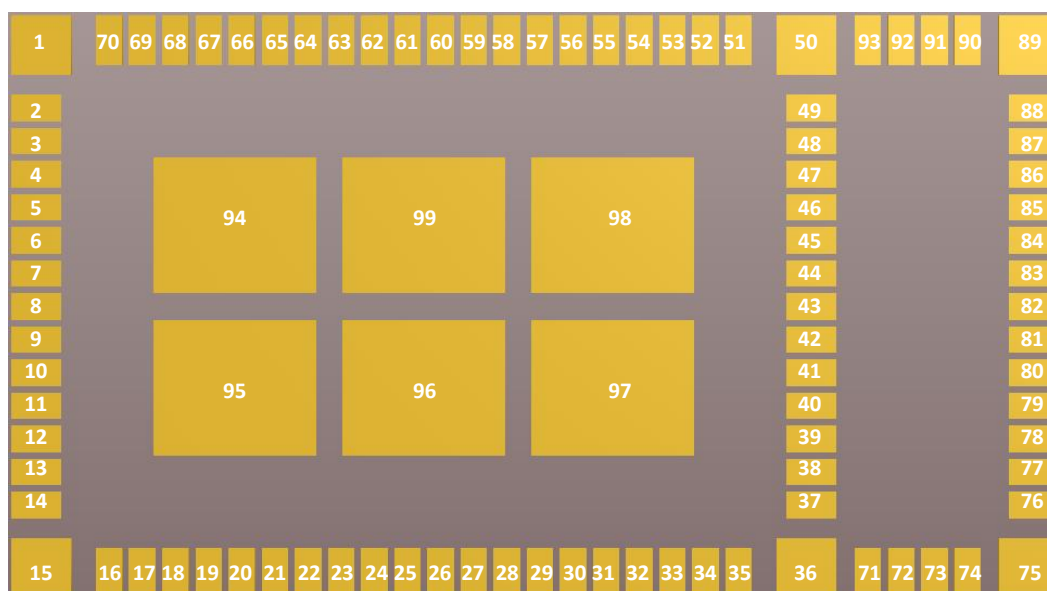
| Pin | Name | Pin function | Description |
|-----|---------------------------|--------------------------|---|
| 1 | GND | Ground | Power Ground – Must be connected to ground on application PCB |
| 2 | P0.20 TRACECLK | Digital I/O | nRF52 general purpose I/O pin Trace port clock output |
| 3 | P0.18 TRACEDATA0 | Digital I/O | nRF52 general purpose I/O pin Trace port output |
| 4 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 5 | UWBMOD | RF I/O | UWB RF I/O pin of the module Should be connected to Pin 6 UWBANT for normal operation |
| 6 | UWBANT | RF I/O | Internal UWB antenna RF I/O pin It should be connected to Pin 5 UWBMOD for normal operation |
| 7 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 8 | P0.14 TRACEDATA3 | Digital I/O | nRF52 general purpose I/O pin Trace port output |
| 9 | P0.16 TRACEDATA1 | Digital I/O | nRF52 general purpose I/O pin Trace port output |
| 10 | P0.10 NFC2 | Digital I/O RF I/O | nRF52 general purpose I/O pin NFC antenna connection pin |
| 11 | P0.15 TRACEDATA2 | Digital I/O | nRF52 general purpose I/O pin Trace port output |
| 12 | P0.09 NFC1 | Digital I/O NFC Input | nRF52 general purpose I/O pin NFC antenna connection pin |
| 13 | P0.06 | Digital I/O | nRF52 general purpose I/O pin |
| 14 | P0.07 | Digital I/O | nRF52 general purpose I/O pin |
| 15 | GND | Ground | Power Ground – Must be connected to ground on application PCB |
| 16 | VDDIO | Power | External supply for DW1000 OTP Internal supply for DW1000 SPI Mode Configuration |
| 17 | VDDAON | Power | External supply for the Always-ON portion of the DW1000 chip If use of DW1000 Deep Sleep mode, should be connected to VCC |
| 18 | GPIO0 RXOKLED | Digital I/O | DW1000 general purpose I/O pin May be configured to drive a LED on reception of a good frame |
| 19 | GPIO1 SFDLED | Digital I/O | DW1000 general purpose I/O pin May be configured to drive a LED on detection of a Start Frame Delimiter |
| 20 | GPIO2 RXLED | Digital I/O | DW1000 general purpose I/O pin May be configured to drive a LED during receive mode |
| 21 | GPIO6 SPIPOL EXTRXE | Digital I/O | On power-up selects DW1000 SPI polarity for SPI operation mode After power-up DW1000 general purpose I/O pin May be configured to go high on receive mode |
| 22 | GPIO3 TXLED | Digital I/O | DW1000 general purpose I/O pin May be configured to drive a LED after transmission |
| 23 | GPIO5 SPIPHA EXTTXE | Digital I/O | On power-up selects DW1000 SPI phase for SPI operation mode After power-up DW1000 general purpose I/O pin May be configured to go high on transmit mode |

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| Pin | Name | Pin function | Description |
|-----|-----------------|-----------------------------|---|
| 24 | GPIO4 EXTPA | Digital I/O | DW1000 general purpose I/O pin May be configured to enable an External Power Amplifier |
| 25 | P0.05 AIN3 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 26 | P0.04 AIN2 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 27 | P0.03 AIN1 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 28 | P0.02 AIN0 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 29 | P0.31 AIN7 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 30 | P0.27 | Digital I/O | nRF52 general purpose I/O pin |
| 31 | P0.30 AIN6 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 32 | P0.29 AIN5 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 33 | P0.28 AIN4 | Digital I/O Analog Input | nRF52 general purpose I/O pin SAADC / COMP / LPCOMP input |
| 34 | P0.25 | Digital I/O | nRF52 general purpose I/O pin |
| 35 | P0.26 | Digital I/O | nRF52 general purpose I/O pin |
| 36 | VCC_BLE | Power | External supply for NRF52 |
| 37 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 38 | BLEMOD | RF I/O | BLE RF I/O pin of the module Should be connected to Pin 39 BLEANT for normal operation |
| 39 | BLEANT | RF I/O | Internal BLE antenna RF I/O pin Should be connected to Pin 38 BLEMOD for normal operation |
| 40 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 41 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 42 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 43 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 44 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 45 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 46 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 47 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 48 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 49 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 50 | GND | Ground | Analog Ground – Must be connected to ground on application PCB |
| 51 | VDD_UWB | Power | External supply for DW1000 |
| 52 | SWDCLK | Digital Input | nRF52 Serial Wire Debug clock input for debug and programming |
| 53 | SWDIO | Digital I/O | nRF52 Serial Wire Debug I/O for debug and programming |
| 54 | P0.21 MRESET | Digital I/O | nRF52 general purpose I/O pin May be configured as nRF52 RESET pin |
| 55 | P0.22 | Digital I/O | nRF52 general purpose I/O pin |
| 56 | GPIO8 IRQ | Digital I/O | DW1000 IRQ output (default: active high, to be pulled down) Should be connected to nRF52 general purpose I/O pin If IRQ is not required, DW1000 general purpose I/O pin |
| 57 | P0.12 | Digital I/O | nRF52 general purpose I/O pin |

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| Pin | Name | Pin function | Description |
|----------|---------|---------------|---|
| 58 | P0.08 | Digital I/O | nRF52 general purpose I/O pin |
| 59 | SPICLK | Digital I/O | DW1000 SPI clock Should be connected to nRF52 general purpose I/O pin |
| 60 | P0.24 | Digital I/O | nRF52 general purpose I/O pin |
| 61 | SPIMISO | Digital I/O | DW1000 SPI data output Should be connected to nRF52 general purpose I/O pin |
| 62 | P0.17 | Digital I/O | nRF52 general purpose I/O pin |
| 63 | SPIMOSI | Digital I/O | DW1000 SPI data output Should be connected to nRF52 general purpose I/O pin |
| 64 | P0.19 | Digital I/O | nRF52 general purpose I/O pin |
| 65 | SRESET | Digital I/O | DW1000 active-low RESET pin May be pulled low by external open-drain driver Must not be pulled high |
| 66 | P0.23 | Digital I/O | nRF52 general purpose I/O pin |
| 67 | SPICS | Digital I/O | DW1000 SPI Chip Select, active low Should be connected to nRF52 general purpose I/O pin The high-to-low transition begins a new SPI transaction May also act as a wake-up signal to bring DW1000 out of sleep or deep-sleep states |
| 68 | P0.13 | Digital I/O | nRF52 general purpose I/O pin |
| 69 | WAKEUP | Digital Input | Active high, brings the DW1000 out of sleep or deepsleep states into operational mode |
| 70 | P0.11 | Digital I/O | nRF52 general purpose I/O pin |
| 71 .. 93 | NC | Not Connected | Isolated pad on application PCB for mechanical stability |
| 94 .. 99 | GND | Ground | Power Ground – Must be connected to ground on application PCB |



ISP1510 pad placement and pin assignment for the LGA QFN package
TOP VIEW

Preliminary Data Sheet

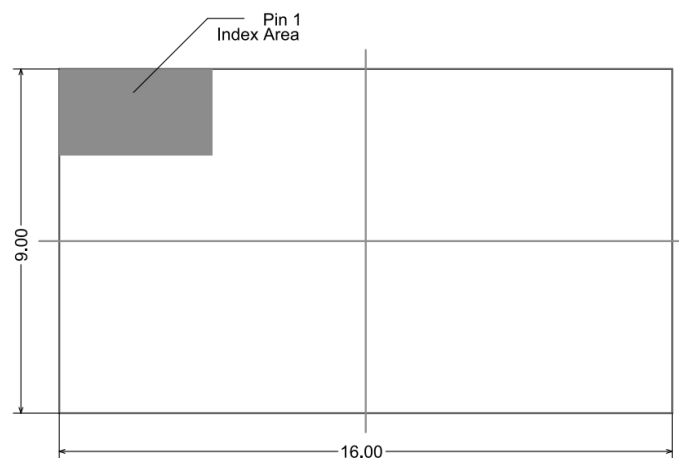
4. Mechanical Outlines

4.1. Mechanical Dimensions

Dimensional drawing for 9.00 x 16.00 x 1.86 mm, 99-Pad LGA Package

UNIT: mm

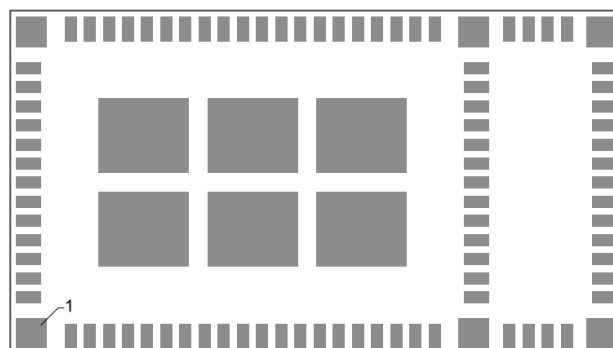
TOLERANCE: ± 0.1 mm



Top view

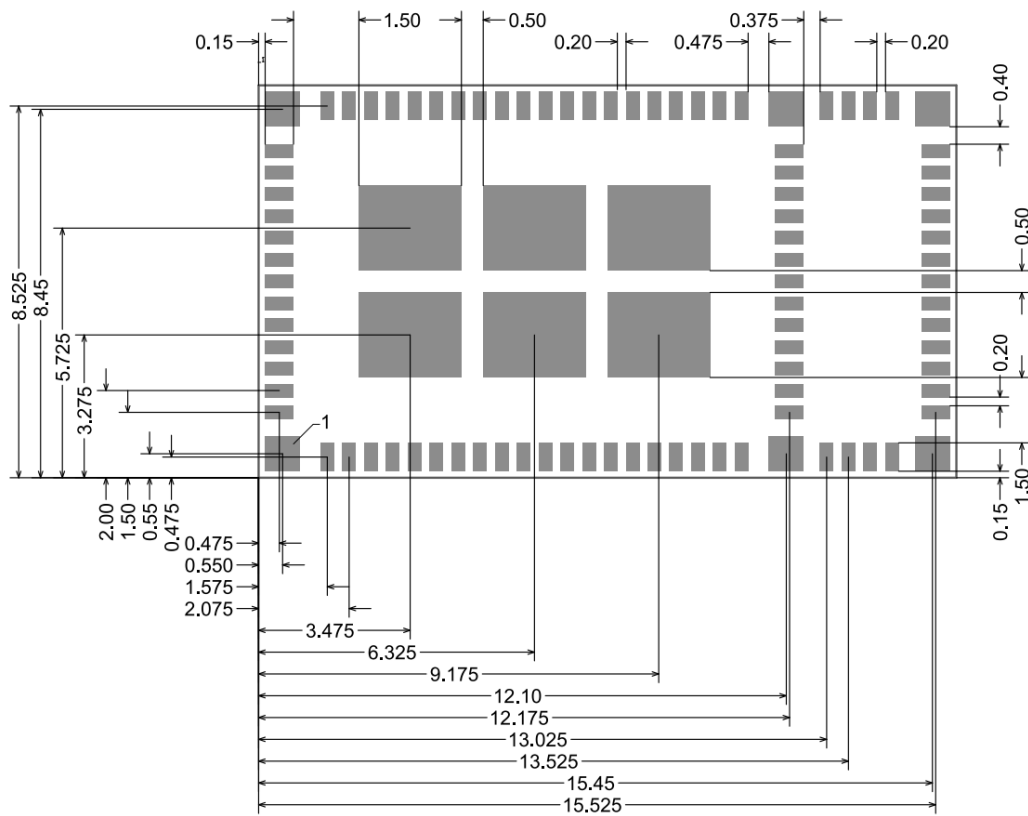


Side view

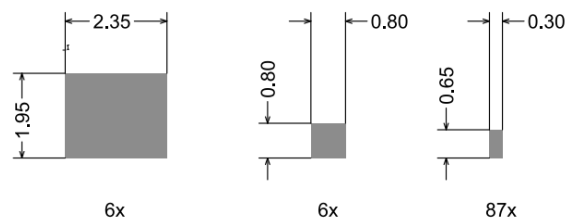


Bottom view

Preliminary Data Sheet



Bottom view



Preliminary Data Sheet

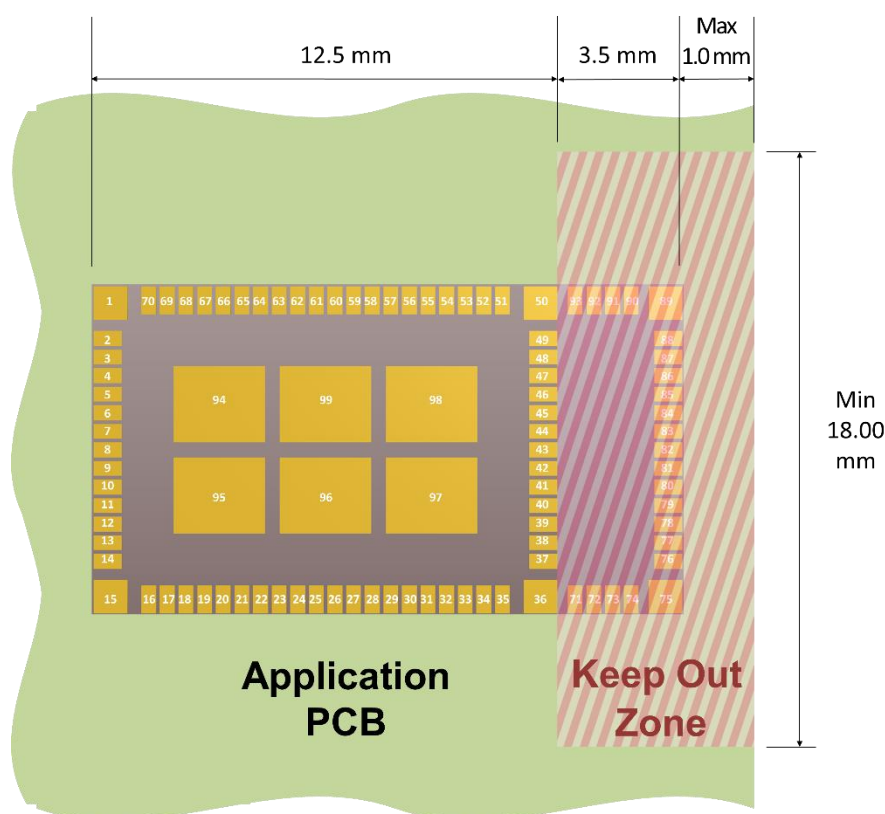
4.2. SMT Assembly Guidelines

For PCB Land Patterns and Solder Mask layout, Insight SiP recommends the use of the same dimensions as the module pads, i.e. 0.65 x 0.30 mm for standard pads, 0.80 x 0.80 mm for corner pads, 2.35 x 1.95 mm for mechanical pads.

Please contact Insight SiP for more detailed information.

4.3. Antenna Keep-Out Zone

For optimal antenna performance, it is recommended to respect a metal exclusion zone to the edge of the board: no metal, no traces and no components on any application PCB layer except mechanical LGA pads.



Preliminary Data Sheet

5. Product Development Tools

5.1. Hardware

In order to assist clients in developing their Bluetooth Smart solutions based on the ISP1510, Insight SiP offers a Development Kit containing:

- 1x Interface Board with integrated J-Link OB JTAG/SWD Emulator
- 1x Test Board
- 1x Development Dongle
- A ranging demonstration including 1x anchor board, 1x tag board, the embedded firmware and Android App
- Cables, Coin cell battery

Using this development kit, product developers can use a working solution as starting point to develop their own products. Time to market is saved by avoiding starting from a blank sheet of paper. In addition, some applications may employ the hardware as is.

5.2. Firmware

Bluetooth Low Energy protocol stacks are downloadable from www.nordicsemi.com. The S132 SoftDevice is a Bluetooth® low energy (BLE) Central and Peripheral protocol stack solution supporting up to three Central and one Peripheral simultaneous connections and concurrent Observer and Broadcaster roles. It integrates a low energy Controller and Host, and provides a full and flexible API for building Bluetooth low energy System on Chip (SoC) solutions.







The decaWave programming interface is downloadable from www.decawave.com/support/software. This package contains the following folder:

- ✚ Decadrivers: Source code of decaWave driver in C programming language.
- ✚ Examples: A set of example codes demonstrating decaWave features (transmission, reception, sleep mode, ranging, etc).

Although the driver is provided as source code to allow it to be ported to any target microprocessor, porting it to nRF52 requires the implementation of low-level functions such as SPI, sleep, interruptions. To help getting started Insight SiP provides within its development kit the source code of the BLE+UWB ranging demo embedded firmware.

5.3. Development Tools

The following development tools and software are recommended for using and testing ISP1510 module:

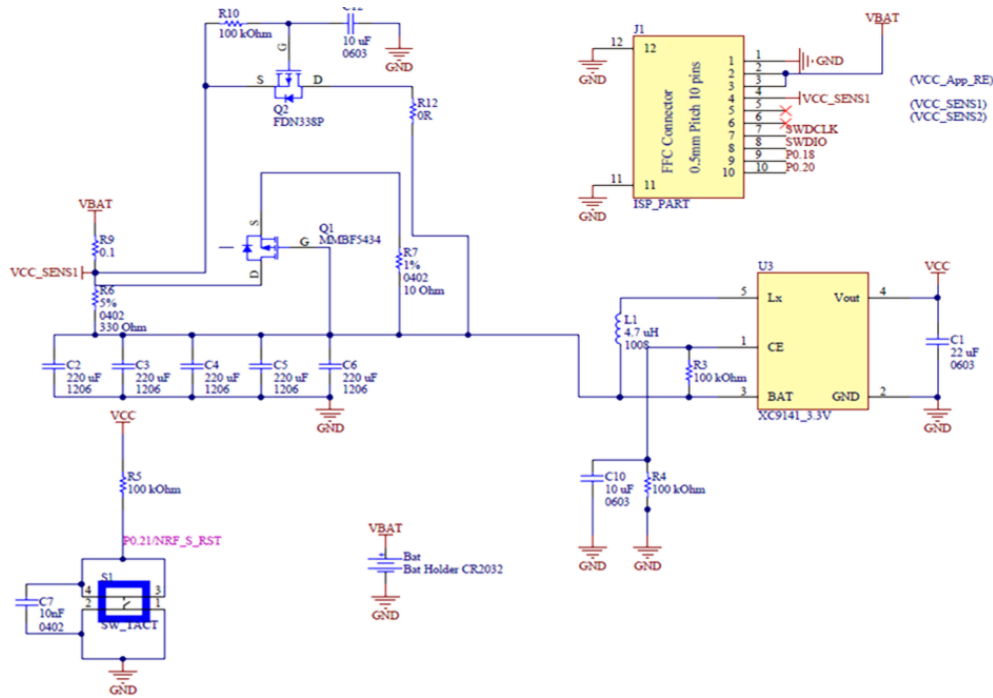
-  DecaWave DW1000 Datasheet and User Manual:
Downloadable after registering at www.decawave.com/support
-  DecaWave DW1000 Application Programming Interface with application examples:
Downloadable after registering at www.decawave.com/support
-  Nordic Semiconductor nRFgo Studio:
Downloadable after registering at www.nordicsemi.com.
-  Nordic Semiconductor Master Control Panel:
Downloadable after registering at www.nordicsemi.com.
-  Keil MDK-ARM Lite:
Downloadable from <https://www.keil.com/demo/eval/arm.htm>.
-  Segger J-Link Lite:
Downloadable from <http://www.segger.com/jlink-software.html>.
-  nRF52 Software Development Kit (SDK):
nRF52 SDK can be downloaded after registering at www.nordicsemi.com. It contains example of source codes applications (C language):
 - Precompiled HEX files
 - Source code
 - Keil ARM project files
 - IAR project files



6. Reference Design

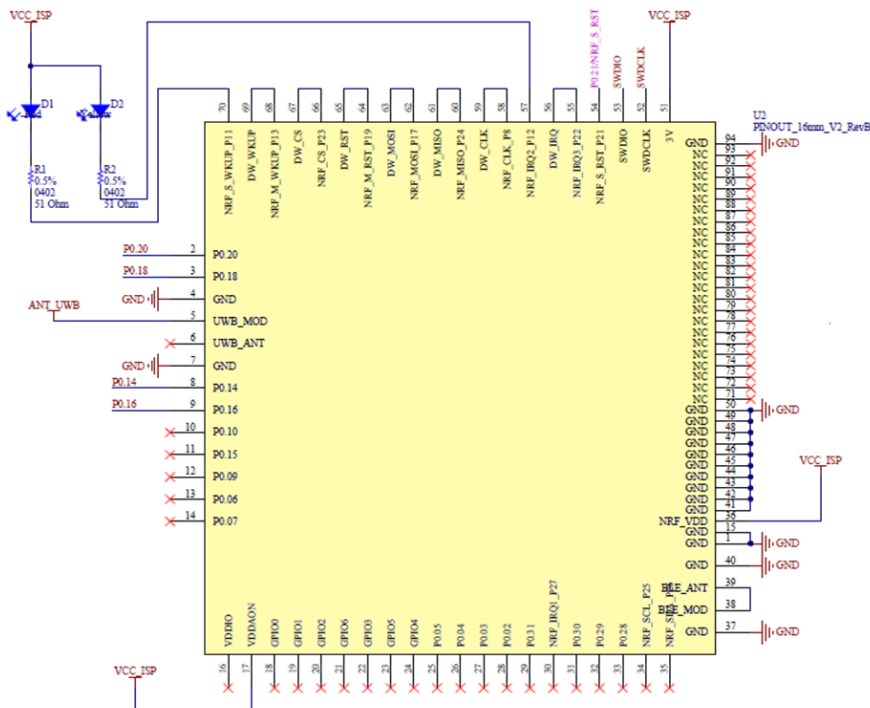
(P0.05_AIN3 to Vbat for batterie level)

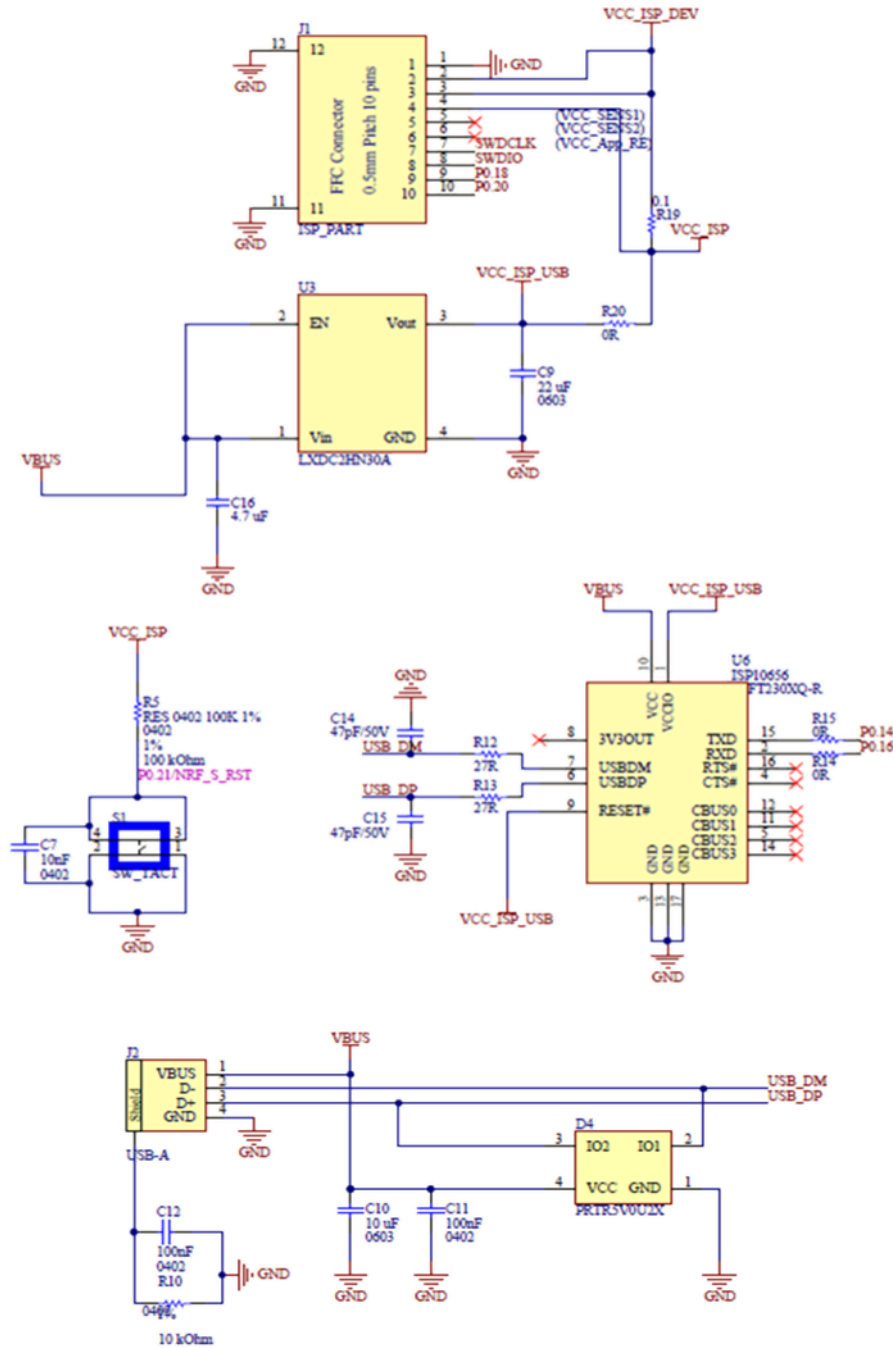
Preliminary Data Sheet



6.2. Tag Board Design

The complete TAG device makes use of Insight SiP ISP1510 BLE and UWB module together with passive components and small primary button cell battery CR2032.





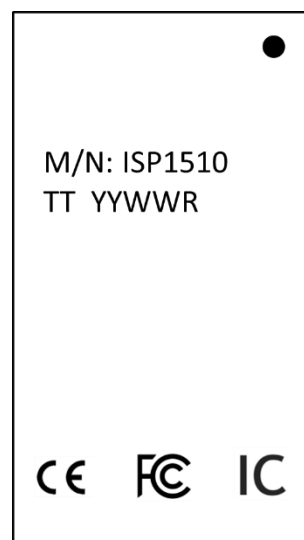
Preliminary Data Sheet

7. Packaging & Ordering information

7.1. Marking

| | | | | | | | | | |
|---|----|---|---|---|---|---|---|---|---|
| M | /N | : | I | S | P | 1 | 5 | 1 | 0 |
| T | T | | Y | Y | W | W | R | | |

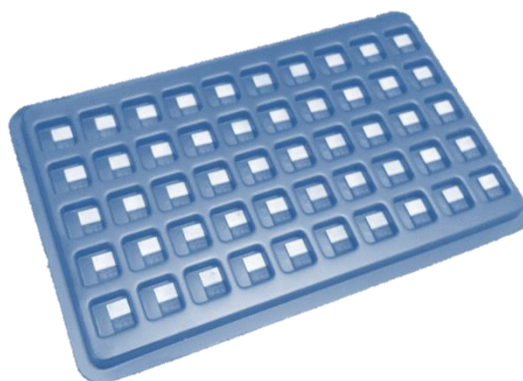
| | |
|---------|---|
| ISP1510 | Part Number |
| TT | 2 letters Module Type (see section 7.5) |
| YY | 2 digits year number |
| WW | 2 digits week number |
| R | 1 letter Hardware revision |



7.2. Prototype Packaging

For engineering samples and prototype quantities up to 99 units, deliveries are provided in thermoformed trays. Please order with "ST" code packaging suffix.

These parts must be backed prior to assembly (see section 8.2).



7.3. Jedec Trays

For higher quantities and volume production, ISP1510 are available in Jedec trays. They are delivered in sealed pack with desiccant pack and humidity sensors. These Jedec trays are also suitable for further baking. Please see section 8.2 for more information on moisture sensitivity.

Jedec trays are proposed in standard quantities of 100 units, 200 units and multiples of 200 units only. Please order with "J1" code packaging suffix for 100-unit tray and "J2" for 200 and multiple unit trays.

Complete information on Jedec trays is available on request.

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7.4. Tape and Reel

ISP1510 are also available in Tape & Reel. They are delivered in sealed pack with desiccant pack and humidity sensors. Reels are proposed in standard quantities of 500 units (180mm / 7" reel) or 2000 units (330mm / 13" reel) only. Please order with "RS" code packaging suffix for 500-unit reels and "R2" for 2000-unit reels.

Complete information is available on request.

7.5. Ordering Information

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---------------------------------------|
| I | S | P | 1 | 5 | 1 | 0 | - | T | T | - | Z | Z | |
| | | | | | | | | ▼ | ▼ | | ▼ | ▼ | |
| | | | | | | | | ▼ | ▼ | | ▼ | ▼ | |
| | | | | | | | | ▼ | ▼ | | ▼ | ▼ | |
| I | S | P | 1 | 5 | 1 | 0 | | | | | | | Part Number |
| | | | | | | | - | U | | | | | UWB & BLE protocol type |
| | | | | | | | - | | X | | | | 512 kB Flash / 64 kB RAM memory type |
| | | | | | | | | | | - | D | K | Development kit ⁽¹⁾ |
| | | | | | | | | | | - | T | B | Test board ⁽¹⁾ |
| | | | | | | | | | | - | A | N | Anchor board ⁽¹⁾ |
| | | | | | | | | | | - | T | G | Tag board ⁽¹⁾ |
| | | | | | | | | | | - | S | T | Unsealed Tray Packaging |
| | | | | | | | | | | - | J | 1 | Jedec Tray with less than 100 units |
| | | | | | | | | | | - | J | 2 | Jedec Tray with multiple of 200 units |
| | | | | | | | | | | - | R | S | Reel of 500 units |
| | | | | | | | | | | - | R | 2 | Reel of 2000 units |

(1) Please see section 5.1 and refer to the following documentation for more information on development kit and test board:

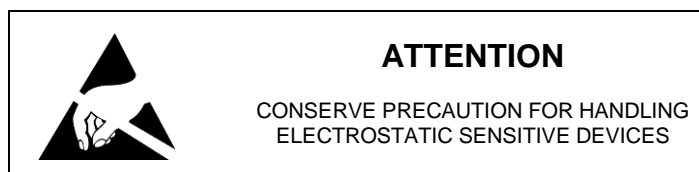
http://www.insightsip.com/fichiers_insightsip/pdf/ble/isp_ble_DS1510_DK.pdf

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8. Storage & Soldering information

8.1. Storage and Handling

- ✚ Keep this product away from other high frequency devices which may interfere with operation such as other transmitters and devices generating high frequencies.
- ✚ Do not expose the module to the following conditions:
 - Corrosive gasses such as Cl₂, H₂S, NH₃, SO₂, or NO_x
 - Extreme humidity or salty air
 - Prolonged exposure to direct Sunlight
 - Temperatures beyond those specified for storage
- ✚ Do not apply mechanical stress
- ✚ Do not drop or shock the module
- ✚ Avoid static electricity, ESD and high voltage as these may damage the module



8.2. Moisture Sensitivity

All plastic packages absorb moisture. During typical solder reflow operations when SMDs are mounted onto a PCB, the entire PCB and device population are exposed to a rapid change in ambient temperature. Any absorbed moisture is quickly turned into superheated steam. This sudden change in vapor pressure can cause the package to swell. If the pressure exerted exceeds the flexural strength of the plastic mold compound, then it is possible to crack the package. Even if the package does not crack, interfacial delamination can occur.

Since the device package is sensitive to moisture absorption, it is recommended to bake the product before assembly. The baking process for dry packing is 24 hours at 125°C.

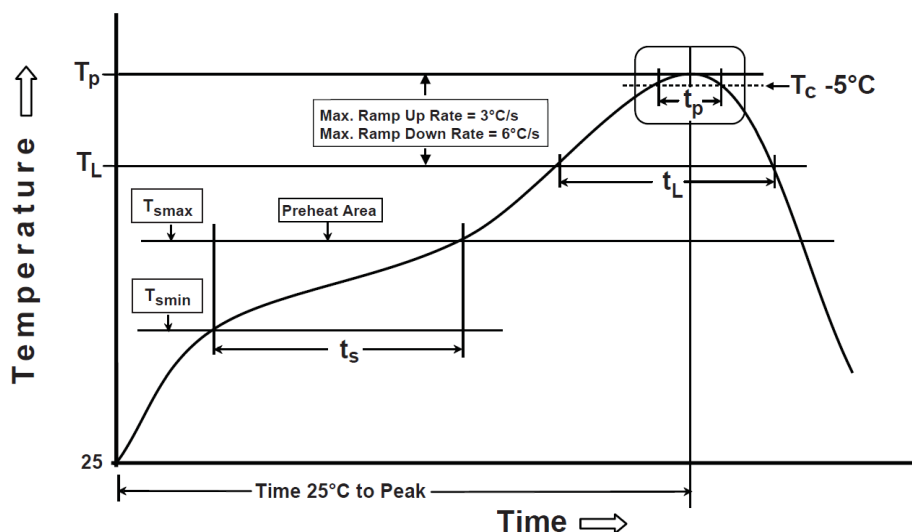
ISP1510 has been tested MSL-5 according to standards. After baking, modules can be exposed to ambient room conditions (approximately 30 °C/60%RH) during 48 hours before assembly on the PCB.



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8.3. Soldering information

Recommendation for RoHS reflow process is according to Jedec J-STD-020 and 033 standard profiles.



| | |
|--|--------------|
| Preheat/Soak | |
| Temperature Min (T_{smin}) | 150 °C |
| Temperature Max (T_{smax}) | 200 °C |
| Time (t_s) from (T_{smin} to T_{smax}) | 60-120 sec |
| Ramp-up rate (T_L to T_p) | 3 °C/sec max |
| Liquidous temperature (T_L) | 217 °C |
| Time (t_L) maintained above T_L | 60-150 sec |

| | |
|---|--------------------|
| Peak package body temperature (T_p) | 260°C (+0/-5°C) |
| Classification Temperature (T_c) | 260 °C |
| Time (t_p) maintained above $T_c - 5^\circ\text{C}$ | 30 sec |
| Ramp-down rate (T_p to T_L) | 6 °C/sec max |
| Time 25 °C to peak temperature | 8 mn max |

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9. Quality & User information

9.1. Pending Certifications

- FCC Identifier tbd
- CE: Complies with 1999/5/EC, EN300328 V1.9.1 – EC DoC N° tbd
- IC – Certification tbd
- Bluetooth SIG certified N° tbd
- RoHS compliant

9.2. USA – User information

This intends to inform how to specify the FCC ID of our module “ISP1510” on the product. Based on the Public Notice from FCC, the host device should have a label which indicates that it contains our module. The label should use wording such as:

“Contains FCC ID: 2AAQS-ISP1510”

Any similar wording that expresses the same meaning may be used.

The label of the host device should also include the below FCC Statement. When it is not possible, this information should be included in the User Manual of the host device:

*“This device complies with part 15 of the FCC rules. Operation is subject to the following two conditions.
(1) This device may not cause harmful interference
(2) This device must accept any interference received, including interference that may cause undesired operation.
Caution: Any Changes or modifications not expressly approved by the party responsible for compliance could void the user’s authority to operate the equipment.”*

9.3. Canada – User information

This intends to inform how to specify the IC ID of our module “ISP1510” on the product. According to Canadian standards “RSS-210” and “RSS-Gen”, the host device should have a label which indicates that it contains our module. The label should use wording such as:

“Contains IC: 11306A-ISP1510”

Any similar wording that expresses the same meaning may be used.

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The label of the host device should also include the below IC Statement. When it is not possible, this information should be included in the User Manual of the host device:

"This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device."

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement."

9.4. RF Exposure Information

This equipment complies with FCC/IC radiation exposure limits set forth for an uncontrolled environment and meets the FCC radio frequency (RF) Exposure Guidelines in Supplement C to OET65 and RSS-102 of the IC radio frequency (RF) Exposure rules. This equipment has very low levels of RF energy that it deemed to comply without maximum permissive exposure evaluation (MPE).

9.5. Informations concernant l'exposition aux fréquences radio (RF)

La puissance de sortie émise par l'appareil de sans fil est inférieure à la limite d'exposition aux fréquences radio d'Industry Canada (IC). Ce module a également été évalué et démontré conforme aux limites d'exposition aux RF d'IC dans des conditions d'exposition à des appareils mobiles et/ou portables.

9.6. Discontinuity

Normally a product will continue to be manufactured as long as all of the following are true:

- The manufacturing method is still available.
- There are no replacement products.
- There is demand for it in the market.

In case of obsolescence, Insight SiP will follow Jedec Standard JSD-48. A Product Discontinuation Notice (PDN) will be sent to all distributors and made available on our website. After this, the procedure goes as follows:

- Last Order Date will be 6 months after the PDN was published.
- Last Shipment Date will be 6 months after Last Order Date, i.e. 12 months after PDN.

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9.7. Disclaimer

Insight SiP's products are designed and manufactured for general consumer applications, so testing and use of the product shall be conducted at customer's own risk and responsibility. Please conduct validation and verification and sufficient reliability evaluation of the products in actual condition of mounting and operating environment before commercial shipment of the equipment. Please also pay attention (i) to apply soldering method that don't deteriorate reliability, (ii) to minimize any mechanical vibration, shock, exposure to any static electricity, (iii) not to overstress the product during and after the soldering process.

The products are not designed for use in any application which requires especially high reliability where malfunction of these products can reasonably be expected to result in personal injury or damage to the third party's life, body or property, including and not limited to (i) aircraft equipment, (ii) aerospace equipment, (iii) undersea equipment, (iv) power plant control equipment, (v) medical equipment, (vi) transportation equipment, (vii) traffic signal equipment, (viii) disaster prevention / crime prevention equipment.

The only warranty that Insight SiP provides regarding the products is its conformance to specifications provided in datasheets. Insight SiP hereby disclaims all other warranties regarding the products, express or implied, including without limitation any warranty of fitness for a particular purpose, that they are defect-free, or against infringement of intellectual property rights. Insight SiP customers agree to indemnify and defend Insight SiP against all claims, damages, costs and expenses that may be incurred, including without any limitation, attorney fees and costs, due to the use of products.

Mouser Electronics

Authorized Distributor

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