

STS32XXXXUXXX

TVS Diode ESD suppressor



Product features

- 2000 Watts peak pulse power per line
 $t_p = 8/20 \mu s$
- Solid-state silicon-avalanche technology
- Protects one uni-directional I/O line
- Low clamping voltage
- Low leakage current
- High surge capability
- Meets moisture sensitivity level (MSL) 3
- Molding compound flammability rating:
UL 94V-0
- Termination finish: Tin

Applications

- Cellular handsets and accessories
- Microprocessor based equipment
- Potable electronics
- Notebooks, desktops, and servers
- Portable instrumentation
- Power lines
- Peripherals

Environmental compliance and general specifications

- IEC61000-4-2 (ESD)
 - Up to ± 30 kV (air)
 - Up to ± 30 kV (contact)
- IEC61000-4-5 (Lightning) Up to 110 A (8/20 μs)



Ordering part number

	ST	S32	1	050	U	852
Family	_____	_____	_____	_____	_____	_____
Package (S32- SOD-323)	_____	_____	_____	_____	_____	_____
Number of channels (1)	_____	_____	_____	_____	_____	_____
Operating voltage (050- 5 V)	_____	_____	_____	_____	_____	_____
Bi/Uni directional (U- Uni)	_____	_____	_____	_____	_____	_____
Capacitance (852- 850 pF)	_____	_____	_____	_____	_____	_____

Pin out/functional diagram



SOD-323



Pin Configuration



Powering Business Worldwide

Absolute maximum ratings

(+25 °C, RH=45%-75%, unless otherwise noted)

Parameter	Symbol	Value	Unit
STS32XXXXULXXX			
Peak pulse power dissipation on 8/20 μ s waveform	P_{pp}	2000	W
ESD per IEC 61000-4-2 (Air)	V_{ESD}	+/-30	kV
ESD per IEC 61000-4-2 (Contact)		+/-30	
Lead soldering temperature	T_L	+260 (10 seconds)	°C
Operating junction temperature range	T_J	-55 to +125	°C
Storage temperature range	T_{STG}	-55 to +150	°C

Electrical characteristics

(+25 °C)

STS321050U852

Parameter	Test condition	Minimum	Typical	Maximum	Symbol (Units)
Reverse working voltage	-	-	-	5	V_{RWM} (V)
Reverse breakdown voltage	$I_t = 1$ mA	6	7	8	V_{BR} (V)
Reverse leakage current	$V_{RWM} = 5$ V	-	-	1	I_R (μ A)
Clamping voltage	$I_{pp} = 50$ A, $t_p = 8/20$ μ s	-	11	13	V_c (V)
	$I_{pp} = 110$ A, $t_p = 8/20$ μ s	-	14	17	V_c (V)
Junction capacitance	$V_{RWM} = 0$ V, $f = 1$ MHz	-	850	1050	C_J (pF)

STS321070U722

Parameter	Test condition	Minimum	Typical	Maximum	Symbol (Units)
Reverse working voltage	-	-	-	7	V_{RWM} (V)
Reverse breakdown voltage	$I_t = 1$ mA	7.5	8	9	V_{BR} (V)
Reverse leakage current	$V_{RWM} = 7$ V	-	-	1	I_R (μ A)
Clamping voltage	$I_{pp} = 50$ A, $t_p = 8/20$ μ s	-	12	15	V_c (V)
	$I_{pp} = 100$ A, $t_p = 8/20$ μ s	-	15	18	V_c (V)
Junction capacitance	$V_{RWM} = 0$ V, $f = 1$ MHz	-	720	900	C_J (pF)

Electrical characteristics
(+25 °C)

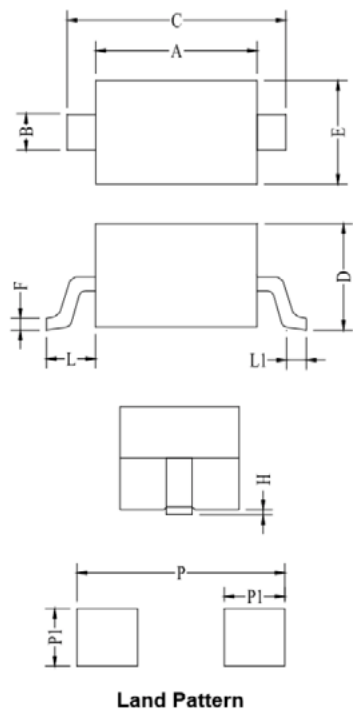
STS321120U372

Parameter	Test condition	Minimum	Typical	Maximum	Symbol (Units)
Reverse working voltage	-	-	-	12	V_{RWM} (V)
Reverse breakdown voltage	$I_t = 1$ mA	13	14.4	17	V_{BR} (V)
Reverse leakage current	$V_{RWM} = 12$ V	-	-	1	I_R (μA)
Clamping voltage	$I_{PP} = 20$ A, $t_p = 8/20$ μs	-	16	19	V_c (V)
	$I_{PP} = 40$ A, $t_p = 8/20$ μs	-	20	24	V_c (V)
	$I_{PP} = 70$ A, $t_p = 8/20$ μs	-	22	28	V_c (V)
	$t_p = 8/20$ μs	-	-	-	-
Junction capacitance	$V_{RWM} = 0$ V, $f = 1$ MHz	-	370	450	C_J (pF)

STS321150U332

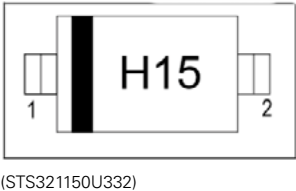
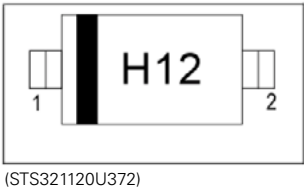
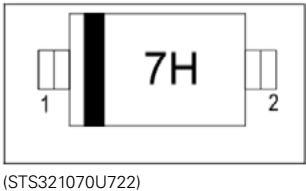
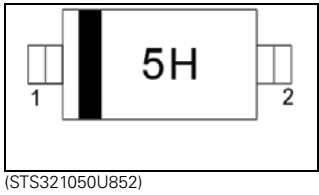
Parameter	Test condition	Minimum	Typical	Maximum	Symbol (Units)
Reverse working voltage	-	-	-	15	V_{RWM} (V)
Reverse breakdown voltage	$I_t = 1$ mA	16	17.2	20	V_{BR} (V)
Reverse leakage current	$V_{RWM} = 15$ V	-	-	1	I_R (μA)
Peak pulse current	$t_p = 8/20$ μs	-	-	55	I_{PP} (A)
Clamping voltage	$I_{PP} = 25$ A, $t_p = 8/20$ μs	-	22	25	V_c (V)
	$I_{PP} = 50$ A, $t_p = 8/20$ μs	-	26	28	V_c (V)
	$I_{PP} = 55$ A, $t_p = 8/20$ μs	-	27	30	V_c (V)
	$t_p = 8/20$ μs	-	-	-	-
Junction capacitance	$V_{RWM} = 0$ V, $f = 1$ MHz	-	330	400	C_J (pF)

Mechanical parameters, pad layout- mm/inches



Dimension	Millimeters		Inches	
	Minimum	Maximum	Minimum	Maximum
A	1.60	1.80	0.063	0.071
B	0.25	0.35	0.010	0.014
C	2.50	2.75	0.098	0.108
D	0.00	1.00	0.000	0.039
E	1.20	1.40	0.047	0.055
F	0.08	0.15	0.003	0.006
L	0.475 REF		0.019 REF	
L1	0.25	0.40	0.010	0.016
H	0.00	0.10	0.000	0.004
P	3.00	3.00	0.118	0.118
P1	0.80	0.80	0.031	0.031

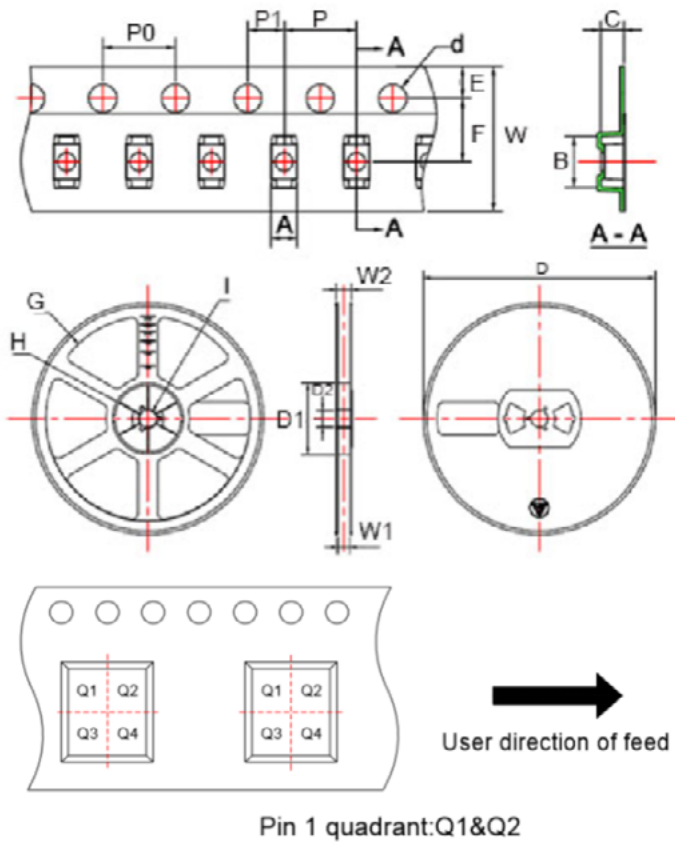
Part marking



Packaging information mm/inches

Drawing not to scale.

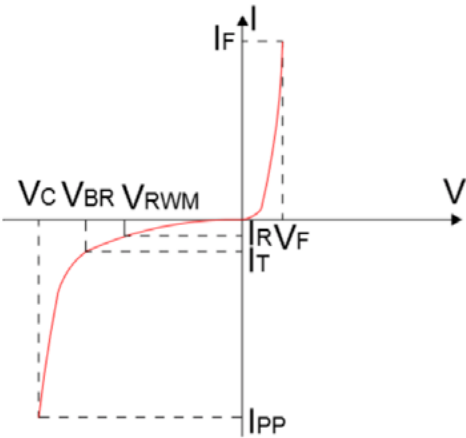
Supplied in tape and reel packaging, 3,000 parts per 7" diameter reel (EIA-481 compliant)



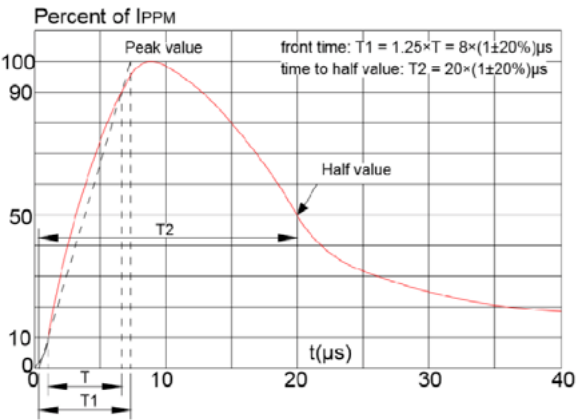
Symbol	Millimeters	Inches
A	1.46±0.05	0.057±0.002
B	2.90±0.05	0.114±0.002
C	1.25±0.05	0.049±0.002
d	±1.50±0.1	±0.059±0.004
E	1.75±0.1	0.069±0.004
F	3.50±0.1	0.138±0.004
P0	4.0±0.1	0.157±0.004
P	4.0±0.1	0.157±0.004
P1	2.0±0.1	0.079±0.004
W	8.00+0.3/-0.1	0.315+0.012/-0.004
D	±178.0±2	±7.008±0.079
D1	54.40±1	2.142±0.039
D2	13.0±1	0.512±0.039
G	R78.0±1	R3.071±0.039
H	R25.60±1	R1.008±0.039
I	R6.50±1	R0.256±0.039
W1	9.50±1	0.374±0.039
W2	12.30±1	0.484±0.039

Ratings and V-I characteristic curves (+25 °C unless otherwise noted)

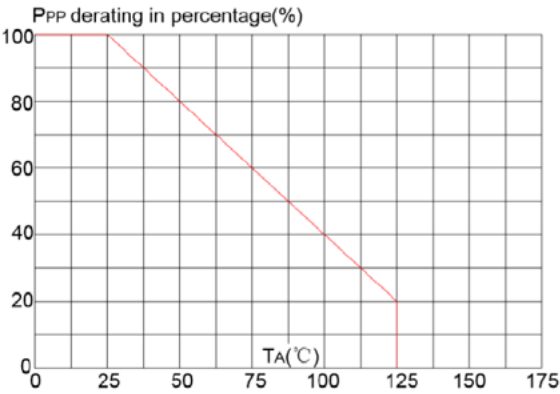
V- I curve characteristics (Uni-directional)



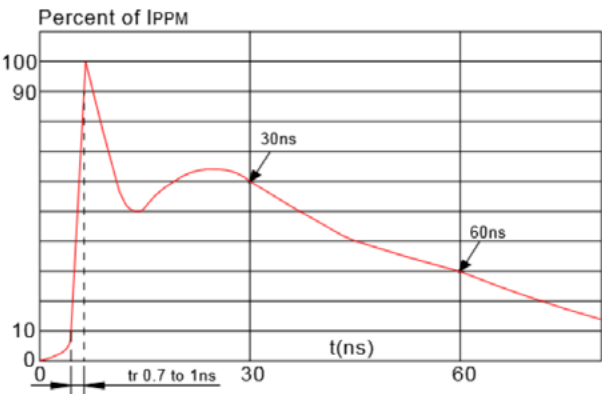
Pulse waveform (8/20 μs)



Pulse derating curve



ESD waveform



Solder reflow profile

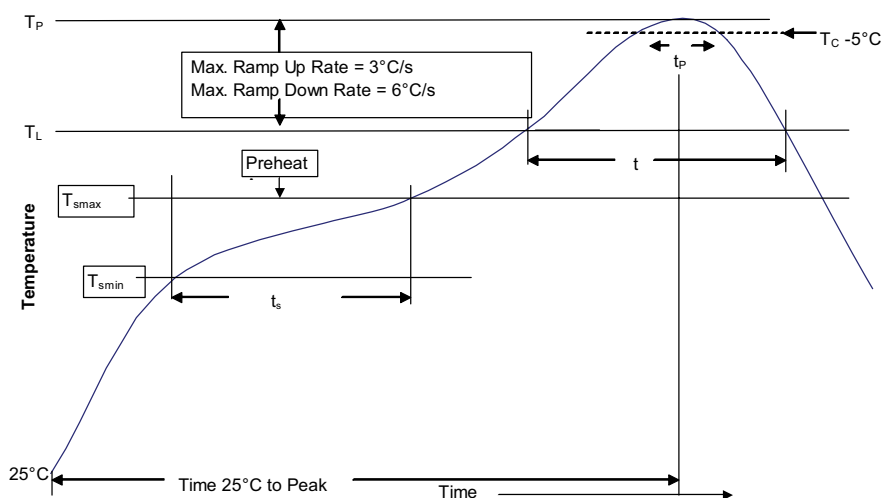


Table 1 - Standard SnPb solder (T_C)

Package thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2 - Lead (Pb) free solder (T_C)

Package thickness	Volume mm ³ <350	Volume mm ³ 350 - 2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 - 2.5 mm	260 °C	250 °C	245 °C
>2.5 mm	250 °C	245 °C	245 °C

Reference J-STD-020

Profile feature	Standard SnPb solder	Lead (Pb) free solder
Preheat and soak		
• Temperature min. (T_{smin})	100 °C	150 °C
• Temperature max. (T_{smax})	150 °C	200 °C
• Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Ramp up rate T_L to T_p	3 °C/ second max.	3 °C/ second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time (t_L) maintained above T_L	60-150 seconds	60-150 seconds
Peak package body temperature (T_p)*	Table 1	Table 2
Time (t_p)* within 5 °C of the specified classification temperature (T_C)	20 seconds*	30 seconds*
Ramp-down rate (T_p to T_L)	6 °C/ second max.	6 °C/ second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

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