

EXL1V0605

High current molded inductor



Product features

- High current carrying capacity
- Low DCR, high efficiency
- Magnetically shielded, low EMI
- Soft saturation
- Inductance range from 0.82 μ H to 8.2 μ H
- Current range from 6.2 A to 21 A
- 7.4 mm x 7.1 mm footprint surface mount package in a 5.0 mm height
- Alloy powder core material
- Moisture Sensitivity Level (MSL) 1

Applications

- Voltage Regulator Module (VRM)
- Multi-phase regulators
- Point-of-load (POL) converters
- Desktop and server VRMs and EVRDs
- Base station equipment
- Battery power systems
- Graphics cards
- Data networking and storage system

Environmental compliance and general specifications

- Storage temperature range (Component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant



Product specifications

Part number ⁵	OCL ¹ (μH) $\pm 20\%$	FLL ² (μH) minimum	I_{rms} ³ (A) typical		I_{sat} ⁴ (A)	DCR (m Ω) typical @ +25 °C	DCR (m Ω) maximum @ +25 °C	SRF (MHz) typical
EXL1V0605-R82-R	0.82	0.45	16	21	20	3.8	4.18	52
EXL1V0605-1R0-R	1.0	0.56	15	20	18	4.1	4.52	50
EXL1V0605-1R2-R	1.2	0.67	14	18	16	5.3	5.83	43
EXL1V0605-1R5-R	1.5	0.84	13	17	14.5	5.7	6.3	40
EXL1V0605-1R8-R	1.8	1.0	12	16	13.5	6.4	7.1	33
EXL1V0605-2R2-R	2.2	1.23	10	13	12	7.7	8.5	30
EXL1V0605-3R3-R	3.3	1.84	8.5	11	10	11.2	12.5	25
EXL1V0605-4R3-R	4.3	2.4	7.0	9.0	8.5	15.1	16.2	23
EXL1V0605-4R7-R	4.7	2.63	6.5	8.5	8.4	16.7	18.4	22
EXL1V0605-5R6-R	5.6	3.13	5.7	7.0	8.3	20	22	19
EXL1V0605-6R8-R	6.8	3.8	5.2	6.6	7.0	23.1	25.4	17
EXL1V0605-8R2-R	8.2	4.59	4.5	6.2	6.8	28.6	31.5	15

1. Open circuit inductance (OCL) Test parameters: 100 kHz, 0.1 V_{rms}, 0.0 Adc, +25 °C

2. Full load inductance (FLL) Test parameters: 100 kHz, 0.1 V_{rms}, I_{sat} , +25 °C

3. I_{rms} : Heat rated current (I_{rms}) will cause the part temperature rise approximately ΔT of 40 °C. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application. The part temperature (ambient + temp rise) should not exceed +125 °C under worst case operating conditions.

4. I_{sat} : Peak current for approximately 30% rolloff @ +25 °C

5. Part Number Definition: EXL1V0605-xxx-R

EXL1V0605 = Product code and size

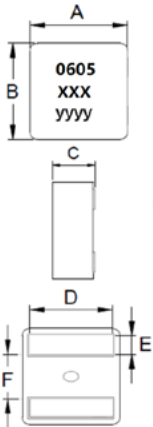
xxx= inductance value in μH , R= decimal point,

If no R is present then third digit equals the number of zeros

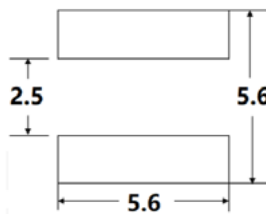
-R suffix = RoHS compliant

6. Rated operating voltage: 40 V typical

Mechanical parameters, schematic, pad layout (mm)



Recommended pad layout



Schematic



Figure 1. DCR test

Part number	A	B	C	D	E	F
EXL1V0605-xxx-R	7.2 \pm 0.2	6.9 \pm 0.2	4.8 \pm 0.2	5.3 \pm 0.3 (R82-1R8)	5.2 \pm 0.3 (2R2-8R2)	1.4 \pm 0.2
						2.6 \pm 0.25

Part marking: 0605, xxx= Inductance value in μH (R= Decimal point, if no R is present then last digit equals number of zeros), yyyy= lot code

All soldering surfaces to be coplanar within 0.1 millimeters

Tolerances are ± 0.3 millimeters unless stated otherwise

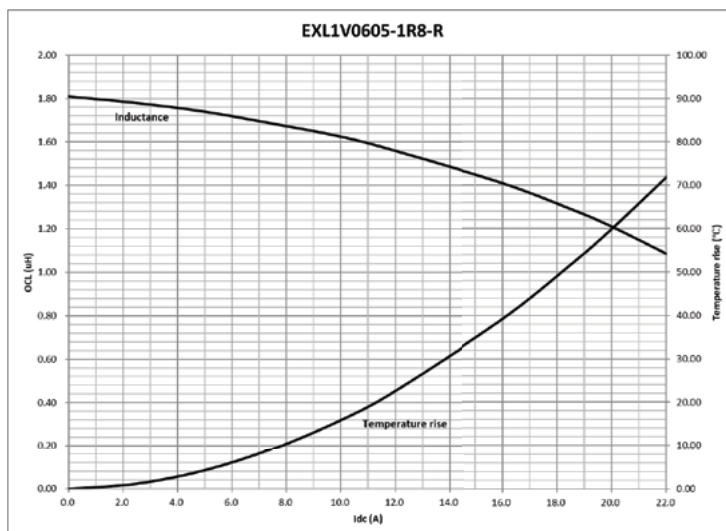
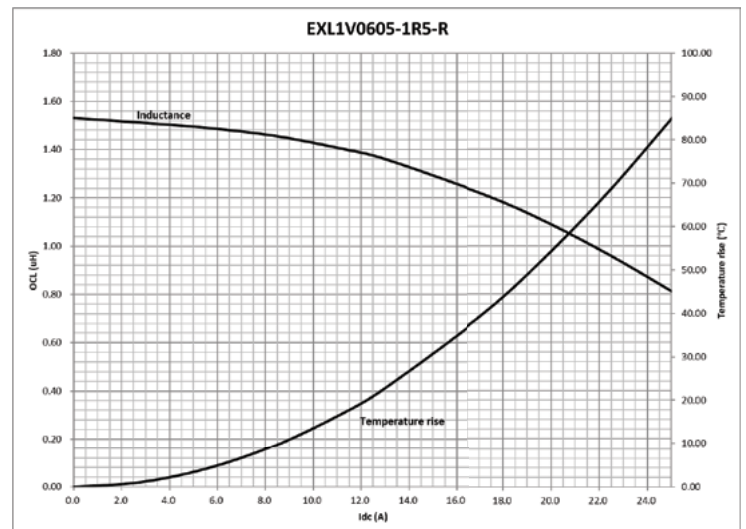
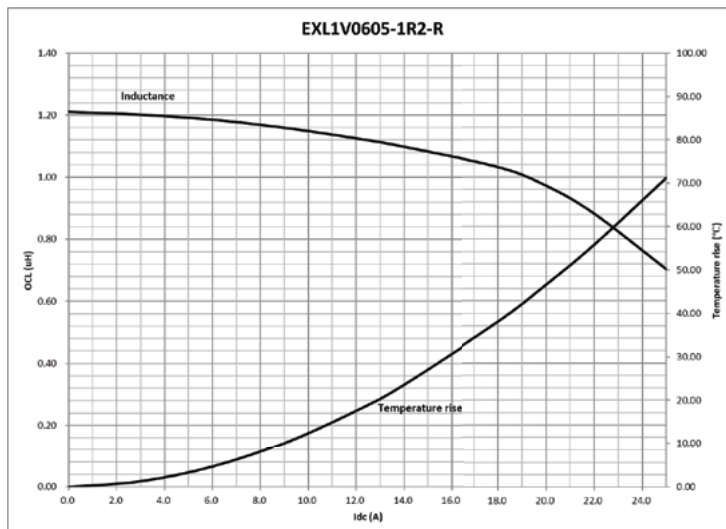
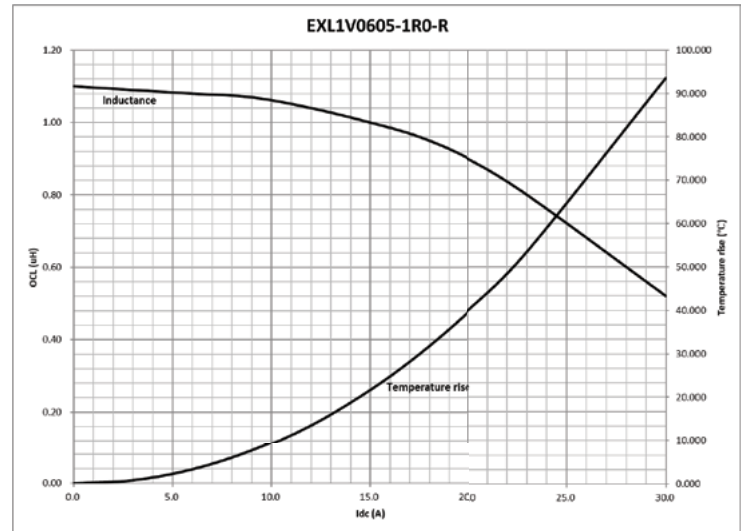
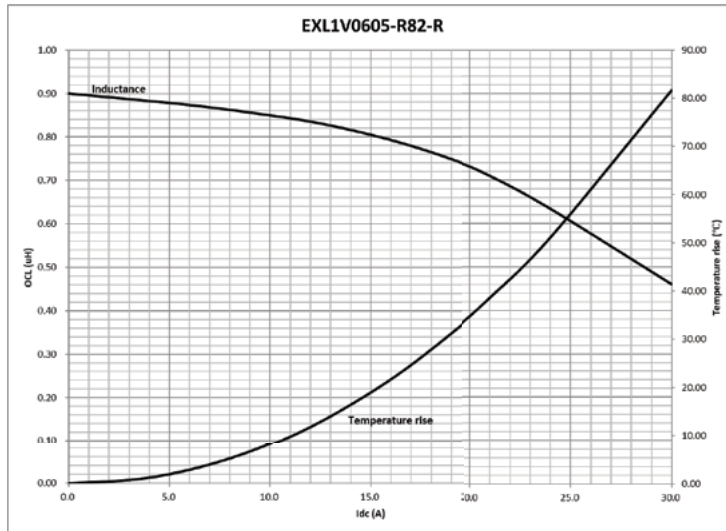
Dimensions of recommended PCB layout are reference only.

Pad layout tolerances are ± 0.1 millimeters unless stated otherwise

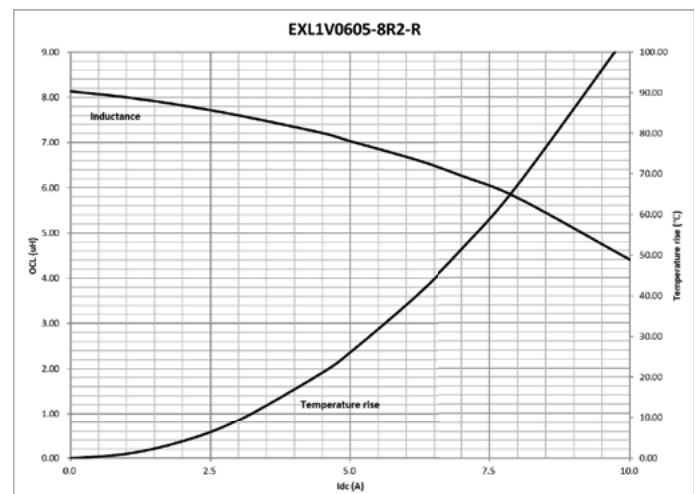
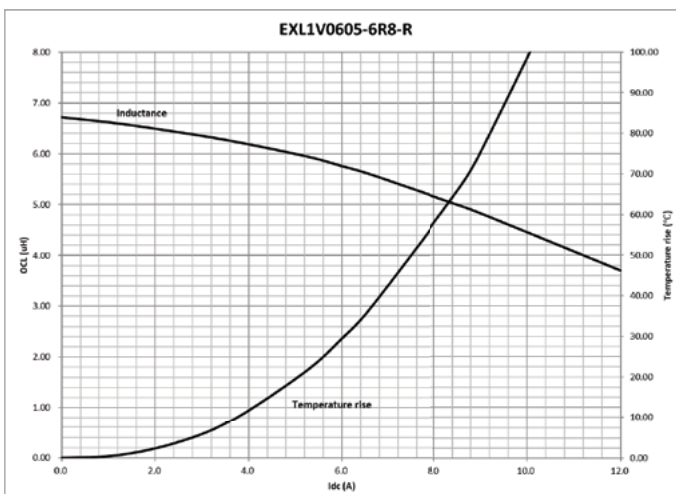
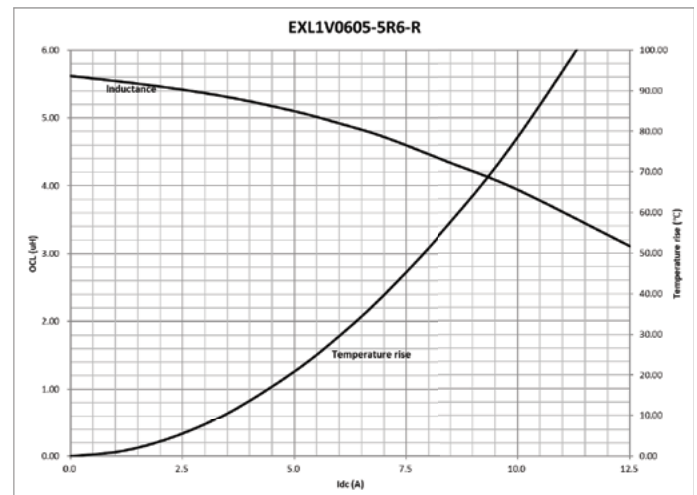
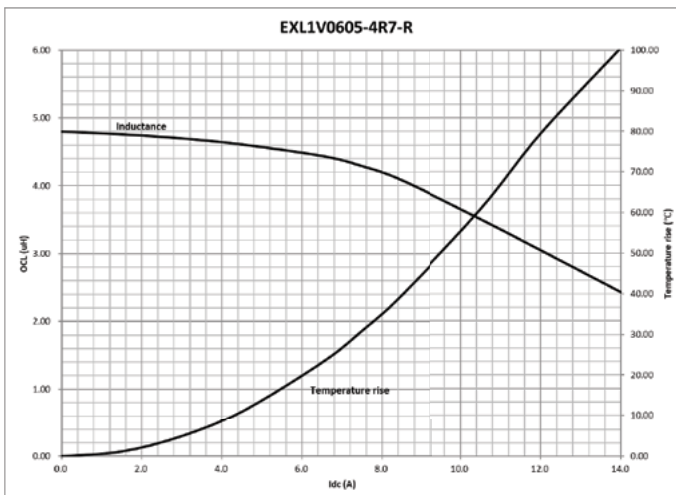
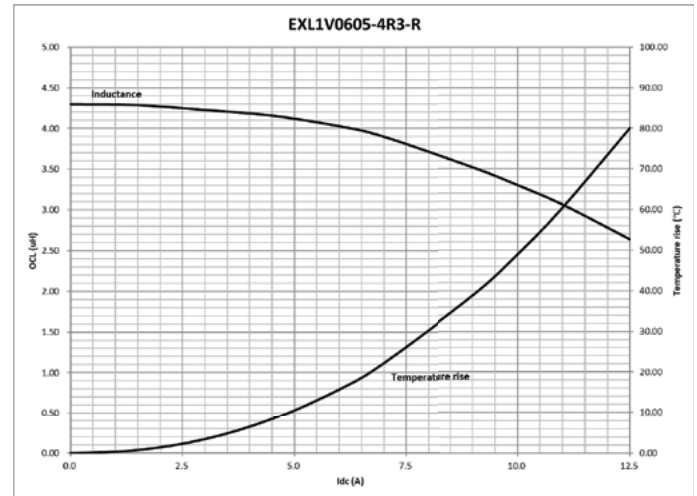
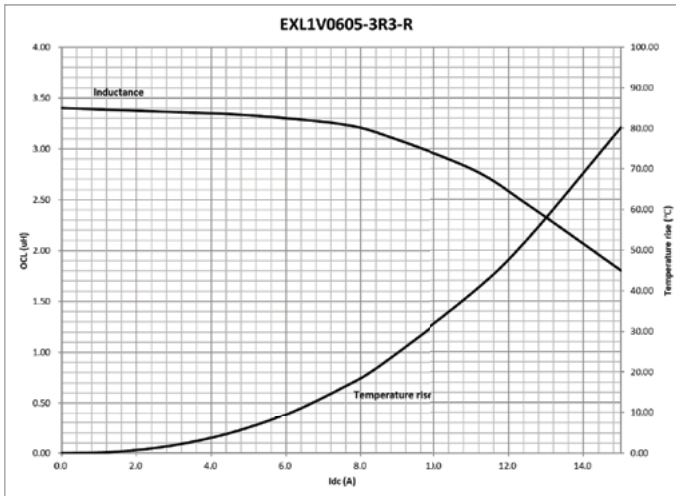
Four terminal kelvin-clip recommended for DCR testing as shown in Figure 1.

Traces or vias underneath the inductor is not recommended.

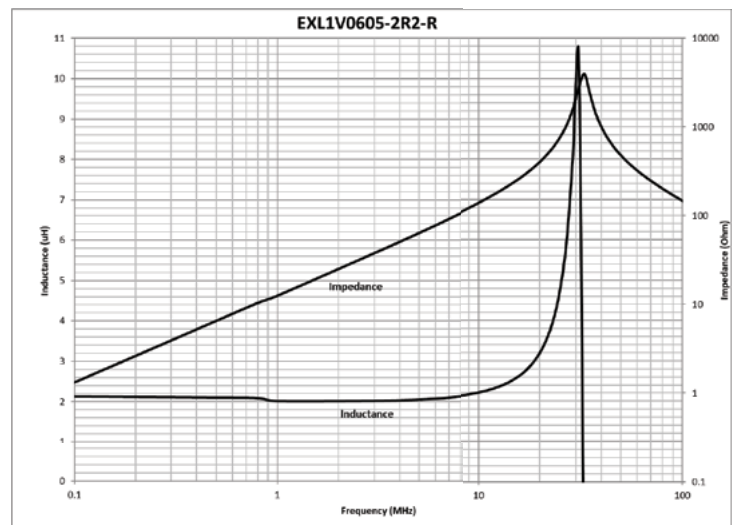
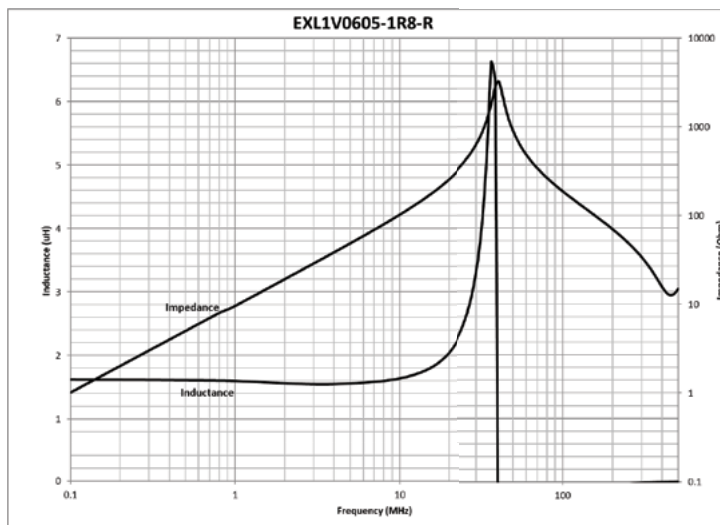
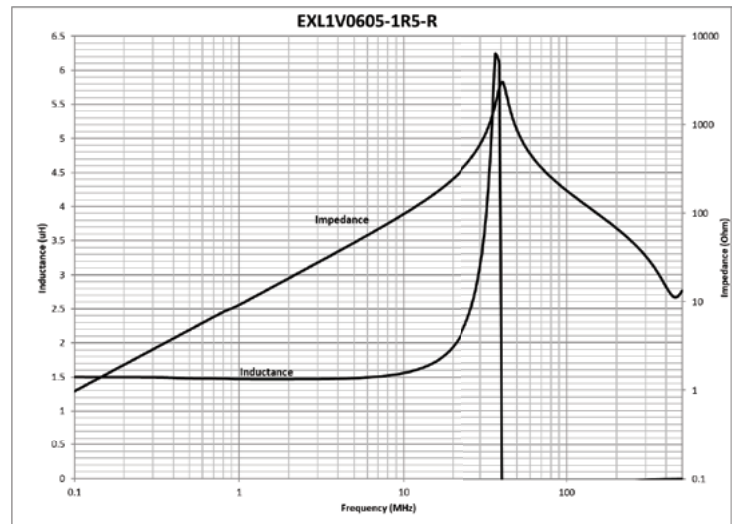
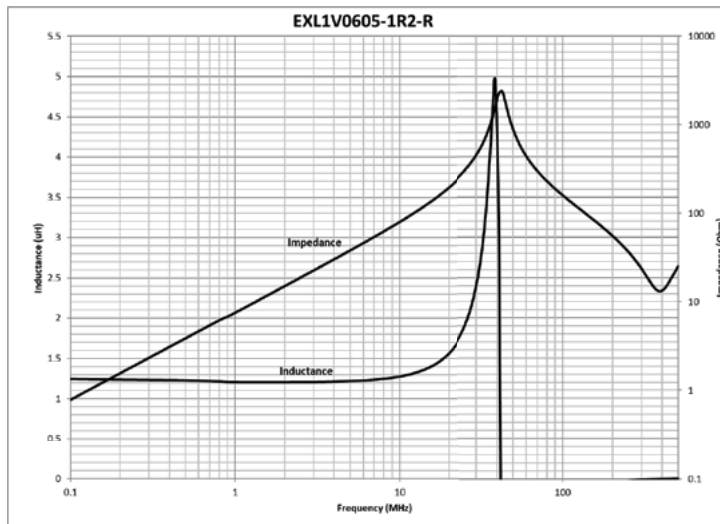
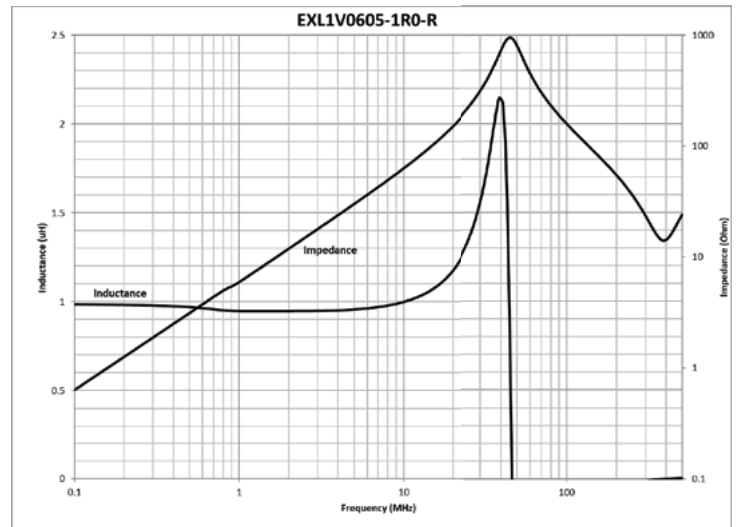
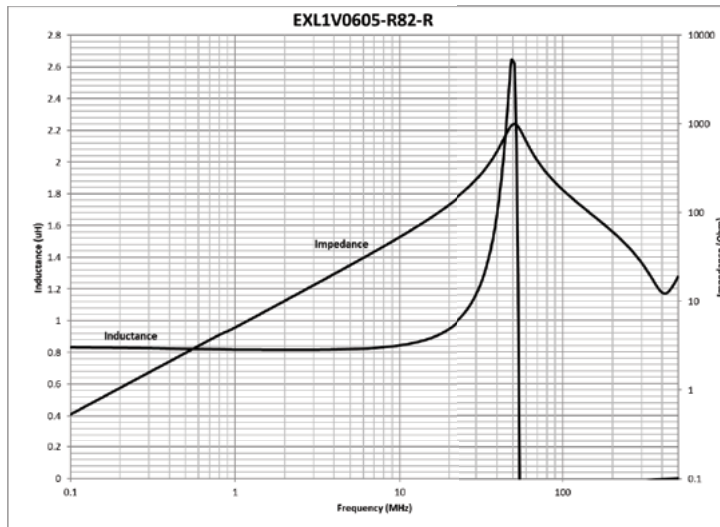
Inductance and temperature rise vs. current



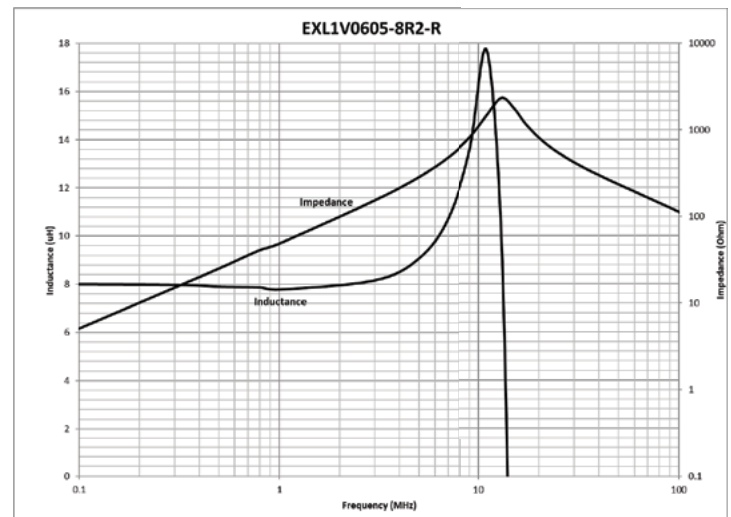
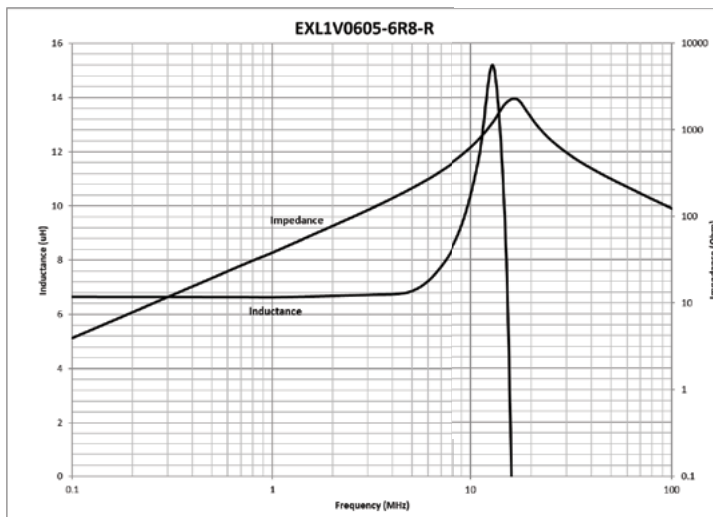
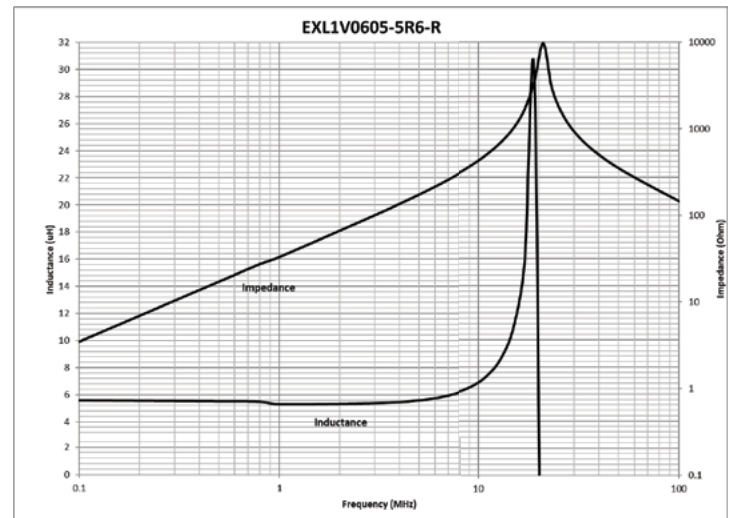
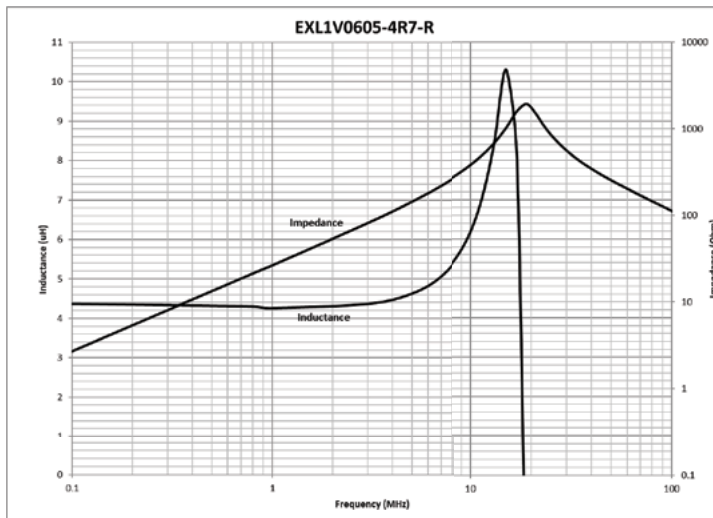
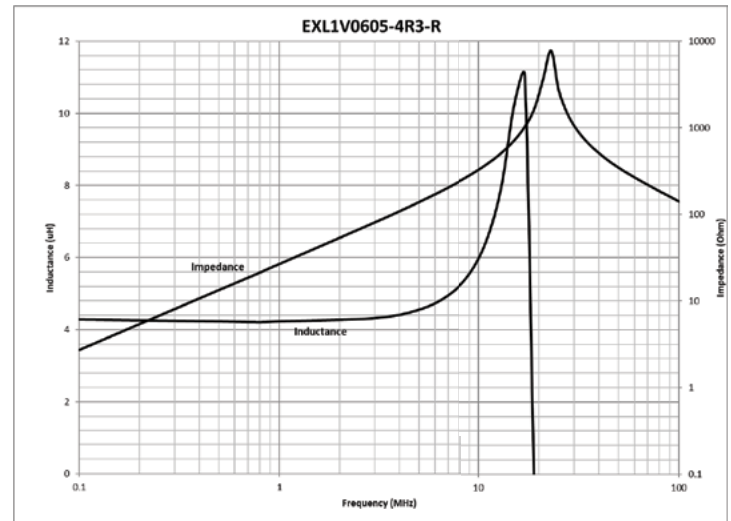
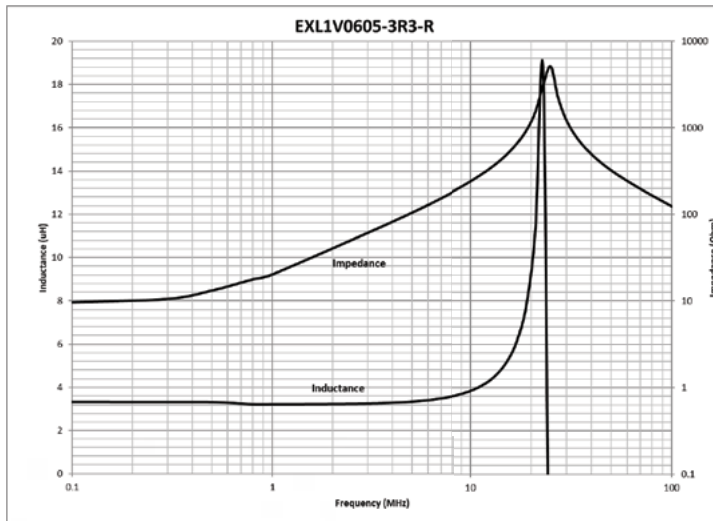
Inductance and temperature rise vs. current, continued



Inductance and impedance vs. frequency curve



Inductance and impedance vs. frequency curve, continued



Solder reflow profile

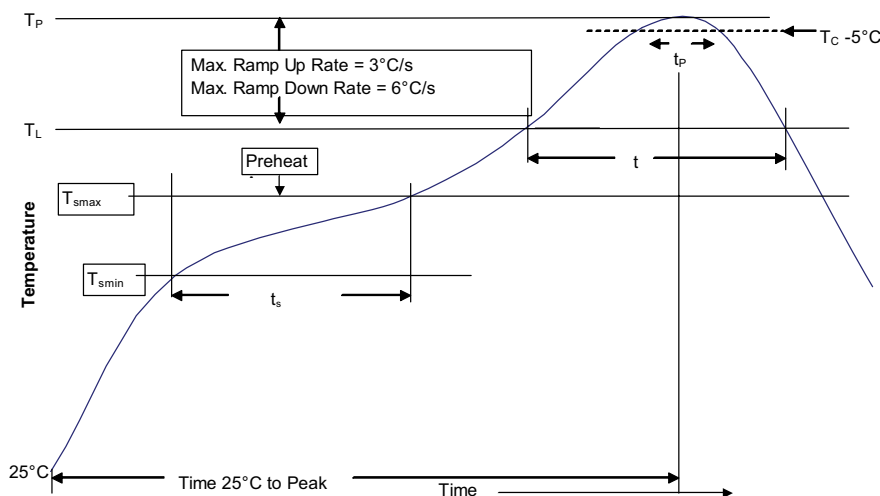


Table 1 - Standard SnPb solder (T_C)

Package thickness	Volume mm^3 <350	Volume mm^3 ≥ 350
<2.5 mm	235 °C	220 °C
≥ 2.5 mm	220 °C	220 °C

Table 2 - Lead (Pb) free solder (T_C)

Package thickness	Volume mm^3 <350	Volume mm^3 350 - 2000	Volume mm^3 >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 – 2.5 mm	260 °C	250 °C	245 °C
>2.5 mm	250 °C	245 °C	245 °C

Reference J-STD-020

Profile feature	Standard SnPb solder	Lead (Pb) free solder
Preheat and soak		
• Temperature min. (T_{smin})	100 °C	150 °C
• Temperature max. (T_{smax})	150 °C	200 °C
• Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Ramp up rate T_L to T_p	3 °C/ second max.	3 °C/ second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time (t_L) maintained above T_L	60-150 seconds	60-150 seconds
Peak package body temperature (T_p)*	Table 1	Table 2
Time (t_p)* within 5 °C of the specified classification temperature (T_C)	20 seconds*	30 seconds*
Ramp-down rate (T_p to T_L)	6 °C/ second max.	6 °C/ second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

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