

# ARTESYN LGA C SERIES

15 - 100 Watts



Advanced Energy's Artesyn LGA03C series is a 3 amp non-isolated DC-DC converter that provides a cost-effective high density power solution in a low profile, surface-mount land grid array package for space sensitive applications. The converter accepts a wide range 3–14Vdc input and has a 15 watt power output rating. Its output voltage is adjustable from 0.59 to 5.1 Vdc to accommodate a wide variety of silicon power needs. Standard features include remote sense, remote enable and voltage margining. LGA03C series converters offer resistor-programmable undervoltage lockout, as well as non-latching short-circuit and overvoltage protection.

## DATA SHEET

### Total Power:

15 - 100 W

### # of Outputs:

Single



## SPECIAL FEATURES

- 3, 6, 10 and 20 A output current rating
- Wide input voltage range; up to 14 V
- Adjustable output voltage; 0.59-5.1 V
- Excellent transient response
- High efficiency
- Output margining
- Power enable
- Minimal airflow requirement
- Termination voltage capability
- Ultra compact profile and footprint
- RoHS compliant
- Remote sense
- Termination voltage capability

## SAFETY

- Designed to meet EN60950
- International Standards for Solderability: J-STD-002B IEC-60068-2-58

## ELECTRICAL SPECIFICATIONS

Output		3/6/10 A Models	20 A Model
Output voltage	See Page 3	0.59 - 5.1 V	
Output setpoint accuracy	0.1% trim resistors	±1.0%	
Line regulation		±0.2%	
Load regulation		±0.5%	
Max Current Max Power		15/30/50 W	100 W
Overshoot	At turn-on	0%	
Undershoot	At turn-off	0 mV	
Ripple and noise 5 Hz to 20 MHz	See Note 1 $V_{in} = 5\text{ V}$ , $V_{out} = 2.5\text{ V}$	20/25/30 mV	30 mV
Transient response	See Notes 1 and 2 $V_{in} = 5\text{ V}$ , $V_{out} = 2.5\text{ V}$	100/160/160 mV 15 $\mu\text{s}$ recovery to within regulation band	1175 mV 15 $\mu\text{s}$ recovery to within regulation band
Input			
Input voltage range <sup>3</sup>		3 - 14 Vdc	4.5 - 14 Vdc
Input current	Enable On at (0 A) Enable Off	50 mA 5 mA	
Start-up time	Power up Enable On/Off	3 ms 2 ms	
General			
Efficiency	$V_{in} = 5\text{ V}$ , $V_{out} = 2.5\text{ V}$ , $I_{out} = 50\% I_{max}$	92% typ.	92% typ.
Switching frequency		1 MHz	800 kHz
Material flammability		UL94V-0	
MTBF	12 V @ 40 °C 100% load Bellcore 332	> 20,000,000 hours	
Coplanarity		150 $\mu\text{m}$	
Thermal performance See Technical Reference Note	Operating ambient Non-operating ambient	-40 °C to +85 °C -40 °C to +125 °C	

Protection		
Short circuit	Hiccup, non-latching	
Oversoltage	Hiccup, non-latching	
Minimum Recommended System Capacitance	3/6/10 A Model	20 A Model
Short circuit	1 $\mu\text{F}$	10 $\mu\text{F}$
Oversoltage	10 $\mu\text{F}$	50 $\mu\text{F}$

## ORDERING INFORMATION

Standard Model Numbers	Output Power (Max.)	Input Voltage	Output Voltage	Output Current		Efficiency (Typical)	Regulation	
				Min	Max		Min	Max
LGA03C-00SADJJ	15 W	3 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	3 A	92%	±0.2%	±0.5%
LGA06C-00SADJJ	30 W	3 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	6 A	92%	±0.2%	±0.5%
LGA10C-00SADJJ	50 W	3 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	10 A	92%	±0.2%	±0.5%
LGA20C-01SADJJ	100 W	4.5 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	20 A	91%	±0.2%	±0.5%

## MODEL NUMBER SYSTEM WITH OPTIONS



Product Family	Rated Output Current	Performance	Input Voltage	Type of Output	Options	RoHS Compliance
<b>LGA</b>	<b>XX</b>	<b>C</b>	<b>00</b>	<b>SADJ</b>	<b>X</b>	<b>J</b>
	<b>Rated Output Current</b> 03 = 3 Amp 06 = 6 Amp 10 = 10 Amp 20 = 20 Amp	<b>Performance</b> C = Cost Optimized	<b>Input Voltage</b> 00 = 3 - 14.0 V 01 = 4.5 - 14.0 V	<b>Type of Output</b> Single Adjustable Output	<b>Options</b> X = Various Options (see Sales Rep)	<b>RoHS Compliance</b> J = Pb free (RoHS 6/6 compliant)

## HEATSINK NUMBER SYSTEM WITH OPTIONS



Product Family	Product	Purpose	Height*
<b>LGA</b>	<b>XX</b>	<b>C</b>	<b>00</b>
Land Grid Array	Heatsink	Heatsink and Adhesive	<b>Total Height</b> (LGA20 + Heatsink) 045 = 0.45" 048 = 0.48" 050 = 0.50"

\* Height is the total height of the LGA20C-00SADJJ with heatsink attached.

## APPLICATION EQUATIONS

## Setting Output Voltage

Default output voltage: 0.591 V

The output voltage may be adjusted with a resistor placed between the "Trim" and "-Sense" pin.

The formula for calculating the value of this resistor is:

$$R_{\text{trim}} \text{ (k}\Omega\text{)} = \frac{1.182}{V_{\text{out}} - 0.591}$$

See Technical Reference Note for other trimming methods.

## Setting Margin Control

To margin the output up, pull the margin control pin high. To margin down, pull the margin control pin low. If the pin is left floating, the feature is disabled. The maximum margining range is  $\pm 33\%$  of the output default voltage setting, with maximum output at 5.5 V

$$V_{\text{margin\_up}} = 0.1182 * \frac{R_{\text{margin}}}{R_{\text{ofs+}}} * \frac{R_{\text{trim}} + 2k}{R_{\text{trim}}}$$

$$V_{\text{margin\_down}} = 0.1182 * \frac{R_{\text{margin}}}{R_{\text{ofs-}}} * \frac{R_{\text{trim}} + 2k}{R_{\text{trim}}}$$

## Setting Output Voltage

Default Turn-on voltage: 2.9 V (300 mV Hysteresis)

The Turn-on voltage may be adjusted with a resistor placed between the "Enable" and "Ground" pins.

The formula for calculating the value of this resistor is:

$$R_{\text{uvlo}} \text{ (k}\Omega\text{)} = \frac{14.81 * 6.81}{(6.81 * V_{\text{Turn\_on}}) - 18.16}$$

## Setting Under Voltage Lock Out – 20 A Models

Default Turn-on voltage: 4.3 V (300 mV Hysteresis)

The Turn-on voltage may be adjusted with a resistor placed between the "Enable" and "Ground" pins.

The formula for calculating the value of this resistor is:

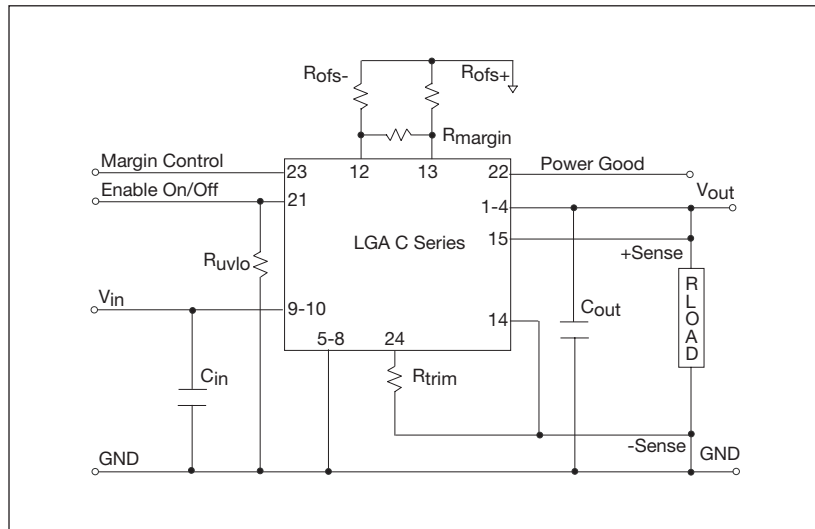
$$R_{\text{uvlo}} \text{ (k}\Omega\text{)} = \frac{30.1 * 4.22}{(8.577 * V_{\text{Turn\_on}}) - 34.32}$$

**\*ONLY USE WITH OPEN COLLECTOR DEVICE**  
**\*DO NOT DRIVE PIN WITH A VOLTAGE**

**\*ONLY USE WITH OPEN COLLECTOR DEVICE**  
**\*DO NOT DRIVE PIN WITH A VOLTAGE**

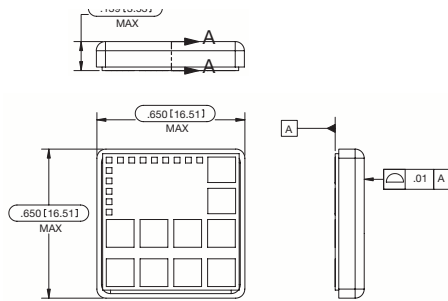
## Notes:

1. Measured as per recommended minimum system capacitance.
2.  $di/dt = 10 \text{ A}/\mu\text{s}$ ,  $12 \text{ Vin} = \text{Norm}$ ,  $T_c = 25^\circ\text{C}$ , load change = 50% to 100%  $I_{\text{max}}$ .
3. Internal input capacitance is rated 16 Vdc maximum.

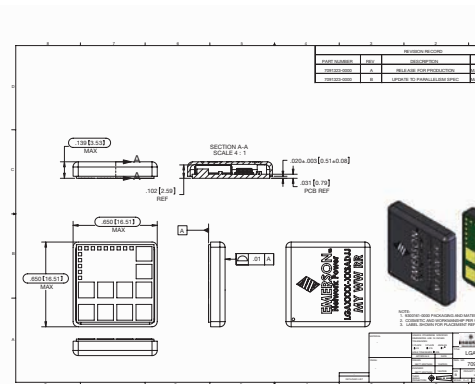


External input fusing is recommended.

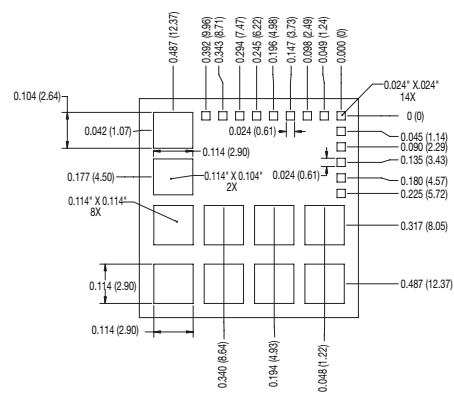
## MECHANICAL DRAWING AND FOOTPRINT



Recommended System Board Footprint

Tolerance Note:  $\pm 0.010$  (0.25)

Recommended Solder Paste Stencil



## Component Height

Model #	DIM A in (mm)
LGA03	0.129 (3.27)
LGA06	
LGA10	
LGA20	0.210 (5.33)

## Pin Assignments

Single Output	
1	Vout
2	Vout
3	Vout
4	Vout
5	GND
6	GND
7	GND
8	GND
9	Vin
10	Vin
11	NC
12	- Offset
13	+ Offset
14	- Sense
15	+ Sense
16	NC
17	NC
18	NC
19	NC
20	NC
21	Enable
22	Power Good
23	Margin Control
23	Trim



For international contact information,  
visit [advancedenergy.com](https://advancedenergy.com).

[powersales@aei.com](mailto:powersales@aei.com) (Sales Support)  
[productsupport.ep@aei.com](mailto:productsupport.ep@aei.com) (Technical Support)  
+1 888 412 7832

## ABOUT ADVANCED ENERGY

Advanced Energy (AE) has devoted more than three decades to perfecting power for its global customers. AE designs and manufactures highly engineered, precision power conversion, measurement and control solutions for mission-critical applications and processes.

Our products enable customer innovation in complex applications for a wide range of industries including semiconductor equipment, industrial, manufacturing, telecommunications, data center computing, and medical. With deep applications know-how and responsive service and support across the globe, we build collaborative partnerships to meet rapid technological developments, propel growth for our customers, and innovate the future of power.

## PRECISION | POWER | PERFORMANCE

---

Specifications are subject to change without notice. Not responsible for errors or omissions. ©2021 Advanced Energy Industries, Inc. All rights reserved. Advanced Energy®, AE® and Artesyn™ are U.S. trademarks of Advanced Energy Industries, Inc.

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[Advanced Energy:](#)

[LGA03C-00SADJJ](#) [LGA20C-01SADJJ](#) [LGA10C-00SADJJ](#) [LGA06C-00SADJJ](#) [LGA-HTSK-KIT-048](#) [LGA-HTSK-KIT-050](#) [LGA-HTSK-KIT-045](#) [LGA10C-EVAL-KIT](#) [LGA20C-EVAL-KIT](#)