

Super-Junction Power MOSFET

Rev.01 - 20 February 2024

Product data sheet

1. General description

WSJM65R170B is a high voltage N-channel MOSFET in TO263 package, which utilizes the advanced super-junction technology to provide superior FOM $R_{DS(on)} * Q_g$ among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.



2. Features and benefits

- Superior FOM R_{DS(on)} * Q_g
- Extremely low switching loss
- 100% avalanche tested

3. Applications

high efficiency power supplies

4. Quick reference data

Table 1. Qu	lick reference data						
Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute	maximum rating						
V _{DS}	drain-source voltage				650		
V _{GS}	gate-source voltage				±30		V
I _D	continuous drain current	T _{mb} = 25 °C			23		А
P _{tot}	power dissipation	T _{mb} = 25 °C		240		W	
Tj	junction temperature			-55 to 150		°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics	·					
$R_{\text{DS(on)}}$	drain-source on-state resistance	V _{GS} = 10 V, I _D = 11 A		-	156	170	mΩ
Dynamic of	characteristics	·					
Q _{G(tot)}	total gate charge	I_{D} = 11 A; V_{DS} = 400 V; V_{GS} = 10 V		-	38	-	nC
E _{oss}	coss stored erergy	$V_{GS} = 0 V; V_{DS} = 0 \text{ to } 400 V$		-	5.1	-	μJ

5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	D	drain		\square
3	S	source	0	
mb	D	mounting base; connected to drain		svm300 S

6. Ordering information

Table 3. Ordering information								
Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date		
WSJM65R170B	TO263	WSJM65R170BJ	Reel	800	TO263d	17-Mar-2023		

7. Marking

Table 4. Marking codes						
Type number	Marking codes					
WSJM65R170B	WSJM 65R170B					

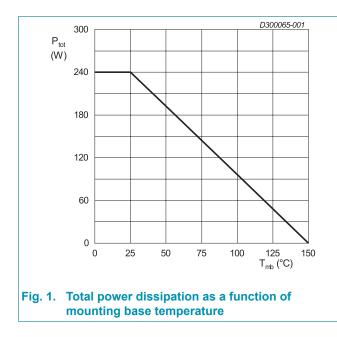
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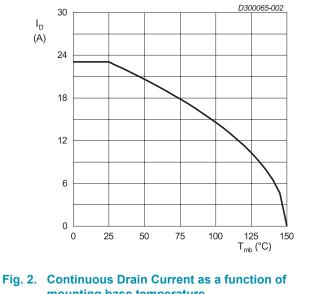
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V _{DS}	drain-source voltage			650	V
V _{GS}	gate-source voltage			±30	V
I _D	continuous drain current	T _{mb} = 25 °C		23	А
		T _{mb} = 100 °C		14	А
I _{DM}	pulsed drain current	T _{mb} = 25 °C		72	А
P _{tot}	power dissipation	T _{mb} = 25 °C		240	W
E _{AS}	single pulse drain-to- source avalanche	I_{AS} = 6.9 A; R _{GS} = 25 Ω; V _{DD} = 50 V; T _j = 25 °C		238	mJ
E _{AR}	repetitive avalanche energy	I_{AS} = 6.9 A; R _{GS} = 25 Ω; V _{DD} = 50 V; T _j = 25 °C		1.67	mJ
I _{AS}	avalanche current, single pulse			6.9	A
dv/dt	MOSFET dv/dt ruggedness			50	V/ns
dv/dt	reverse diode dv/dt			15	V/ns
dI _F /dt	maximum diode commutation speed			500	A/µs
T _{stg}	storage temperature			-55 to 150	°C
Tj	junction temperature			-55 to 150	°C

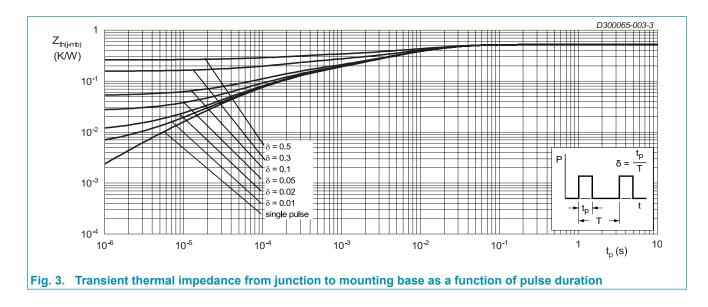




9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	0.45	0.52	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		-	60	-	K/W



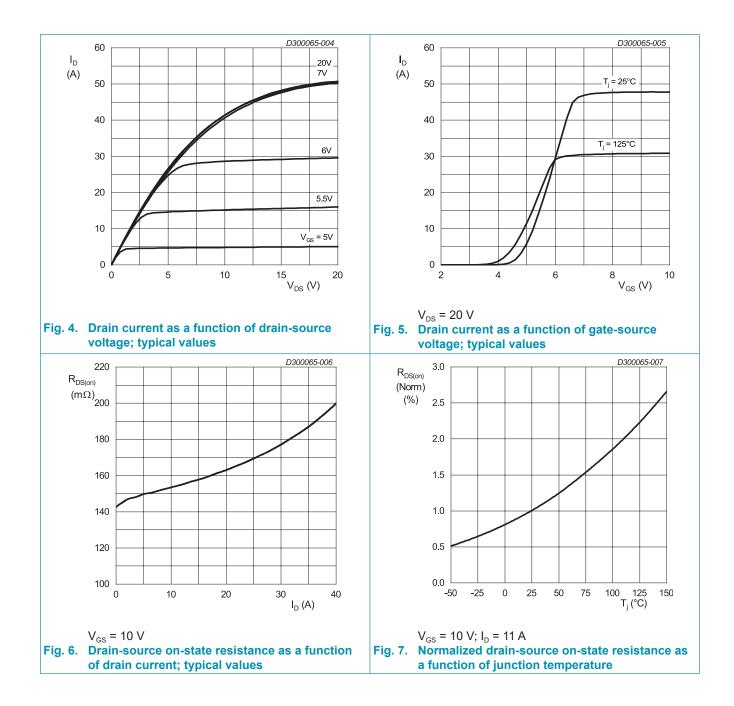
10. Characteristics

Table 7. Characteristics

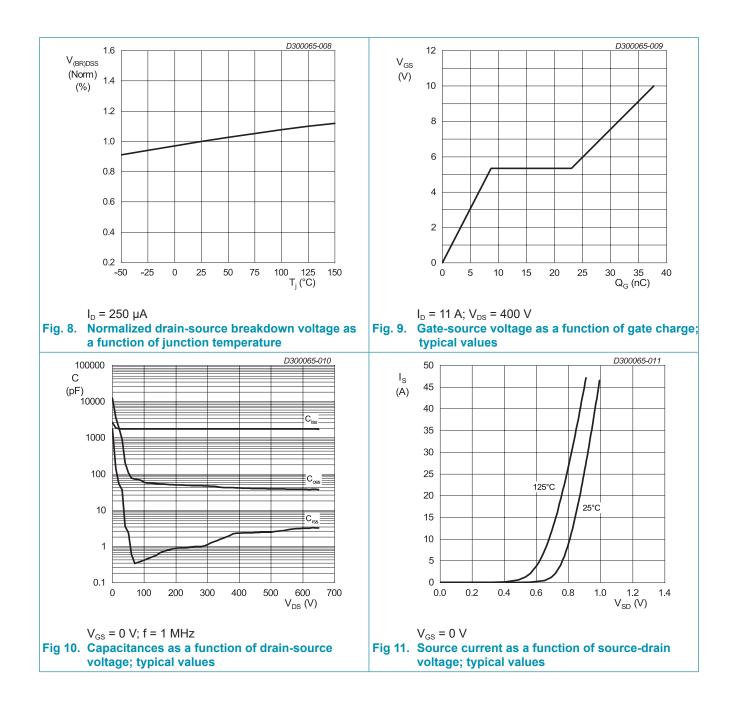
T_j = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_{\rm D}$ = 250 µA; $V_{\rm GS}$ = 0 V		650	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	$I_{\rm D}$ = 250 µA; $V_{\rm DS}$ = $V_{\rm GS}$		2.5	-	4.5	V
I _{DSS}	drain leakage current	$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}$		-	-	1	μA
		$V_{DS} = 650 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 \text{ °C}$		-	-	10	μA
I _{GSS}	gate leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$		-	-	±100	nA
$R_{\text{DS(on)}}$	drain-source on-state resistance	V _{GS} = 10 V; I _D = 11 A		-	156	170	mΩ
R _G	gate resistance	f = 1 MHz		-	12	-	Ω
Dynamic	characteristics						
Q _{G(tot)}	total gate charge	I_{D} = 11 A; V_{DS} = 400 V; V_{GS} = 10 V		-	38	-	nC
Q _{GS}	gate-source charge			-	8.7	-	nC
Q _{GD}	gate-drain charge			-	14	-	nC
C _{iss}	input capacitance	V _{DS} = 400 V; V _{GS} = 0 V; f = 1 MHz		-	1751	-	pF
C _{oss}	output capacitance			-	41	-	pF
C _{rss}	reverse transfer capacitance			-	2.3	-	pF
C _{o(er)}	effective output capacitance, energy related	V_{GS} = 0 V; V_{DS} = 0 to 400 V		-	64	-	pF
C _{o(tr)}	effective output capacitance, time related			-	370	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 400 V; V_{GS} = 10 V; R_{G} = 2 Ω ;		-	21	-	ns
t _r	rise time	I _D = 11 A		-	21	-	ns
t _{d(off)}	turn-off delay time			-	72	-	ns
t _f	fall time			-	11	-	ns
Source-d	rain diode						
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 11 A		-	0.8	1.1	V
l _s	body-diode continuous current	T _{mb} = 25 °C		-	-	23	A
t _{rr}	reverse recovery time	V_{R} = 400 V; I _F = 11 A; dI _F /dt = 100 A/µs		-	285	-	ns
Q _{rr}	reverse recovered charge			-	3.8	-	μC
l _{rrm}	reverse recovery current			-	26	-	Α

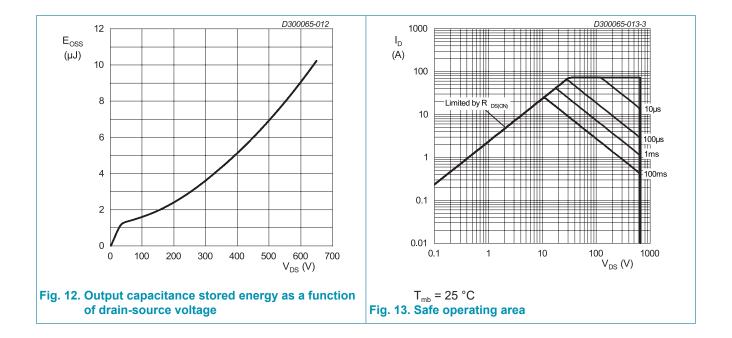
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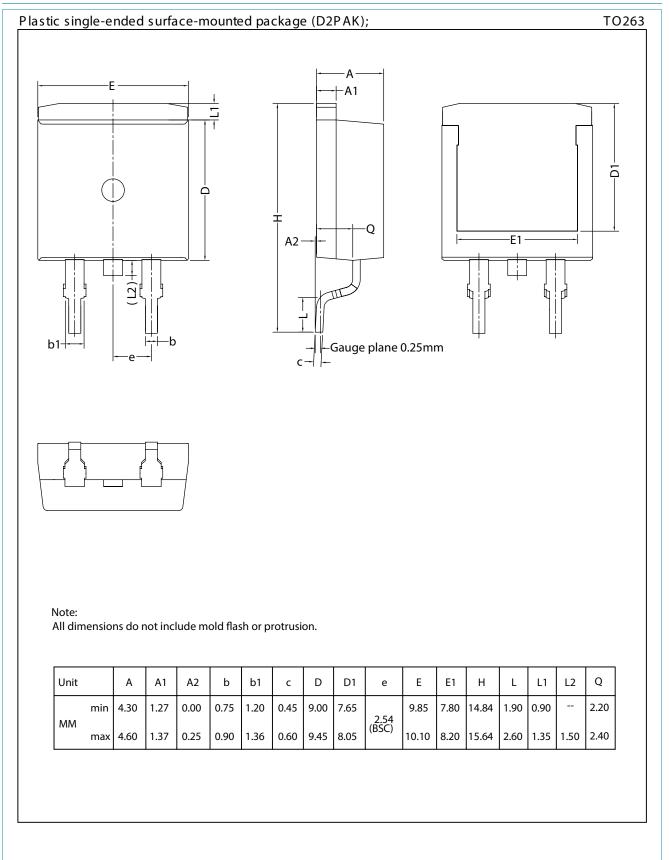
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11. Package outline



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12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

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