

1. General description

Planar passivated Silicon Controlled Rectifier (SCR) in a TO220 package intended for use in applications requiring good bidirectional blocking voltage and high surge current capability and high junction temperature capability ($T_{j(max)} = 150\text{ °C}$).

2. Features and benefits

- High junction operating temperature capability ($T_{j(max)} = 150\text{ °C}$)
- High bidirectional blocking voltage capability
- Very high current surge capability
- High thermal cycling performance
- Planar passivated for voltage ruggedness and reliability

3. Applications

- Capacitive Discharge Ignition (CDI)
- Crowbar protection
- Inrush protection
- Motor control
- Voltage regulation

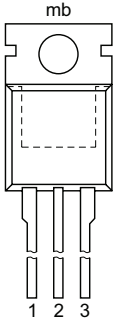

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	600	V
$I_{T(RMS)}$	RMS on-state current	half sine wave; $T_{mb} \leq 134\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	-	16	A
I_{TSM}	non-repetitive peak on-state current	half sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 10\text{ ms}$; Fig. 4 ; Fig. 5	-	-	188	A
		half sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 8.3\text{ ms}$	-	-	207	A
T_j	junction temperature		-	-	150	°C
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 25\text{ °C}$; Fig. 7	5	-	10	mA
I_H	holding current	$V_D = 12\text{ V}$; $T_j = 25\text{ °C}$; Fig. 9	-	-	40	mA
V_T	on-state voltage	$I_T = 16\text{ A}$; $T_j = 25\text{ °C}$; Fig. 10	-	-	1.6	V
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 402\text{ V}$; $T_j = 150\text{ °C}$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit	400	-	-	V/ μ s

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		
2	A	anode		
3	G	gate		
mb	A	mounting base; connected to anode		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
TYN16-600CTF	TO220	TYN16-600CTFQ	Tube	50	SOT78	13-Jun-2008

7. Marking

Table 4. Marking codes

Type number	Marking codes
TYN16-600CTF	TYN16 600CTF

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage			-	600	V
V_{RRM}	repetitive peak reverse voltage			-	600	V
$I_{\text{T(AV)}}$	average on-state current	half sine wave; $T_{\text{mb}} \leq 134\text{ }^{\circ}\text{C}$;		-	10.2	A
$I_{\text{T(RMS)}}$	RMS on-state current	half sine wave; $T_{\text{mb}} \leq 134\text{ }^{\circ}\text{C}$; Fig. 1 ; Fig. 2 ; Fig. 3		-	16	A
I_{TSM}	non-repetitive peak on-state current	half sine wave; $T_{\text{j(init)}} = 25\text{ }^{\circ}\text{C}$; $t_{\text{p}} = 10\text{ ms}$; Fig. 4 ; Fig. 5		-	188	A
		half sine wave; $T_{\text{j(init)}} = 25\text{ }^{\circ}\text{C}$; $t_{\text{p}} = 8.3\text{ ms}$		-	207	A
I^2t	I^2t for fusing	$t_{\text{p}} = 10\text{ ms}$; sine-wave pulse		-	177	A^2s
di_{T}/dt	rate of rise of on-state current	$I_{\text{G}} = 20\text{ mA}$		-	100	$\text{A}/\mu\text{s}$
I_{GM}	peak gate current			-	4	A
V_{GM}	peak gate voltage			-	5	V
P_{GM}	peak gate power			-	10	W
$P_{\text{G(AV)}}$	average gate power	over any 20 ms period		-	1	W
T_{stg}	storage temperature			-40	150	$^{\circ}\text{C}$
T_{j}	junction temperature			-	150	$^{\circ}\text{C}$

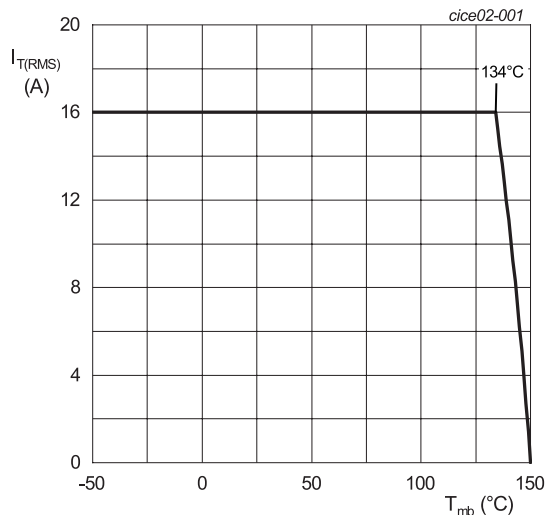
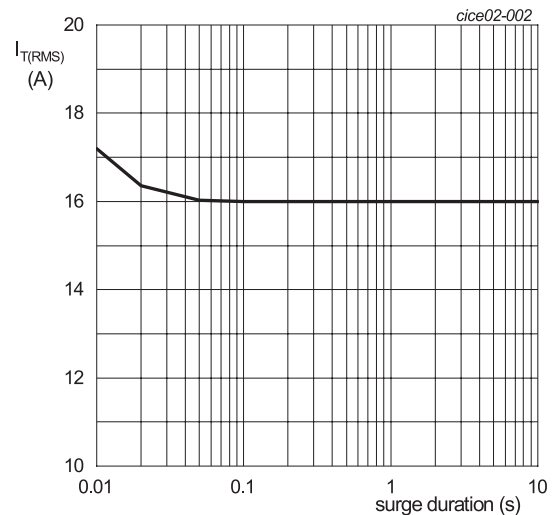


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values



$f = 50\text{ Hz}$; $T_{\text{mb}} = 134\text{ }^{\circ}\text{C}$

Fig. 2. RMS on-state current as a function of surge duration; maximum values

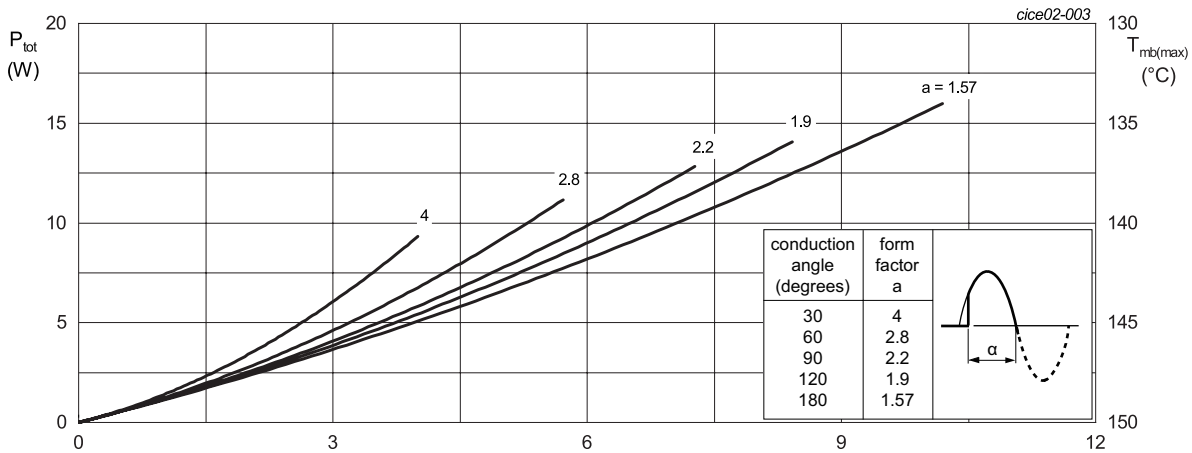


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

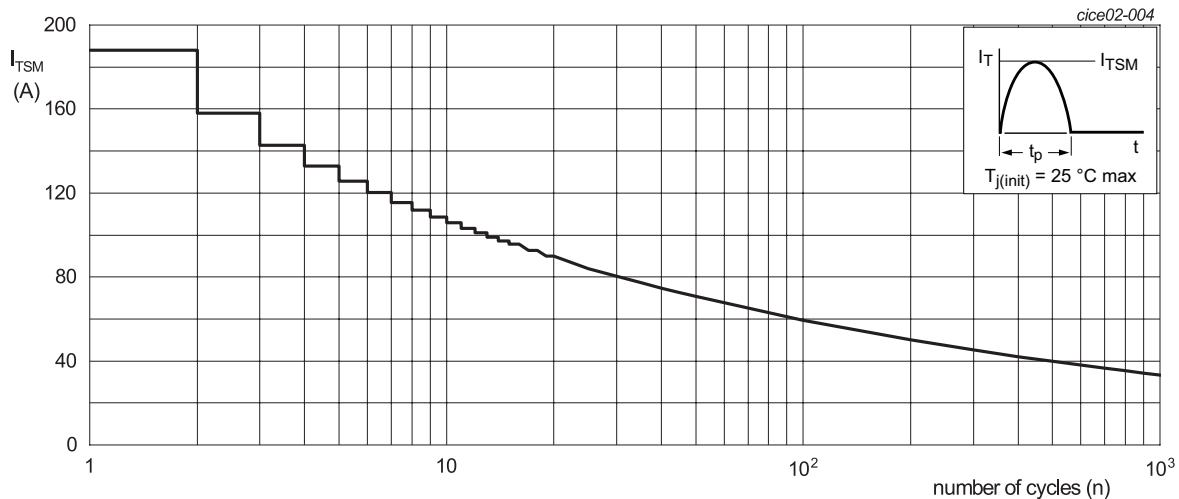


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

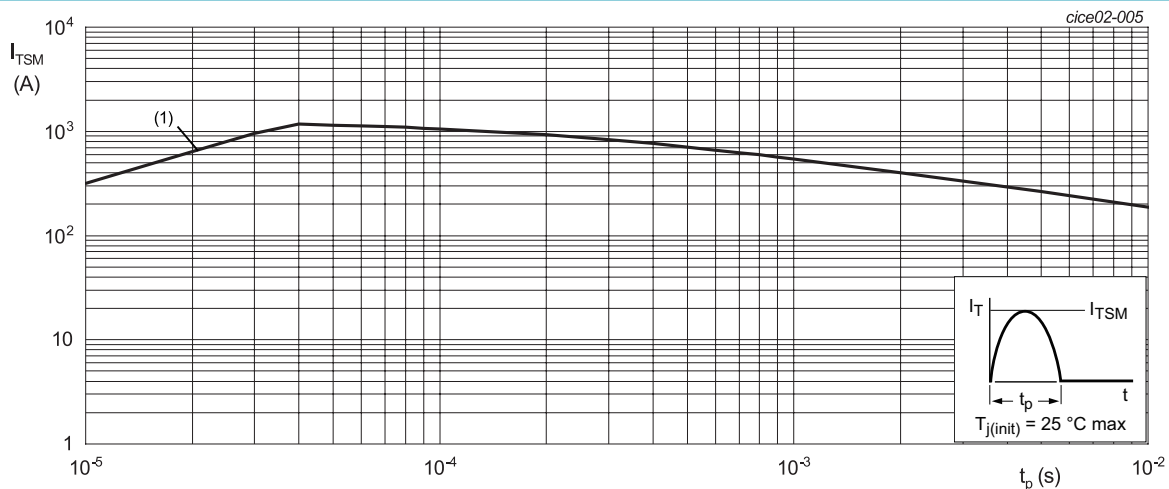
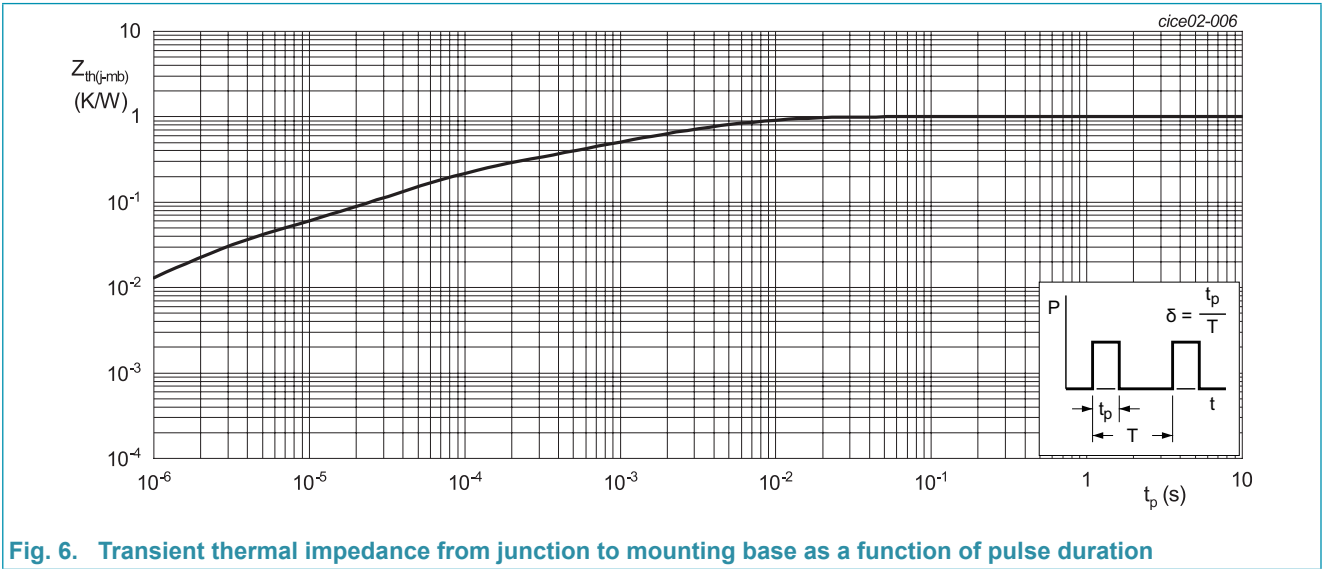


Fig. 5. Non-repetitive peak on-state current as a function of pulse duration; maximum values

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 6		-	-	1	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	60	-	K/W



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T _J = 25 °C; Fig. 7		5	-	10	mA
I _L	latching current	V _D = 12 V; I _G = 0.1 A; T _J = 25 °C; Fig. 8		-	-	60	mA
I _H	holding current	V _D = 12 V; T _J = 25 °C; Fig. 9		-	-	40	mA
V _T	on-state voltage	I _T = 16 A; T _J = 25 °C; Fig. 10		-	-	1.6	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _J = 25 °C; Fig. 11		-	0.8	1	V
		V _D = 400 V; I _T = 0.1 A; T _J = 125 °C		0.25	0.45	-	V
V _{GR}	gate reverse voltage	I _{RG} = 100 mA		10	-	-	V
I _D	off-state current	V _D = 600 V; T _J = 25 °C		-	-	10	μA
		V _D = 600 V; T _J = 150 °C		-	-	2	mA
I _R	reverse current	V _D = 600 V; T _J = 25 °C		-	-	10	μA
		V _D = 600 V; T _J = 150 °C		-	-	2	mA
Dynamic characteristics							
dV _D /dt	rate of rise of off-state voltage	V _{DM} = 402 V; T _J = 150 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit		400	-	-	V/μs
t _{gt}	gate-controlled turn-on time	I _{TM} = 16 A; V _D = 600 V; I _G = 20 mA; dI _G /dt = 5 A/μs; T _J = 25 °C		-	2	-	μs
t _q	commutated turn-off time	I _{TM} = 2 A; t _p = 50 μs; dV/dt = 5 V/μs; dI/dt = 30 A/μs		-	-	12	μs

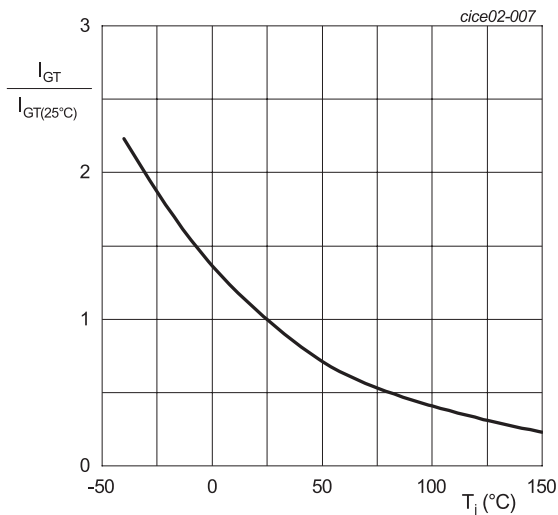


Fig. 7. Normalized gate trigger current as a function of junction temperature

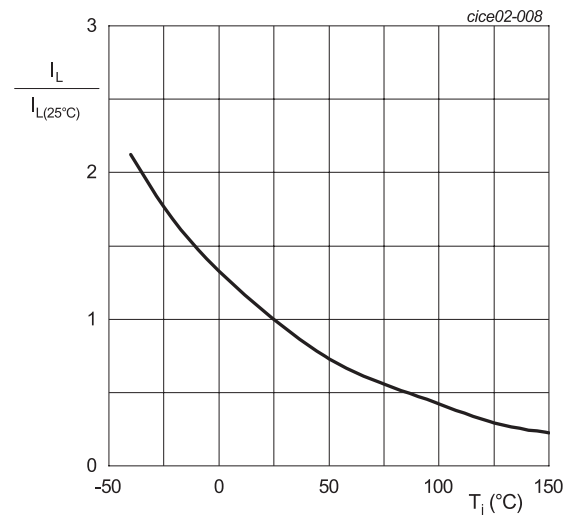


Fig. 8. Normalized latching current as a function of junction temperature

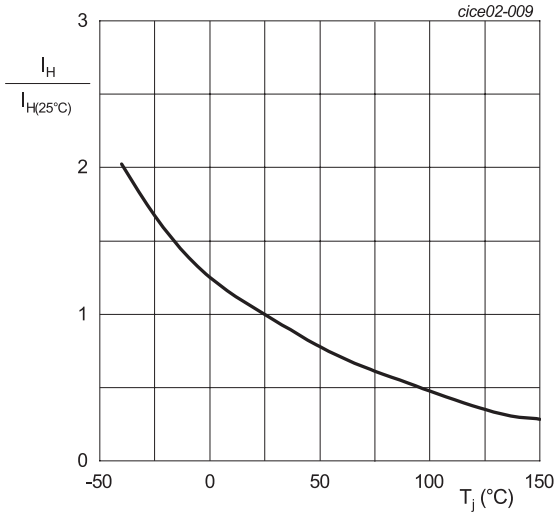
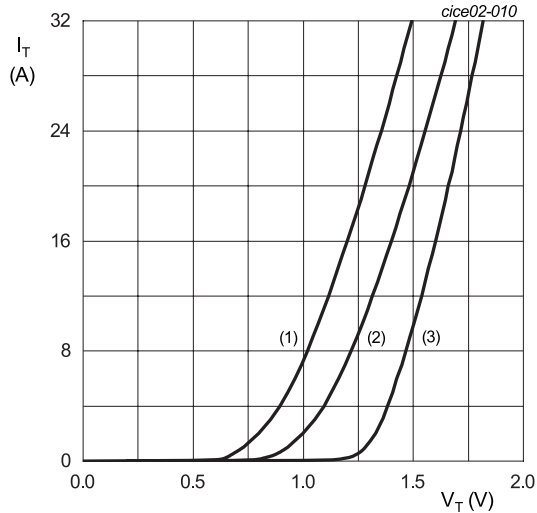


Fig. 9. Normalized holding current as a function of junction temperature



$V_o = 1.075\text{ V}; R_s = 0.0196\ \Omega$
(1) $T_j = 150^\circ\text{C}$; typical values
(2) $T_j = 150^\circ\text{C}$; maximum values
(3) $T_j = 25^\circ\text{C}$; maximum values

Fig. 10. On-state current as a function of on-state voltage

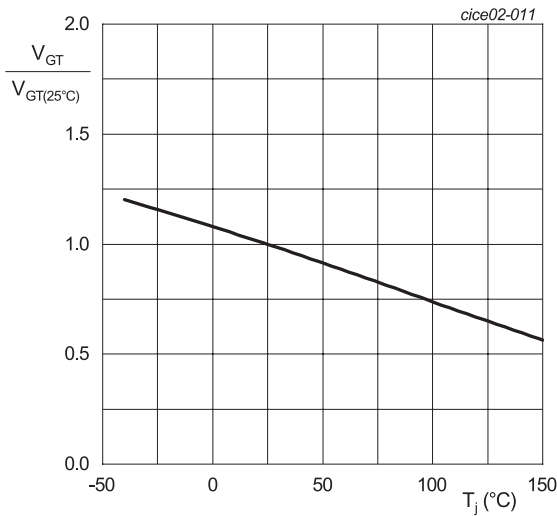
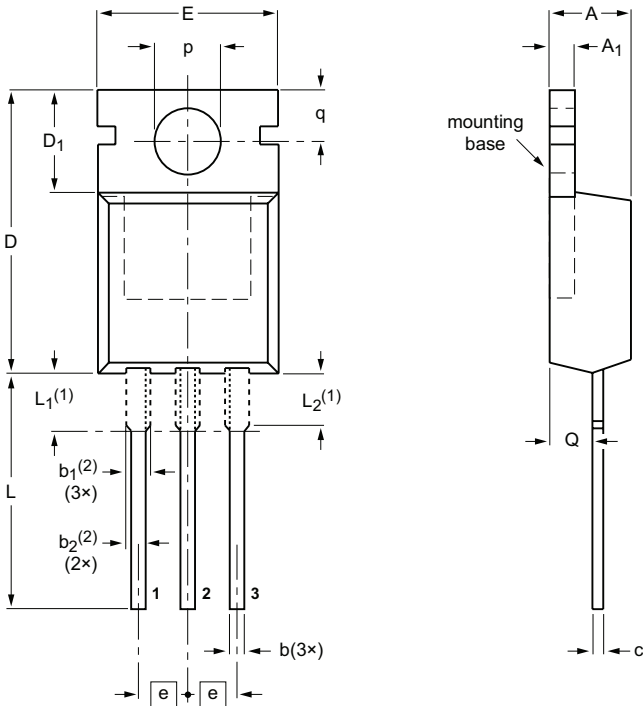


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

11. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78




DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁	b	b ₁ (2)	b ₂ (2)	c	D	D ₁	E	e	L	L ₁ (1)	L ₂ (1) max.	p	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

Notes

- Lead shoulder designs may vary.
- Dimension includes excess dambar.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT78		3-lead TO-220AB	SC-46			08-04-23 08-06-13

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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13. Contents

1. General description.....	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values	3
9. Thermal characteristics	5
10. Characteristics.....	6
11. Package outline	8
12. Legal information	9
13. Contents	11

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