

1. General description

The ESDALD05UG4 is a low capacitance TVS (Transient Voltage Suppressor) array designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from over-stress caused by ESD (Electrostatic Discharge).

2. Features and benefits

- Peak pulse power 60W @ 8/20µs waveform
- Protects two or four I/O lines
- IEC 61000-4-2 (ESD) ±15kV(air), ±8kV(contact)
- IEC 61000-4-5 (Lightning) 4A (8/20µs)
- Low capacitance
- Low leakage current
- 5V operating voltage
- Solid-state silicon avalanche technology
- Device meets MSL 1 requirements
- Halogen free and RoHS compliant

3. Applications

- High Definition Multi-Media Interface (HDMI)
- Digital Visual Interface (DVI)
- USB 1.1/2.0/OTG
- IEEE 1394 Firewire Ports
- Notebooks & Handhelds
- Projection TV & Monitors
- Set-top box
- Flat Panel Displays
- PCI Express

4. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Marking	Package issue date
ESDALD05UG4	DFN2510	ESDALD05UG4X	Tape and reel	3000	0524P	13-Oct-2020

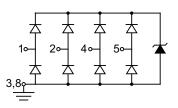
5. Absolute maximum ratings

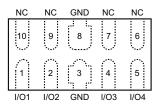
In accordance with the Absolute Maximum Rating System (IEC 60134).

 $T_j = 25 \ ^{\circ}C \ unless \ otherwise \ specified.$

Symbol	Parameter	Conditions	Values	Unit	
Absolute maximum rating					
P _{PPM}	peak pulse power	t _p = 8/20 μs	60	W	
I _{PP}	peak pulse current	t _p = 8/20 μs	4	А	
V_{ESD}	ESD per IEC 61000-4-2 (air) ESD per IEC 61000-4-2 (contact)		±15 ±8	kV kV	
T _{stg}	storage temperature range		-55 to 150	°C	
T _j	operating temperature range		-55 to 150	°C	







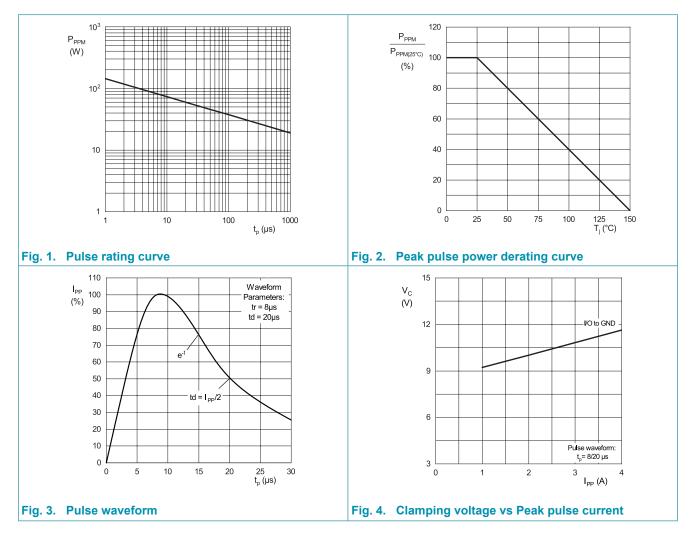


6. Characteristics

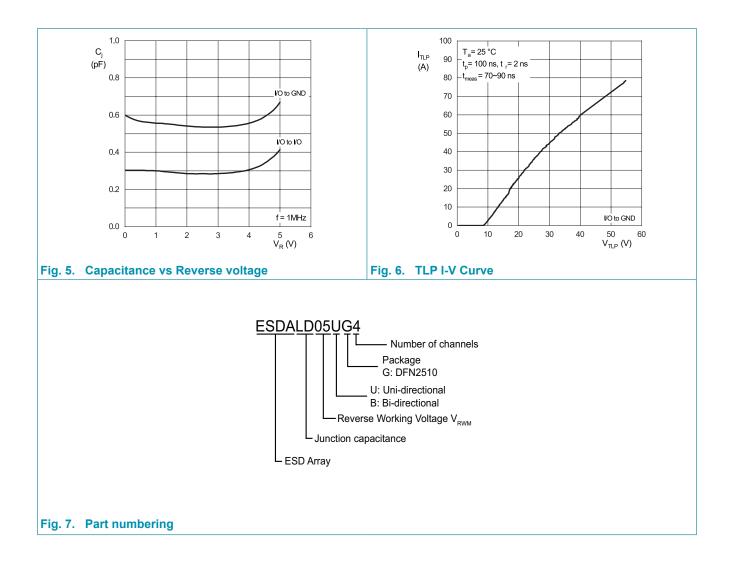
T_j = 25 °C unless otherwise specified.

Symbol	Parameter	Condition	Min	Тур	Max	Unit
V _{RWM}	Reverse Working Voltage	Any I/O pin to GND	-	-	5	V
$V_{\rm BR}$	Reverse Breakdown Voltage	$I_T = 1 \text{ mA}; \text{ Any I/O pin to GND}$	6	-	-	V
I _R	Reverse Leakage Current	V _{RWM} = 5 V; Any I/O pin to GND	-	-	100	nA
V _F	Diode Forward Voltage	I _F = 15 mA	-	0.85	1.2	V
V _c	Clamping Voltage	$I_{PP} = 4 \text{ A}; t_p = 8/20 \mu\text{s};$ Any I/O pin to GND	-	-	15	V
CJ	Junction Capacitance	V _R = 0 V; f = 1 MHz; Between I/O pins	-	0.3	0.5	pF
		$V_R = 0$ V; f = 1 MHz; Any I/O pin to GND	-	0.6	1	pF

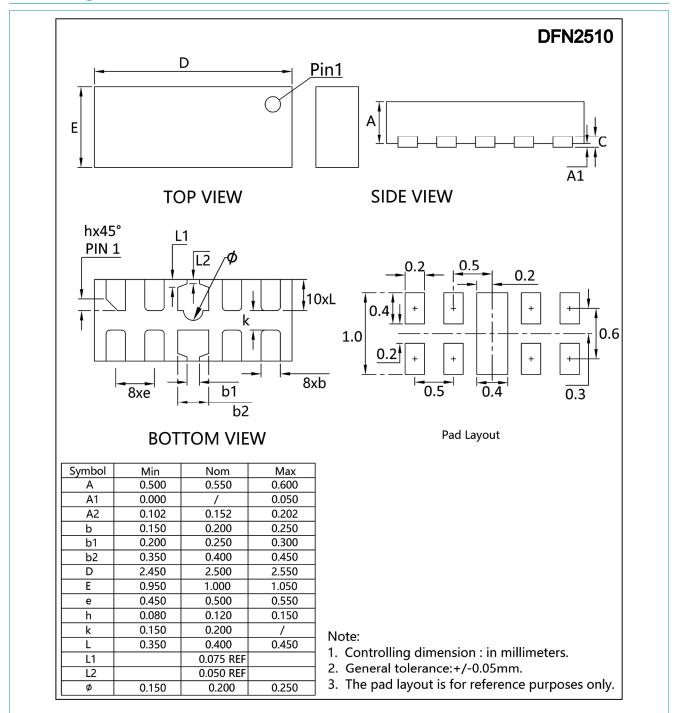
Note: I/O pins are pin 1,2,4,5



ESD Protection Diodes Array



7. Package outline



ESDALD05UG4

ESD Protection Diodes Array

8. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.ween-semi.com</u>.

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