

### 1. General description

Planar passivated high commutation three quadrant triac in a SOT78 plastic package intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. This "series C" triac will commutate the full rated RMS current at the maximum rated junction temperature without the aid of a snubber.

### 2. Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High immunity to false turn-on by dV/dt
- High voltage capability
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only

### 3. Applications

- Electronic thermostats (heating and cooling)
- High power motor controls e.g. washing machines and vacuum cleaners
- Rectifier-fed DC inductive loads e.g. DC motors and solenoids

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>DRM</sub>	repetitive peak off- state voltage		-	-	800	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 101 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	-	16	A
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 20 \text{ ms}; \text{ Fig. 4}; \text{ Fig. 5}$	-	-	140	A
		full sine wave; $T_{j(init)} = 25 \text{ °C};$ t <sub>p</sub> = 16.7 ms	-	-	150	A
Tj	junction temperature		-	-	125	°C
Static chara	acteristics		· ·			
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	-	35	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 18 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.3	1.5	V
Dynamic cha	racteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 536 V; T <sub>j</sub> = 125 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	500	-	-	V/µs
dl <sub>com</sub> /dt	rate of change of commutating current	$V_D$ = 400 V; T <sub>j</sub> = 125 °C; I <sub>T(RMS)</sub> = 16 A; dV <sub>com</sub> /dt = 20 V/µs; (snubberless condition); gate open circuit	15	-	-	A/ms

## 5. Pinning information

Table 2.	Pinning in	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2-71
2	T2	main terminal 2		G sym051
3	G	gate		Symoon
mb	Τ2	mounting base; main terminal 2		
			TO-220AB (SOT78)	

## 6. Ordering information

Table 3. Ordering information							
Type number	Package	age					
	Name	Description	Version				
BTA316-800C	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78				

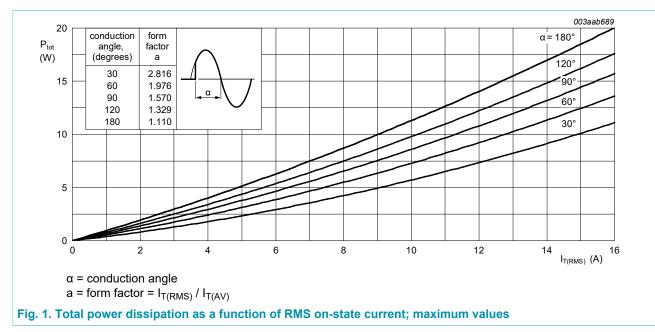
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### 7. Limiting values

#### Table 4. Limiting values

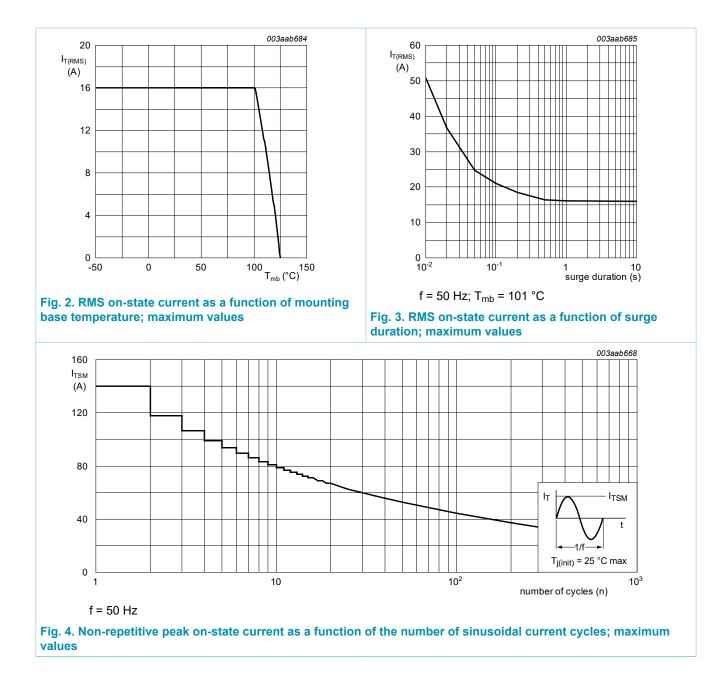
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	800	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 101 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	16	A
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 20 ms; Fig. 4; Fig. 5	-	140	A
		full sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 16.7 ms	-	150	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	-	98	A²s
dl <sub>T</sub> /dt	rate of rise of on-state current	I <sub>G</sub> = 0.2 A	-	100	A/µs
I <sub>GM</sub>	peak gate current		-	2	А
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C



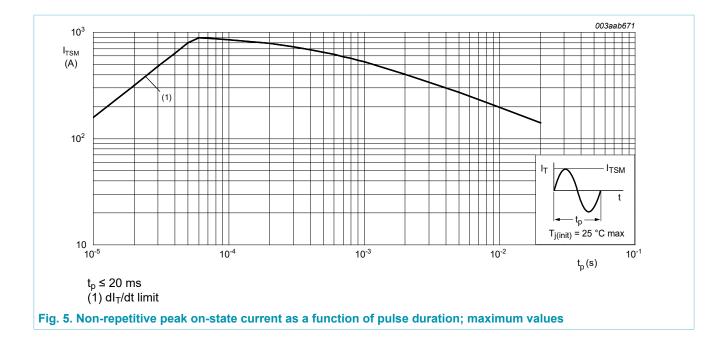
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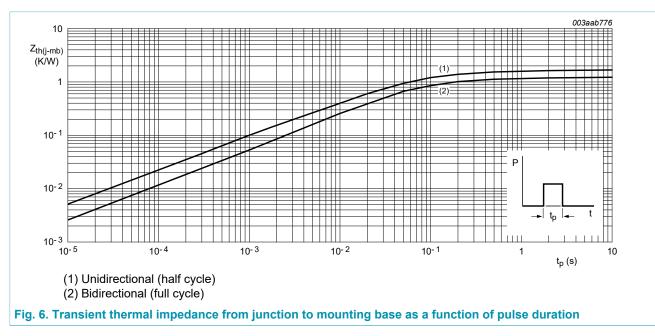
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### 8. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	full cycle; <u>Fig. 6</u>	-	-	1.2	K/W
		half cycle; <u>Fig. 6</u>	-	-	1.7	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	in free air	-	60	-	K/W



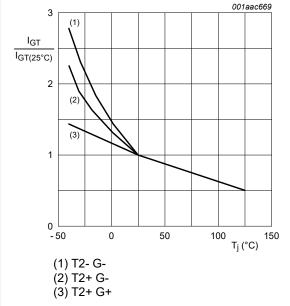
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### 9. Characteristics

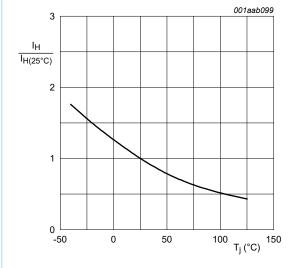
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		· · · · · ·			
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
ΙL	latching current	V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	-	50	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	-	60	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	-	50	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	-	35	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 18 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.3	1.5	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	0.8	1	V
		V <sub>D</sub> = 400 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 125 °C; <u>Fig. 11</u>	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 800 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mA
Dynamic ch	naracteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 536 V; T <sub>j</sub> = 125 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	500	-	-	V/µs
dl <sub>com</sub> /dt	rate of change of commutating current	$V_D$ = 400 V; $T_j$ = 125 °C; $I_{T(RMS)}$ = 16 A; dV <sub>com</sub> /dt = 20 V/µs; (snubberless condition); gate open circuit	15	-	-	A/ms

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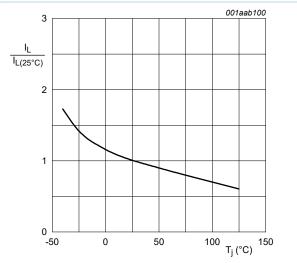
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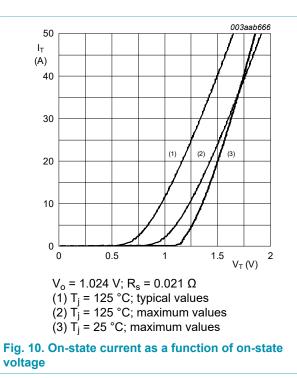






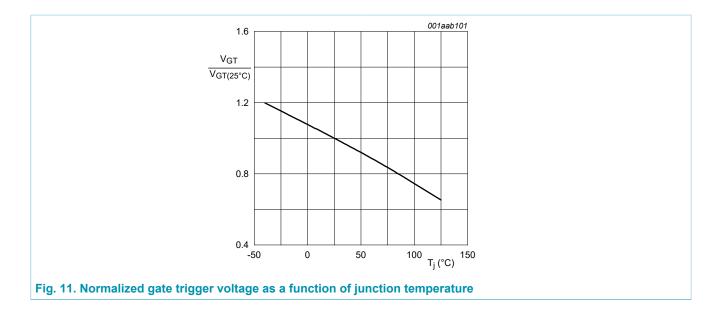






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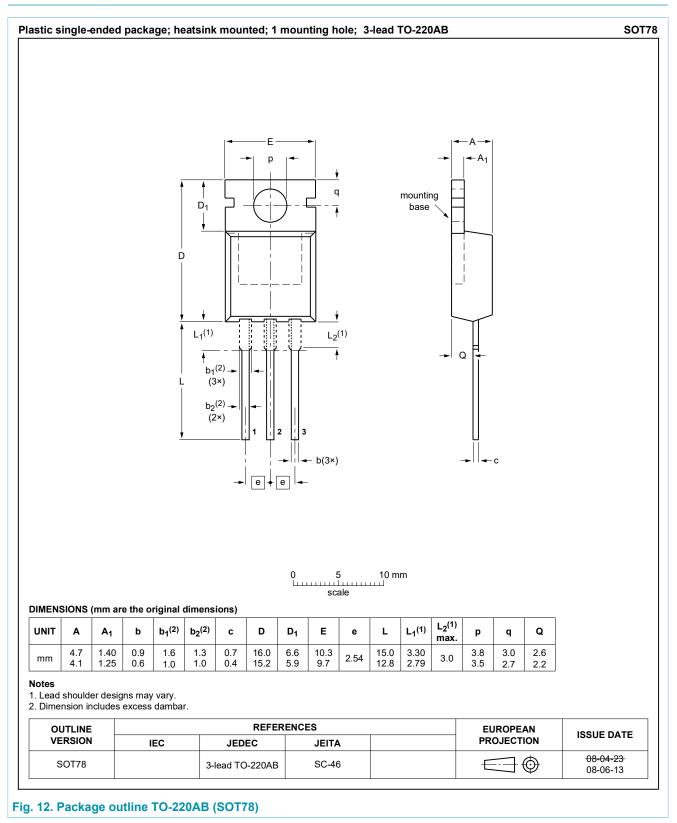
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### 10. Package outline



#### **3Q Hi-Com Triac**

### 11. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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