

# **GRF5010**

28.5 dBm Power-LNA™ Tuning Range: 0.05 – 6.0 GHz



#### **Features**

Reference: 8V/95mA/2.5GHz

Gain: 17.0 dBNF: 0.85 dB

OP1dB: 28.5 dBmOIP3: 45.0 dBm

Reference: 5V/60mA/2.5GHz

Gain: 17.0 dBNF: 0.82 dB

OP1dB: 24.5 dBmOIP3: 38.5 dBm

Flexible Bias Voltage and Current

Process: GaAs pHEMT

#### Applications

Multi-stage LNA

 Linear Driver Amplifier for High PAR Waveforms

Distributed Antenna Systems

Microwave Backhaul

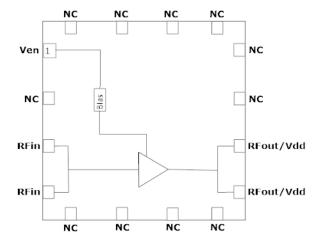
Revision Date: 10/01/19

#### **Product Description**

GRF5010 is a high linearity PA with ultra-low noise figure (NF). The primary tune for this device covers 1.7 to 3.8 GHz and it achieves outstanding P1dB, IP3 and NF over the band. The device can be tuned to deliver outstanding performance over 0.1 GHz. to 6.0 GHz with fractional bandwidths >30%. With an 8.0 Volt supply, the device can deliver broadband OP1dB values greater than 28.0 dBm.

In addition to use as a PA or linear driver, GRF5010 is well suited to demanding first, second or third stage LNA applications requiring high linearity, ruggedness and low NF.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device sparameters.



3.0 x 3.0 mm QFN-16



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### **Absolute Ratings:**

| Parameter   | Symbol                | Min. | Max. | Unit |
|---|-----------------------|------|------|------|
| Drain Voltage   | V <sub>DD</sub>       |      | 10.0 | V    |
| Transient Average RF Input Power: (Load VSWR < 2:1; Duration: <1 hour)                            | P <sub>IN MAX</sub>   |      | 22.0 | dBm  |
| Average RF Output Power: (Load VSWR < 2:1;<br>V <sub>D</sub> : > 8.0 volts; Duration: Continuous) | Роит мах              |      | 26.0 | dBm  |
| Average RF Output Power: (Load VSWR < 2:1; V <sub>D</sub> : <= 8.0 volts; Duration: Continuous)   | Роцт мах              |      | NA   | dBm  |
| Operating Temperature (Package Heat Sink)   | T <sub>AMB</sub>      | -40  | 105  | °C   |
| Maximum Channel Temperature (MTTF > 10^6 Hours)   | Тмах                  |      | 170  | °C   |
| Maximum Dissipated Power  | P <sub>DISS MAX</sub> |      | 1.5  | W    |
| Electrostatic Discharge:  |                       |      |      |      |
| Charged Device Model:   | CDM                   | 1000 |      | V    |
| Human Body Model:   | HBM                   | 250  |      | V    |
| Storage:  |                       |      |      |      |
| Storage Temperature   | T <sub>STG</sub>      | -65  | 150  | °C   |
| Moisture Sensitivity Level  | MSL                   |      | 1    | -    |



Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF5010 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

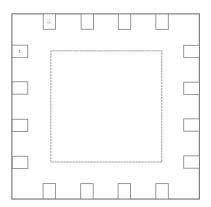
Link to manufacturing note



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### Pin Out (Top View)



### Pin Assignments:

| Pin      | Name       | Description          | Note  |
|----------|------------|----------------------|---|
| 1        | VENABLE    | Enable Voltage Input | Venable and series resistor set IdDQ. Venable < =0.2 volts disables device.  On-die pull-down resistor will turn the part off if this node is allowed to float.   |
| 2        | NC         | No Connect or Ground | No internal connection to die   |
| 3        | RF_In      | RF Input             | Pins 3-4 tied together on system board  |
| 4        | RF_In      | RF Input             | Pins 3-4 tied together on system board  |
| 5        | NC         | No Connect or Ground | No internal connection to die   |
| 6        | NC         | No Connect or Ground | No internal connection to die   |
| 7        | NC         | No Connect or Ground | No internal connection to die   |
| 8        | NC         | No Connect or Ground | No internal connection to die   |
| 9        | RF_Out/VDD | PA Output/Bias       | Pins 9-10 tied together on system board. Supply Vdd here.   |
| 10       | RF_Out/VDD | PA Output/Bias       | Pins 9-10 tied together on system board. Supply Vdd here.   |
| 11       | NC         | No Connect or Ground | No internal connection to die   |
| 12       | NC         | No Connect or Ground | No internal connection to die   |
| 13       | NC         | No Connect or Ground | No internal connection to die   |
| 14       | NC         | No Connect or Ground | No internal connection to die   |
| 15       | NC         | No Connect or Ground | No internal connection to die   |
| 16       | NC         | No Connect or Ground | No internal connection to die   |
| PKG BASE | GND        | Ground               | Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page. |



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### **Nominal Operating Parameters:**

| Davamatav  | Cyroob ol       | Specification |                   |      | l locit | Condition   |  |
|--|-----------------|---------------|-------------------|------|---------|---|--|
| Parameter  | Symbol          | Min.          | Тур.              | Max. | Unit    | Condition   |  |
| Target Performance (1.7-3.8 GHz Tune)                    |                 |               |                   |      |         | Bias: 8.0 V and 95 mA unless otherwise noted. (+25C)                                      |  |
| Test Frequency   | FTEST           |               | 2.5               |      | GHz     |   |  |
| Gain   | S(2,1)          | 16.0          | 17.0              |      | dB      |   |  |
| Noise Figure (Evaluation Board)                          | NF              |               | 0.85              | 1.05 | dB      |   |  |
| Output 1dB Compression Point                             | OP1dB           | 26.5          | 28.5              |      | dBm     |   |  |
| Output Third Order Intercept Point                       | OIP3            |               | 45.0              |      | dBm     | Tones: 2499 and 2501 MHz at +8.0 dBm per tone   |  |
| Switching Rise Time                                      | Trise           |               | 100               |      | ns      |   |  |
| Switching Fall Time                                      | TFALL           |               | 100               |      | ns      |   |  |
| Quiescent Supply Current                                 | IDDQ            |               | 95                |      | mA      |   |  |
| Disabled Mode  |                 |               |                   |      |         | VDD: 8.0 volts; VENABLE: 0.0 volts  |  |
| Supply Current (Leakage)                                 | I <sub>DD</sub> |               | 350               |      | uA      |   |  |
| Thermal Data   |                 |               |                   |      |         |   |  |
| Thermal Resistance: (IR Scan Method)                     | Θјс             |               | 45                |      | °C/W    |   |  |
| Channel Temperature @ +85C Reference (package heat sink) | TCHANNEL        |               | 119<br>(See note) |      | °C      | V <sub>DD:</sub> 8.0 volts; I <sub>DDQ</sub> : 95 mA<br>P <sub>DISS</sub> : 760 mW; No RF |  |

Note: MTTF >10^6 hours for TCHANNEL < =170 degrees C.

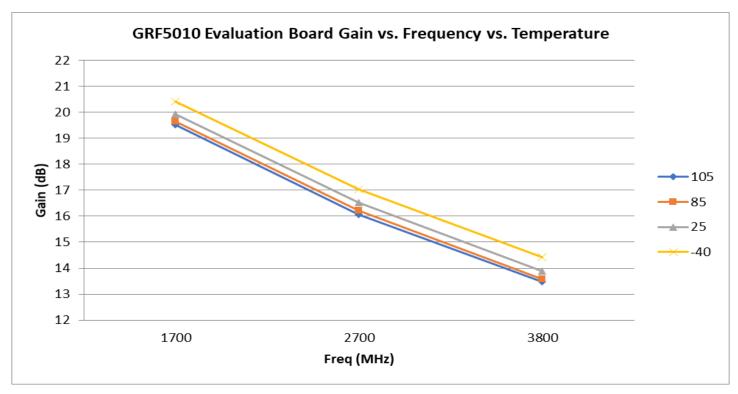


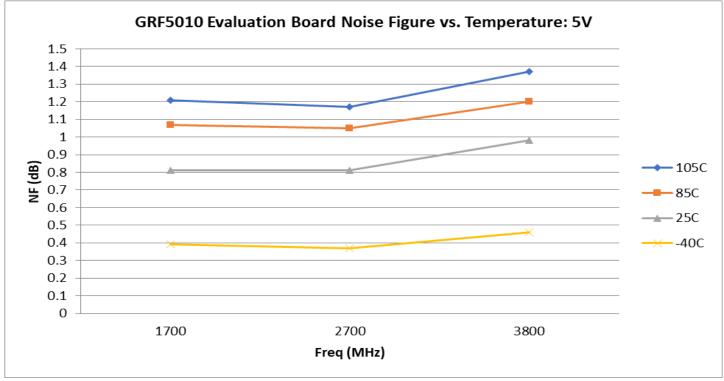
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# **GRF5010**

28.5 dBm Power-LNA™ Tuning Range: 0.05 - 6.0 GHz

GRF5010 Evaluation Board Data; Bias: 5.0V/60mA; (1.7 to 3.8 GHz Tune)





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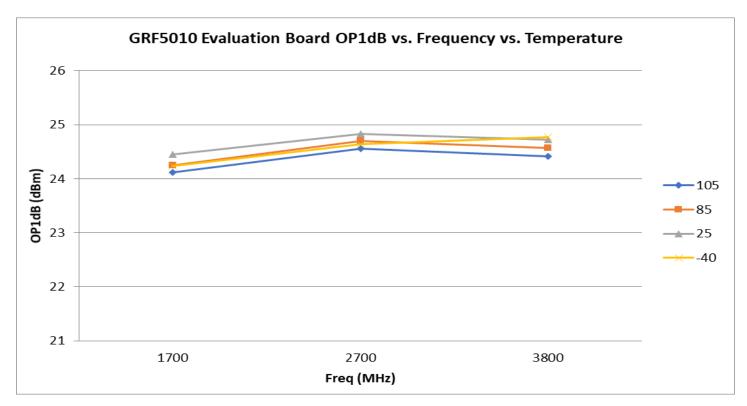


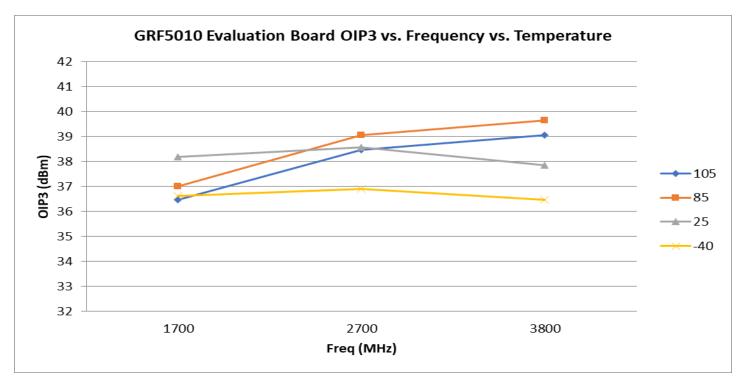
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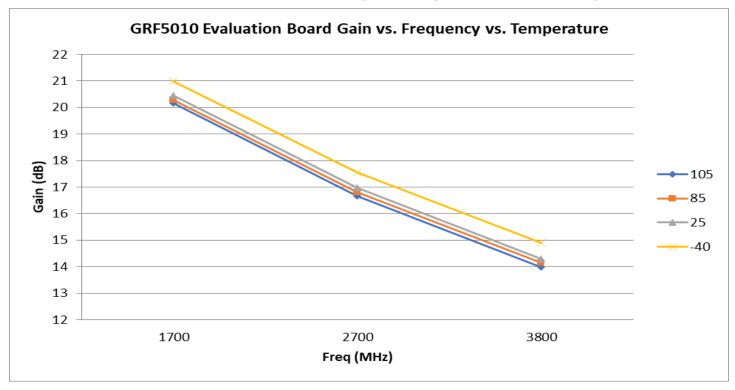


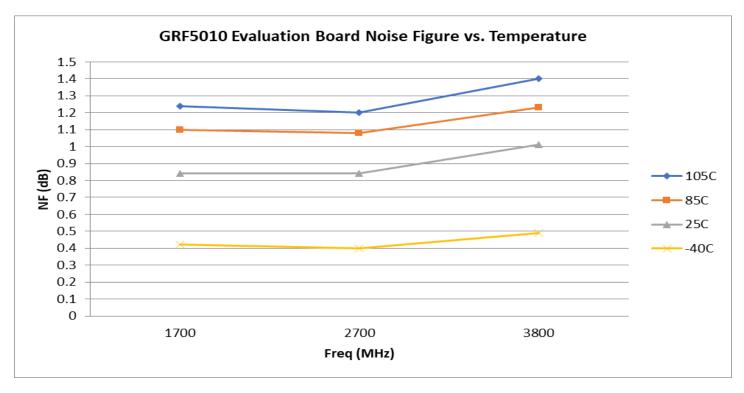
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GRF5010 Evaluation Board Data; Bias: 8.0V/95mA; (1.7 to 3.8 GHz Tune)





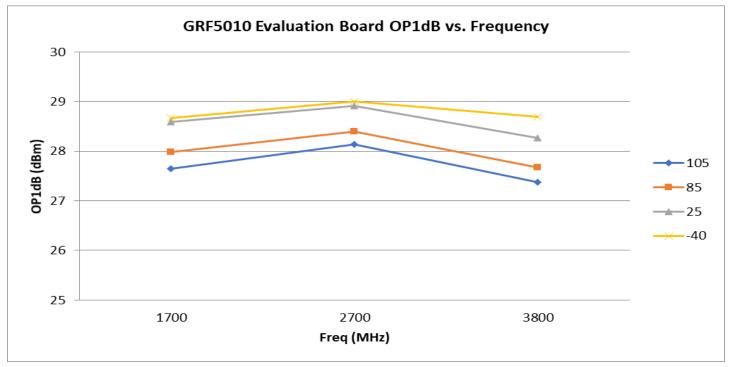


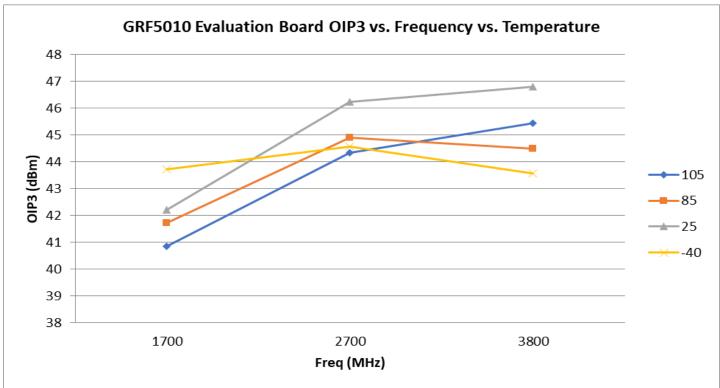
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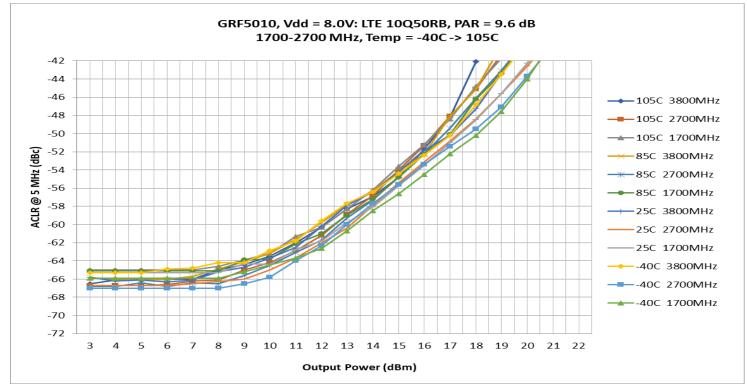


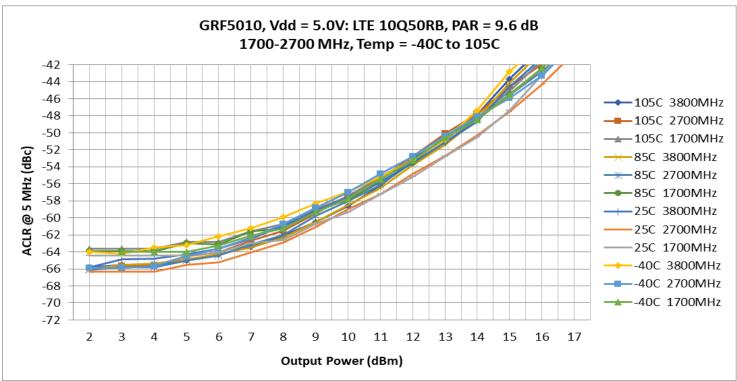


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28.5 dBm Power-LNA™ Tuning Range: 0.05 - 6.0 GHz

#### GRF5010 Evaluation Board Data: (1.7 to 3.8 GHz Tune)





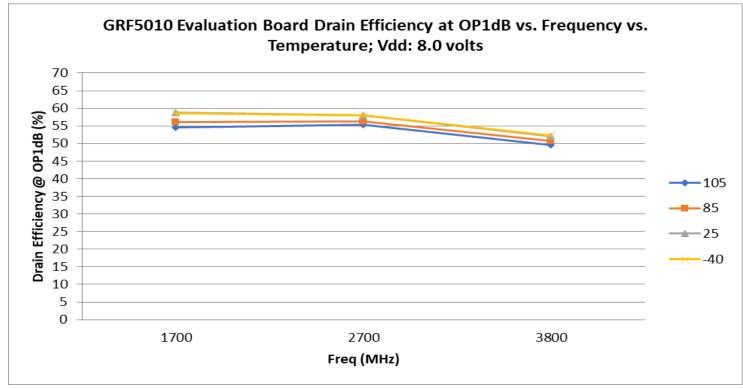


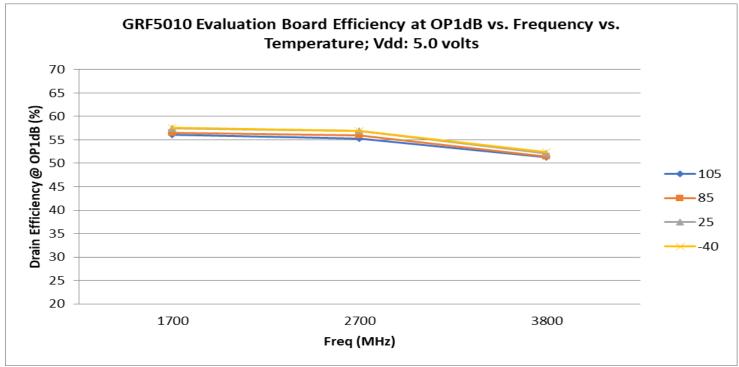
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28.5 dBm Power-LNA™ Tuning Range: 0.05 – 6.0 GHz

#### **GRF5010** Evaluation Board Data: (1.7 to 3.8 GHz Tune)



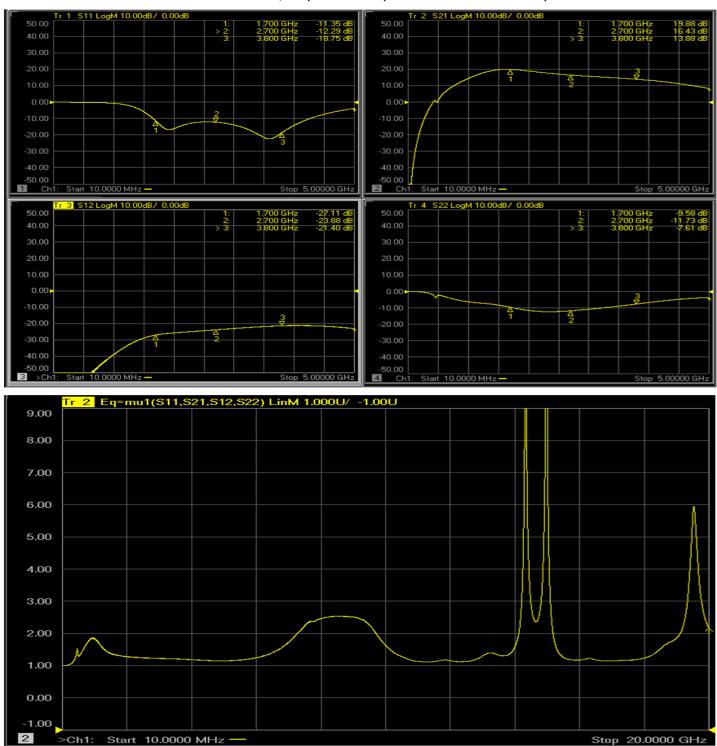




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#### GRF5010 Evaluation Board S-Pars; 5V/60 mA: (1.7 to 3.8 GHz Tune)



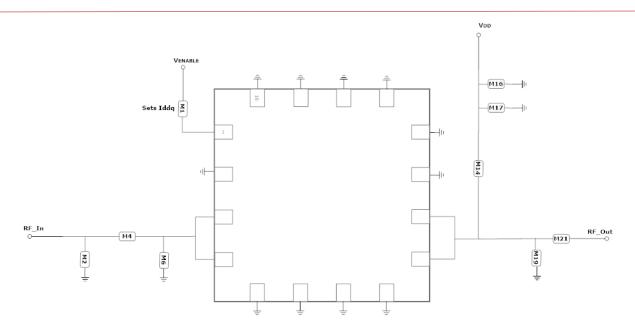
Note: Mu factor >= 1.0 implies unconditional stability.



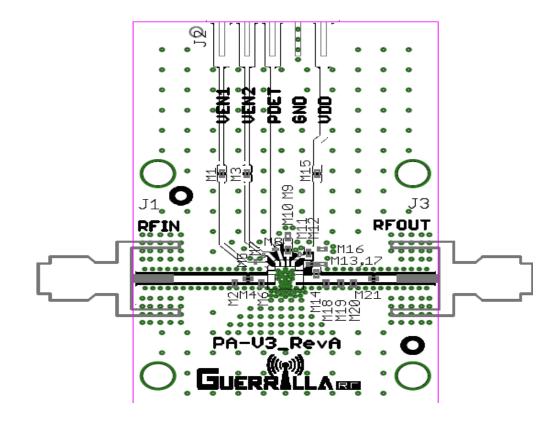
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**GRF5010** Application Schematic: (1.7 to 3.8 GHz)



**GRF50XX Evaluation Board Assembly Drawing** 



# **GRF5010**

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### GRF5010 Evaluation Board BOM: (1.7—3.8 GHz)

| Component        | Туре              | Manufacturer | Family | Value     | Package Size | Substitution |
|------------------|-------------------|--------------|--------|-----------|--------------|--------------|
| M1 (see curves)  | Resistor          | Various      | 5%     | Sets Iddq | 0402         | ok           |
| M2               | Inductor: High Q  | Coilcraft    | НР     | 3.6 nH    | 0402         | ok           |
| M4               | Capacitor: High Q | Murata       | GJM    | 2.0 pF    | 0402         | ok           |
| M6               | Capacitor: High Q | Murata       | GJM    | 0.7 pF    | 0402         | ok           |
| M14              | Inductor          | Coilcraft    | НР     | 10 nH     | 0402         | ok           |
| M15 (Jumper)     | Resistor          | Various      | 5%     | 0 Ohm     | 0402         | ok           |
| M16              | Capacitor         | Murata       | GRM    | 0.1 uF    | 0402         | ok           |
| M17              | Capacitor         | Murata       | GRM    | 100 pF    | 0402         | ok           |
| M19              | DNP               | _            | _      | _         | _            | _            |
| M21              | Capacitor         | Murata       | GJM    | 10 pF     | 0402         | ok           |
| Evaluation Board | PA-V3_RevA        |              |        |           |              |              |

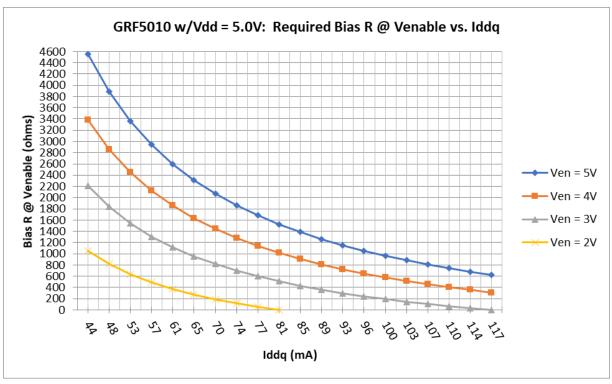


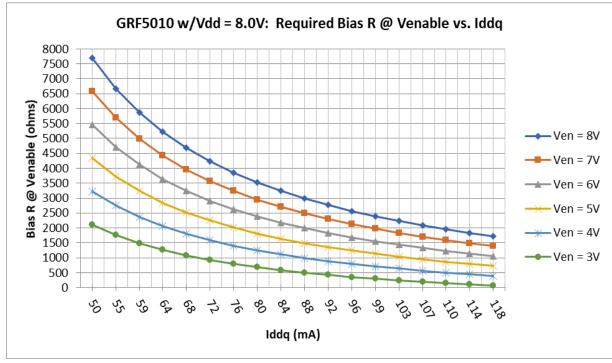
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#### **GRF5010** Bias Resistor (M1) Selection Curves:



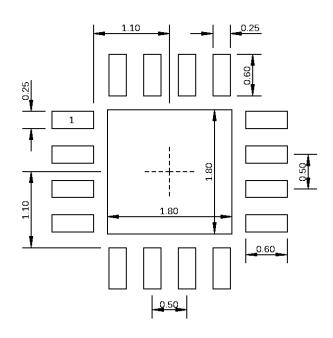




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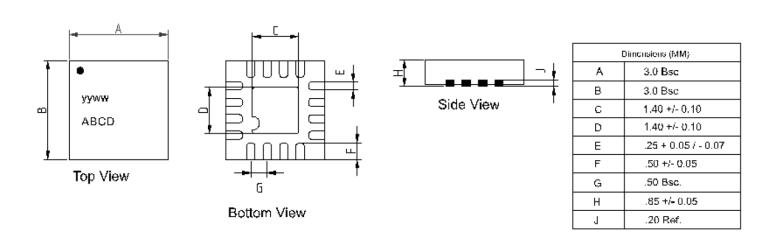
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Dimensions in millimeters

#### 3.0 mm QFN-16 Suggested PCB Footprint (Top View)



3.0 mm QFN-16 Package Dimensions



# **GRF5010**

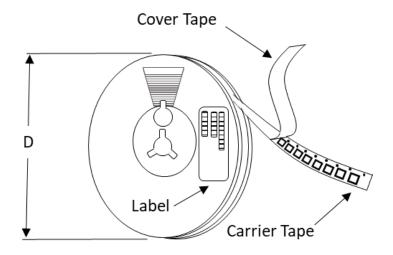
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#### Tape and Reel Information:

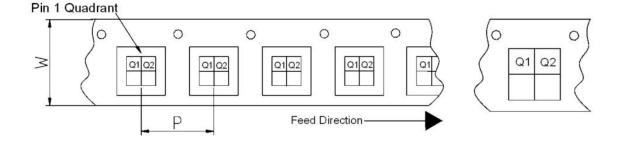
Revision Date: 10/01/19

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling". Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



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#### Tape and Reel Specification and Device Package Information Table

| Package |                    |             | Carrier Tape   |                   |                          | Reel                |                          |                   |
|---------|--------------------|-------------|----------------|-------------------|--------------------------|---------------------|--------------------------|-------------------|
| Туре    | Dimensions<br>(mm) | Leads       | Weight<br>(mg) | Width (W)<br>(mm) | Pocket Pitch (P)<br>(mm) | Pin 1 Quad-<br>rant | Diameter (D)<br>(inches) | Units per<br>Reel |
| QFN     | 2.0 x 2.0 x 0.50   | 12          | 7              | 8                 | 4                        | Q1                  | 7                        | 2500              |
| QFN     | 3.0 x 3.0 x 0.85   | 16          | 24             | 12                | 8                        | Q1                  | 7                        | 1500              |
| DFN     | 1.5 x 1.5 x 0.45   | 6           | 4              | 8                 | 4                        | Q1                  | 7                        | 2500              |
| DFN     | 2.0 x 2.0 x 0.75   | 8           | 12             | 8                 | 4                        | Q1                  | 7                        | 2500              |
| LFM     | 3.5 x 3.5 x 0.75   | See<br>note | TBD            | 12                | 8                        | Q2                  | 7                        | 1500              |
| LFM     | 4.0 x 4.0 x 0.75   | See<br>note | TBD            | 12                | 8                        | Q2                  | 7                        | 1500              |

Note: Lead count may vary. Reference applicable product data sheet



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| Data Sheet Release Status: | Notes   |
|----------------------------|---|
| Advance                    | S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices. |
| Preliminary                | All data based on evaluation board measurements in the Guerrilla RF Applications Lab.   |
| Released                   | All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.                                  |

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 10/01/19

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