

GRF2014

Broadband Linear Gain Block 0.05 to 3.8 GHz



Features

Reference: 5.0V/150mA/900 MHz

Gain: 15.9 dB

OIP3: 43.5 dBm;

OP1dB: 24.0 dBm

Reference: 8.0V/150mA/900 MHz

Gain: 15.9 dB

OIP3: 41.0 dBm;

OP1dB: 27.5 dBm

Flexible Bias Voltage and Current

• Internally Matched to 50 Ω

Process: GaAs pHEMT

Applications

Linear Driver Amplifier

Small Cells and Cellular Repeaters

Distributed Antenna Systems

IF Amplifier

Revision Date: 05/31/19

General Purpose Linear Gain Block

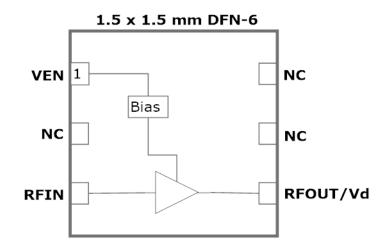
Product Description

GRF2014 is a broadband, linear gain block designed for small cell, wireless infrastructure and other high performance applications. It exhibits outstanding broadband linearity and return losses over 500 to 2700 MHz with a single match.

The device is operated from a supply voltage (V_{DD}) of 2.7 to 8.0V with adjustable I_{DDQ} for optimal efficiency and linearity.

The device is internally matched to 50 Ω at the input and output ports, needing only external DC blocks and a bias choke on the output.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device sparameters.





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Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V _{DD}	0	9.0	V
RF Input Power: (Load VSWR < 2:1; V _D : <= 8.0 volts)	P _{IN MAX}		21	dBm
Operating Temperature (Package Heat Sink)	T _{AMB}	-40	105	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power	P _{DISS MAX}		1.5	W
Electrostatic Discharge:				
Charged Device Model:	CDM	1500		V
Human Body Model:	HBM	250		V
Storage:				
Storage Temperature	T _{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	



Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

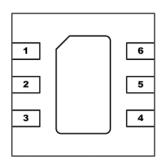
Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF2014 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

Link to manufacturing note



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Pin Out (Top View)



Pin Assignments:

Pin	Name	Description	Note
1	VENABLE	Enable Voltage Input	Venable and series resistor set IDDQ. Venable < 0.2 volts disables device. On- die pull-down resistor will turn the part off if this node is allowed to float.
2	NC	No Connect or Ground	No internal connection to die
3	RF_In	LNA RF input	Internally matched $50\Omega.$ An external DC blocking cap must be used.
4	RF_Out	LNA RF output	Internally matched 50 Ω . V_{DD} must be applied through a choke to this pin
5	NC	No Connect or Ground	No internal connection to die
6	NC	No Connect or Ground	No internal connection to die
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.



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Nominal Operating Parameters:

Doromotor	Symbol	Specification		Lloit	Condition		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition	
Test Frequency	F _{TEST}		900		MHz	$V_{DD} = 5.0 \text{ V}, T_A = 25 ^{\circ}\text{C}$	
Gain	S21	14.9	15.9		dB		
Evaluation Board Noise Figure	NF		3.4		dB		
Output 3rd Order Intercept	OIP3		43.5		dBm	+2.0 dBm P _{OUT} per tone at 2 MHz Spacing (899 and 901 MHz)	
Output 1dB Compression Power	OP1dB	22.5	24.0		dBm		
Switching Rise Time	T _{RISE}		200		ns		
Switching Fall Time	T _{FALL}		200		ns		
Supply Current	I _{DD}		150		mA		
Enable Current	lenable		5.5		mA		
Disabled Mode							
Leakage Current	ILEAKAGE		800		uA	Vdd: 5.0V; Venable: 0.0V	
Thermal Data							
Thermal Resistance: (Infra-Red Scan)	Θјс		38		°C/W	On standard Evaluation Board	
Channel Temperature @ +85 C Reference (Package heat sink)	TCHANNEL		114 (See note)		°C	V _{DD} : 5.0 V; I _{DDQ} : 150 mA; No RF; P _{DISS} : 750 mW	

Note: MTTF >10^6 hours for Tchannel < =170 degrees C.



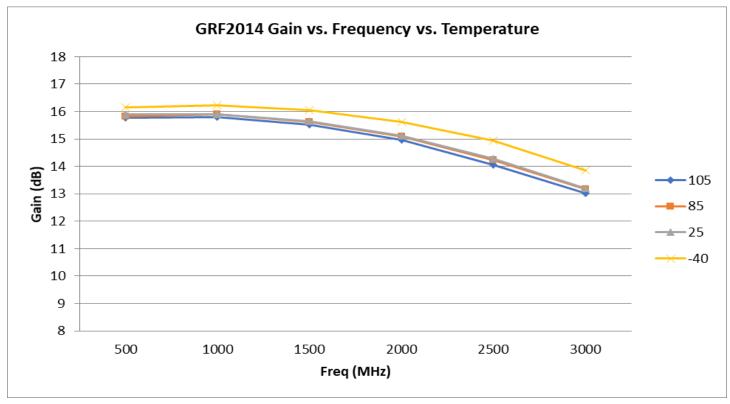


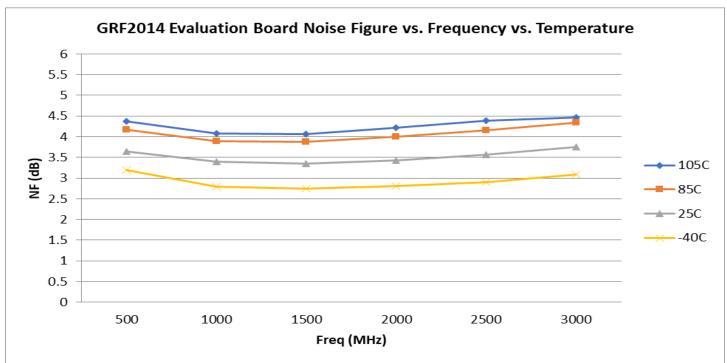
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GRF2014 Performance over Temperature (5V/150 mA)



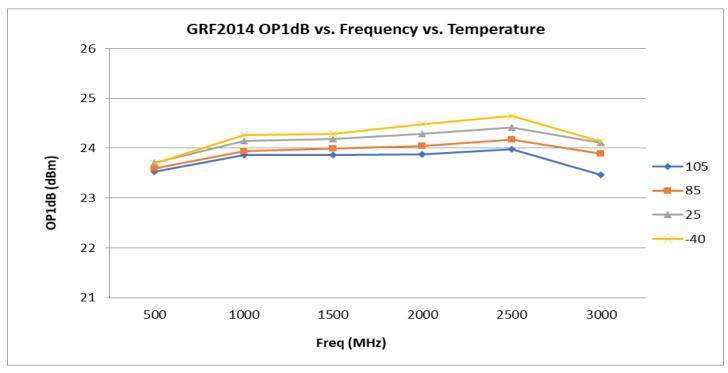


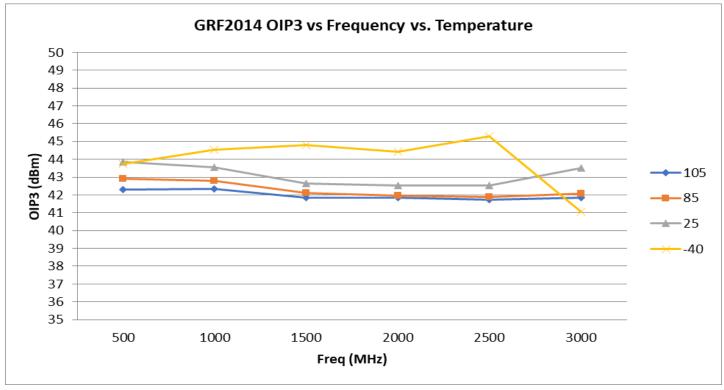




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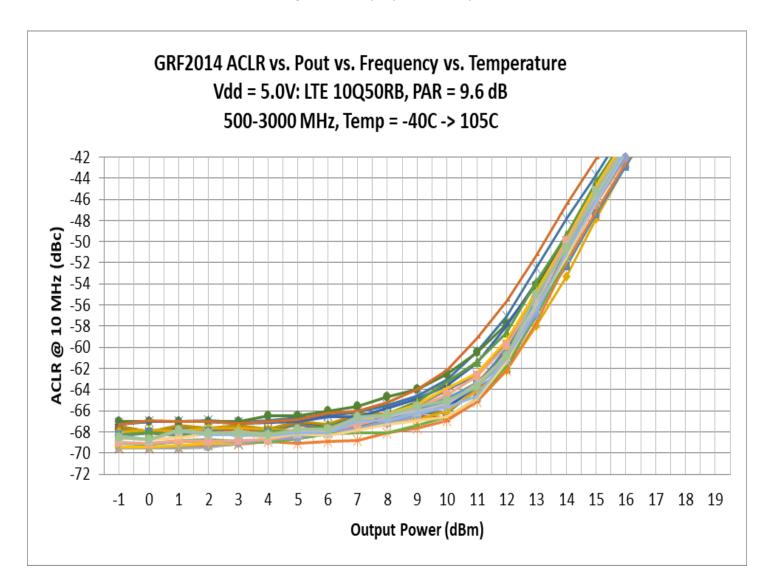


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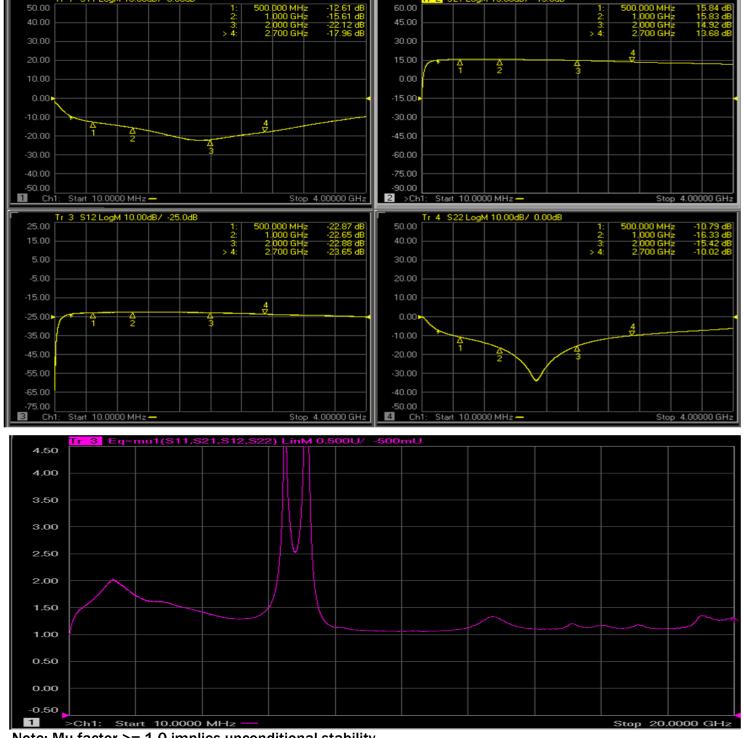






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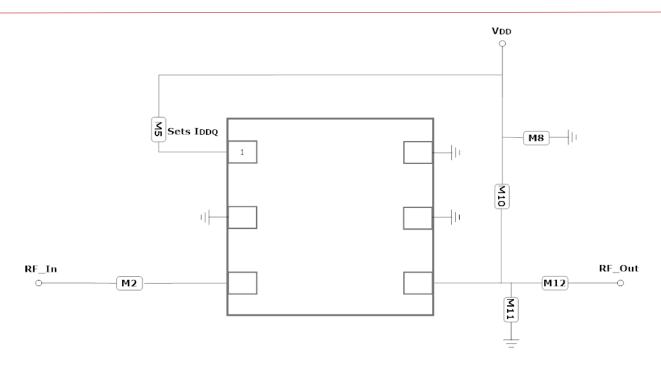
GRF2014 Evaluation Board S-Pars and Stability Mu Factor: (0.5 – 2.7 GHz Match)



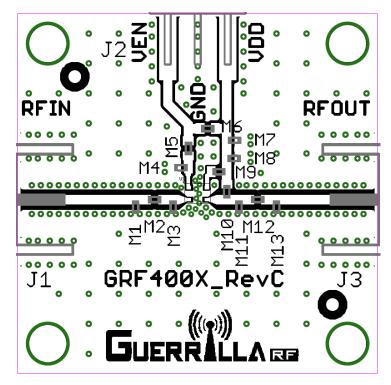
Note: Mu factor >= 1.0 implies unconditional stability.



Broadband Linear Gain Block 0.05 to 3.8 GHz



GRF2014 Application Schematic



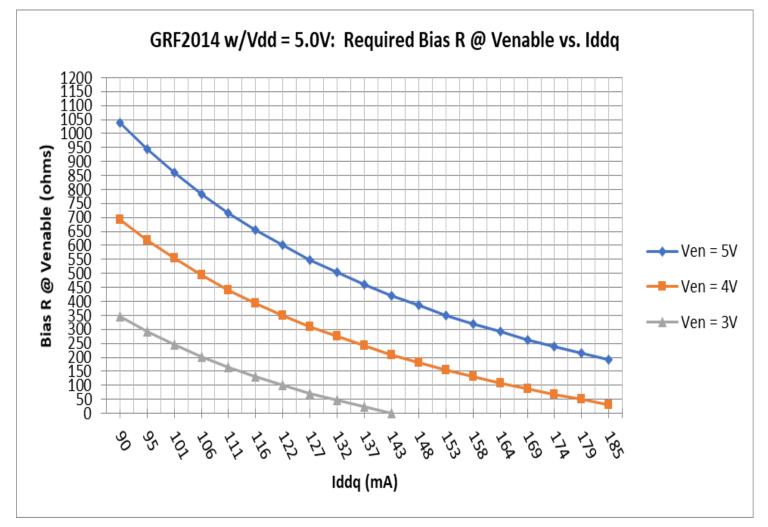
GRF2014 Evaluation Board Assembly Diagram



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GRF2014 Standard Evaluation Board BOM: (0.5 to 2.7 GHz Tune)

Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M2	Capacitor	Murata	GRM	100 pF	0402	ok
M5 (See curves)	Resistor	Various	5%	Sets Iddq	0402	ok
M8	Capacitor	Murata	GRM	0.1 uF	0402	ok
M10	Inductor	Various	MLC	33 nH	0402	ok
M11	Capacitor	Murata	GRM	0.5 pF	0402	ok
M12	Capacitor	Murata	GRM	100 pF	0402	ok
Evaluation Board	GRF400X_RevC					



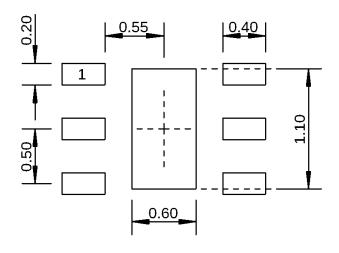




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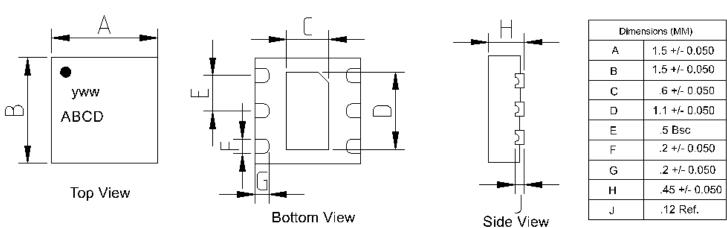
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Dimensions in millimeters

1.5 mm DFN-6 Suggested PCB Footprint (Top View)



Dimensions (MM)			
A	1.5 +/- 0.050		
В	1.5 +/- 0.050		
Ç	.6 +/- 0.050		
D	1.1 +/- 0.050		
E	.5 Bsc		
F	.2 +/- 0.050		
G	.2 +/- 0.050		
Н	.45 +/- 0.050		
J	.12 Ref.		

1.5 mm DFN-6 Package Dimensions



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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 05/31/19

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