

#### Released

# **GRF2004**

#### Broadband Gain Block 0.1 to 10.0 GHz



#### **Features**

Reference: 5V/100mA/4.0 GHz

Gain: 16.5 dB

OP1dB: 18.0 dBm

OIP3: 31.0 dBm

Eval Board NF: 1.9 dB

- Flexible Bias Voltage and Current
- Internally Matched to 50 Ω
- Process: GaAs pHEMT

#### **Applications**

- Microwave Backhaul
- C/X-Band Amplifiers
- General Purpose Amplifiers
- Instrumentation

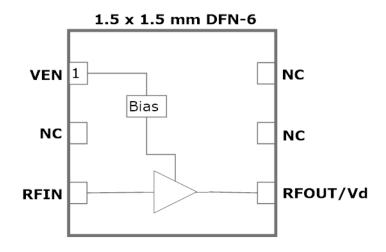
Revision Date: 08/19/19

#### **Product Description**

GRF2004 is a broadband, low noise linear gain block designed for small cell, wireless infrastructure and other high performance RF applications. Due the extreme broadband nature of the device, data is presented for wideband RF measurements using network analyzer bias Tees. Under these conditions, the device exhibits good performance over 100 MHz to 10 GHz with minimal external components.

The device can be operated over a range of supply voltages from 1.8 to 5.0 V selectable Iddq for optimal efficiency and linearity.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device sparameters.





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# **Absolute Ratings:**

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V <sub>DD</sub>	0	6.0	V
RF Input Power: (Load VSWR < 2:1; V <sub>D</sub> : 5.0 volts)	P <sub>IN MAX</sub>		15	dBm
RF Input Power: (Load VSWR < 2:1; V <sub>D:</sub> <4.0 volts)	P <sub>IN MAX</sub>		20	dBm
Operating Temperature (Package Heat Sink)	Т <sub>АМВ</sub>	-40	105	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power	PDISS MAX		600	mW
Electrostatic Discharge:				
Charged Device Model:	CDM	1500		V
Human Body Model:	HBM	250		V
Storage:				
Storage Temperature	T <sub>STG</sub>	-65	150	°C
Moisture Sensitivity Level	MSL		1	



Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF2004 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

Link to manufacturing note

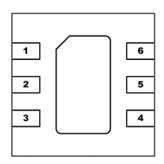
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# Pin Out (Top View)



# Pin Assignments:

Revision Date: 08/19/19

Pin	Name	Description	Note
1	VENABLE	Enable Voltage Input	Venable and series resistor set IDDQ. Venable < 0.2 volts disables device. On- die pull-down resistor will turn the part off if this node is allowed to float.
2	NC	No Connect or Ground	No internal connection to die
3	RF_In	LNA RF input	Internally matched $50\Omega.$ An external DC blocking cap must be used.
4	RF_Out/VDD	LNA RF output	Internally matched 50 $\Omega$ . $V_{DD}$ must be applied through a choke to this pin
5	NC	No Connect or Ground	No internal connection to die
6	NC	No Connect or Ground	No internal connection to die
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.



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# **Nominal Operating Parameters:**

Davamatav	Cymabal	Specification		Hois	Condition		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition	
Test Frequency	F <sub>TEST</sub>		4.0		GHz	$V_{DD} = 5.0 \text{ V}, T_A = 25 ^{\circ}\text{C}$	
Gain	S21	15.0	16.5		dB		
Evaluation Board Noise Figure	NF		1.9		dB		
Output 3rd Order Intercept	OIP3		31.0		dBm	0.0 dBm P <sub>OUT</sub> per tone at 2 MHz Spacing (3999 and 4001 MHz)	
Output 1dB Compression Point	OP1dB	16.0	18.0		dBm		
Switching Rise Time	T <sub>RISE</sub>		800		ns		
Switching Fall Time	$T_{FALL}$		600		ns		
Supply Current	I <sub>DD</sub>		100		mA		
Enable Current	lenable		1.8		mA		
Leakage Current	ILEAKAGE		1		uA	Vdd: 5.0V; Venable: 0.0V	
Thermal Data							
Thermal Resistance: (Infra-Red Scan)	Θјс		104		°C/W	On standard Evaluation Board	
Channel Temperature @ +85 C Reference (Package heat sink)	TCHANNEL		137 (See note)		°C	V <sub>DD</sub> : 5.0 V; I <sub>DDQ</sub> : 100 mA; No RF; P <sub>DISS</sub> : 500 mW	

Note: MTTF >10^6 hours for TCHANNEL < =170 degrees C.

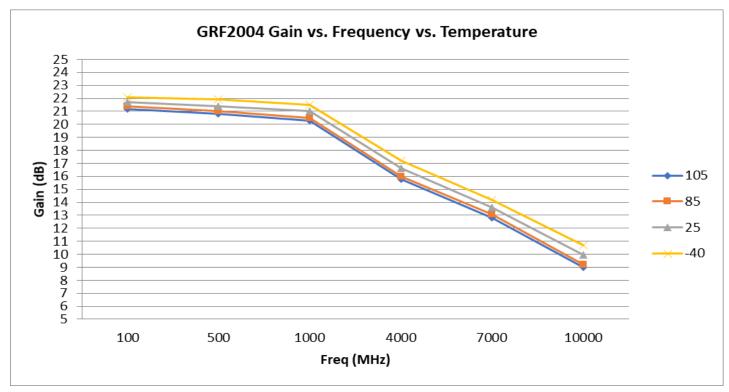
Revision Date: 08/19/19

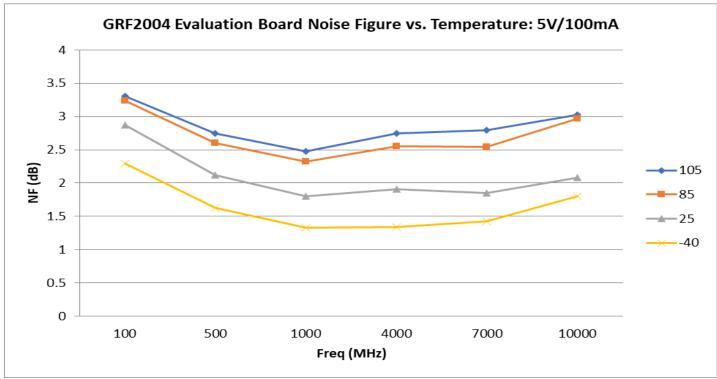




## Broadband Gain Block 0.1 to 10.0 GHz

#### **GRF2004** Evaluation Board Measured Data: (0.1 to 10.0 GHz Bias T Measurements)





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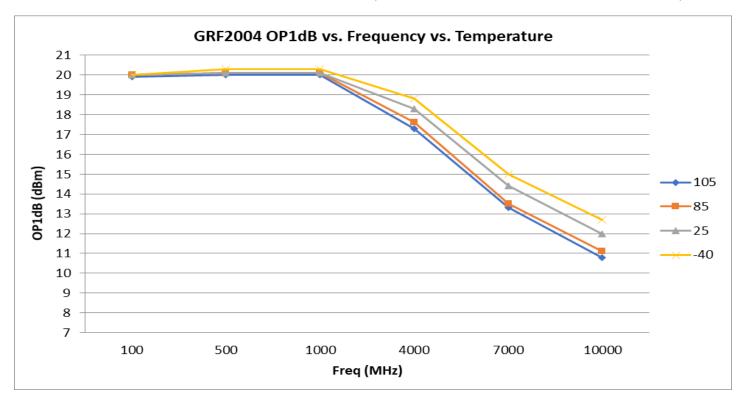


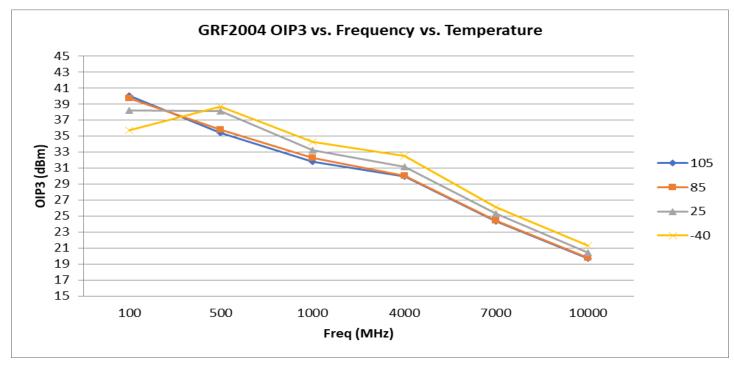


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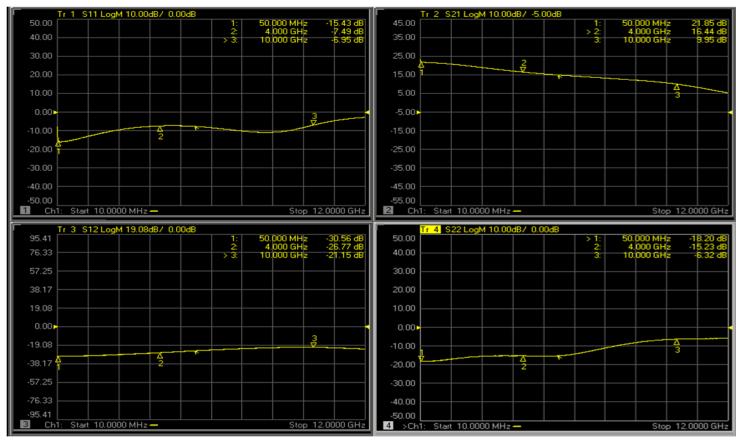


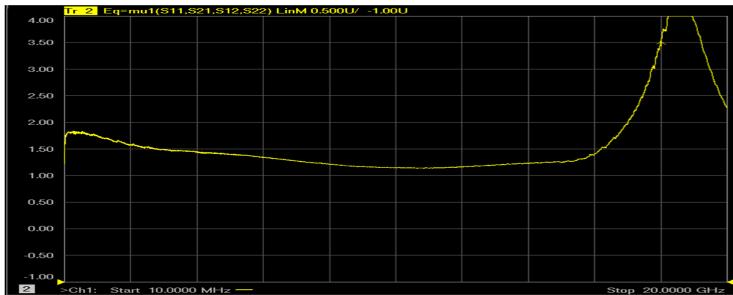




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#### **GRF2004 Evaluation Board S-Pars and Stability Mu Factor: (0.1 to 10.0 GHz VNA Bias Ts)**





Note: Mu factor >= 1.0 implies unconditional stability.

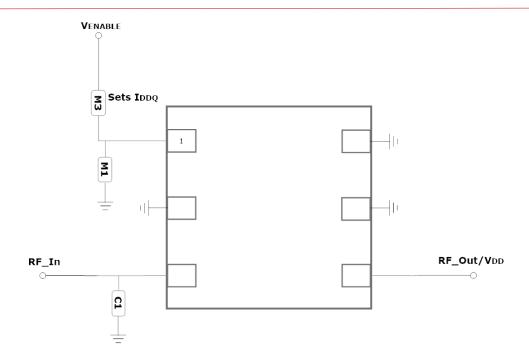




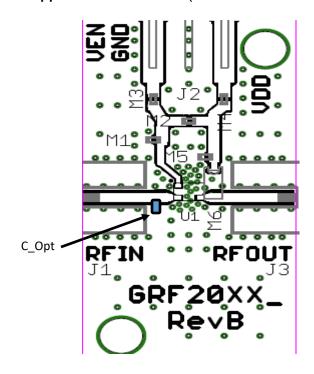
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Broadband Gain Block 0.1 to 10.0 GHz



#### GRF2004 Application Schematic (0.1 to 10.0 GHz Eval Board)



**GRF2004 Evaluation Board Assembly Drawing** 



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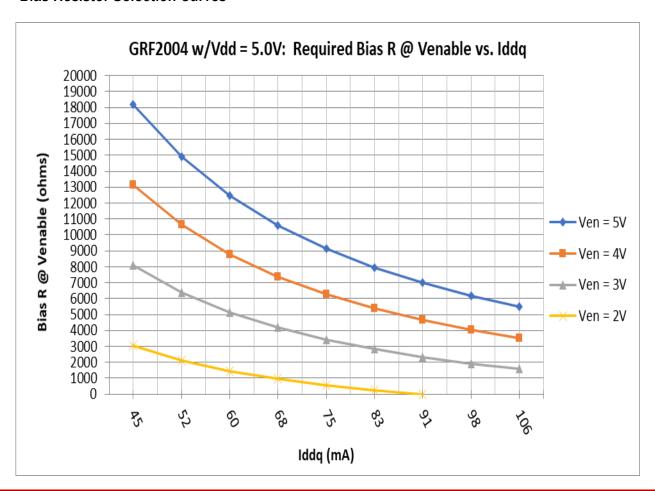
#### **GRF2004 Evaluation Board BOM for VNA S-parameter measurement**

Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M1	Capacitor	Murata	GRM	1000 pF	0402	ok
M3 (See curves)	Resistor	Various	5%	Sets Iddq	0402	ok
C1	Capacitor	Murata	GJM	0.2 pF	0402	ok
Evaluation Board	GRF20XX_RevB	_	_	_	_	_

Note: C1 is added to test board input to enhance high frequency gain of the device.

#### **Bias Resistor Selection Curves**

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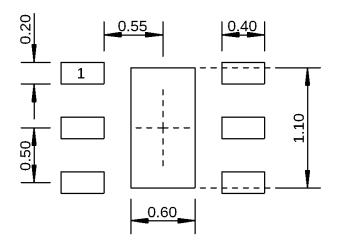




Revision Date: 08/19/19

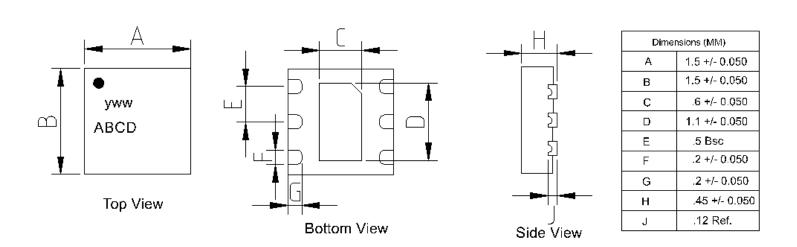
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Dimensions in millimeters

#### 1.5 mm DFN-6 Suggested PCB Footprint (Top View)



#### 1.5 mm DFN-6 Package Dimensions



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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 08/19/19

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