

High Current, High Speed LDO Regulators, Voltage Detector Function

■ GENERAL DESCRIPTION

The XC6413/XC6414 series are highly precise, low noise, positive voltage LDO regulators with voltage detector manufactured using CMOS processes which features high ripple rejection and low dropout. The series is composed of a voltage reference, an error amplifier, a current limiter and a phase compensation circuit plus a driver transistor. Detect voltage is selectable in 0.1V increments within a range of 0.9V ~ 6.0V and VR output voltage is selectable within a range of 0.9V ~ 5.5V. The series is also compatible with low ESR ceramic capacitors which give added output stability. This stability can be maintained even during load fluctuations due to the excellent transient response of the series. The current limiter's foldback circuit operates as a short-circuit protection as well as the output current limiter for the output pin. The series provides optional user selection for a variety of circuit applications such as detector monitoring, detector output logic, CE and EN pin input logic, internal pull-up/down resistors. The XC6413/XC6414FY type can delay the detector output. Delay time can be controlled by the use of an external capacitor (semi-custom).

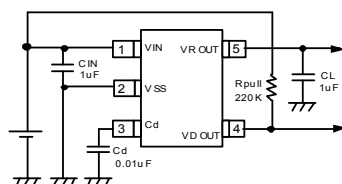
■ APPLICATIONS

- Smart phones / Mobile phones
- Digital still cameras / Camcorders
- Portable games
- Digital audio equipment
- Reference voltage sources
- Multi-function power supplies

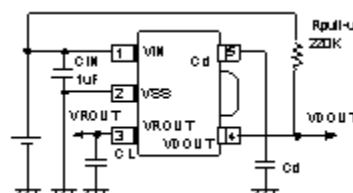
■ FEATURES

Maximum Output Current	: 300mA (380mA limit) ($1.8 \leq V_{ROUT} \leq 5.5V$) [XC6413] 500mA (600mA limit) ($2.5V \leq V_{ROUT} \leq 5.5V$) [XC6414]
Dropout Voltage	: 200mV @ $I_{ROUT}=100mA$
Operating Voltage Range	: 2.0V ~ 10.0V
VR Output Voltage Range	: 0.9V ~ 5.5V (0.1V increments)
Detect Voltage Range	: 0.9V ~ 6.0V (0.1V increments, $\pm 2\%$) More than 2.0V (V_{IN} sensing)
VR Output Voltage Accuracy	: $\pm 2\%$
Detect Voltage Accuracy	: $\pm 2\%$
VR.VD Temperature Coefficient	: $\pm 100ppm/^{\circ}C$ (TYP.)
Low Power Consumption	: $35 \mu A$ (TYP.)
High Ripple Rejection	: 65dB @ 10kHz
Low ESR Capacitor	: Ceramic Capacitor Compatible
Operating Ambient Temperature	: $-40^{\circ}C \sim +85^{\circ}C$
Packages	: SOT-25, SOT-89-5, USP-6B
Environmentally Friendly	: EU RoHS Compliant, Pb Free

■ TYPICAL APPLICATION CIRCUITS



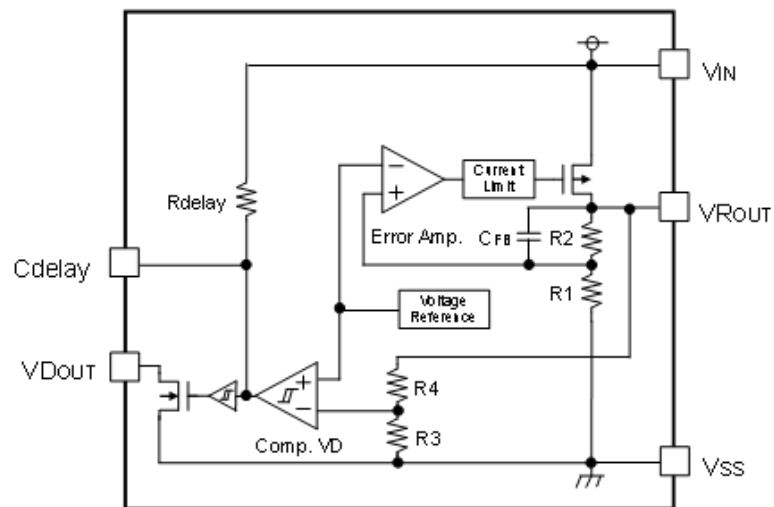
XC6413 Series
Type FY



XC6414 Series
Type FY

■ BLOCK DIAGRAM

XC6413/XC6414 Series Type FY



■PRODUCT CLASSIFICATION

●Ordering Information

XC6413/XC6414①②③④⑤⑥-⑦^(*)

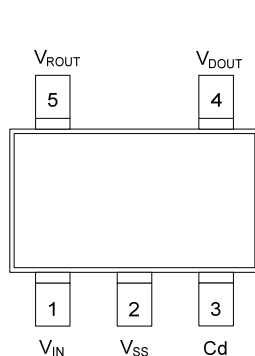
DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
①	Operational Function	F	Cd pin
②	Type of Regulator	Y	VD Sense pin : VR _{OUT} , VD Output Logic : Detect L
③④	Output Voltage & Detect Voltage	-	Internally set sequential number relating to output voltage and detect voltage (refer to the chart below) VR setting output voltage range: 0.9V ~ 5.5V Detect voltage setting range: 0.9V ~ 6.0V 100mV increments are available
⑤⑥-⑦	Packages (Order Unit)	MR	SOT-25 (3,000pcs/Reel)
		MR-G	SOT-25 (3,000pcs/Reel)
		PR	SOT-89-5 (1,000pcs/Reel)
		PR-G	SOT-89-5 (1,000pcs/Reel)
		DR	USP-6B (3,000pcs/Reel)
		DR-G	USP-6B (3,000pcs/Reel))

(*) The “-G” suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

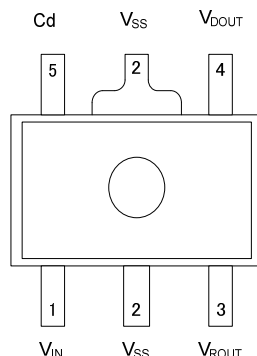
PIN NUMBER: ③, ④ Types (80 ~ 99 : Standard voltage products)

③④	VR _{OUT}	VD _{OUT}	③④	VR _{OUT}	VD _{OUT}	③④	VR _{OUT}	VD _{OUT}	③④	VR _{OUT}	VD _{OUT}
01	3.2	2.5	11	4.0	3.6	80	1.8	1.6	90	1.3	2.0
02	-	-	12	3.1	2.8	81	2.8	3.1	91	1.5	2.0
03	4.5	3.0	13	5.0	4.2	82	1.8	2.0	92	-	-
04	2.5	1.7	14	3.3	2.4	83	2.5	2.8	93	-	-
05	5.0	4.5	15	3.5	3.8	84	2.85	3.2	94	-	-
06	3.3	3.0	16	-	-	85	3.0	3.3	95	-	-
07	2.8	2.2	17	-	-	86	3.5	3.8	96	-	-
08	5.0	4.6	18	-	-	87	3.0	4.2	97	-	-
09	5.0	5.0	19	-	-	88	3.3	4.0	98	-	-
10	3.3	2.7	20	-	-	89	-	-	99	-	-

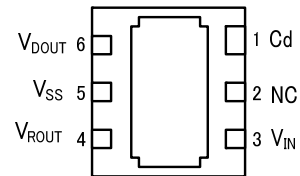
PIN CONFIGURATION



SOT-25
(TOP VIEW)



SOT-89-5
(TOP VIEW)



USP-6B
(BOTTOM VIEW)

*The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release. If the pad needs to be connected to other pins, It should be connected to the V_{SS} (No. 5) pin.

PIN ASSIGNMENT

PIN NUMBER			PIN NAME	FUNCTIONS
SOT-25	SOT-89-5	USP-6B		
1	1	3	V_{IN}	Power Input
2	2	5	V_{SS}	Ground
3	5	1	Cd	Delay Capacitor Connection
4	4	6	V_{DOUT}	VD Output
5	3	4	V_{ROUT}	VR Output
—	—	2	NC	No connection

■ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Input Voltage		V _{IN}	12.0	V
VR Output Current		VR _{IOUT}	700 ^(*1)	mA
VR Output Voltage		VR _{OUT}	V _{SS} -0.3 ~ V _{IN} +0.3	V
VD Output Current		VD _{IOUT}	50	mA
VD Output Voltage		VD _{OUT}	V _{SS} -0.3 ~ 12.0	V
V _{CE} / V _{SEN} / Cd Voltage		V _{CE} / V _{SEN} / Cd	V _{SS} -0.3 ~ V _{IN} +0.3	V
Power Dissipation	SOT-25	Pd	250	mW
			600 (40mm x 40mm standard board) ^(*2)	
	SOT-89-5		500	
			1750(JEDEC board) ^(*2)	
	USP-6B		120	
			1000 (40mm x 40mm standard board) ^(*2)	
Operating Ambient Temperature		Topr	- 40 ~ +85	°C
Storage Temperature		Tstg	- 55 ~ +125	°C

^(*) $VR_{IOUT} \leq Pd / (V_{IN} - VR_{OUT})$

^(*)The power dissipation figure shown is PCB mounted. Please see the power dissipation page for the mounting condition.

XC6413/XC6414 Series

ELECTRICAL CHARACTERISTICS

XC6413/XC6414 Type FY

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNIT	CIRCUIT
VR Output Voltage	V _{ROUT(E)}	I _{ROUT} =30mA		× 0.98	V _{ROUT(T)}	× 1.02	V	1
VR Maximum Output Current (XC6413 Series)	I _{ROUTMAX}	V _{IN} =V _{ROUT(T)} +1.0V When V _{ROUT} ≤ 2.1V, V _{IN} =3.1V		300	-	-	mA	1
VR Maximum Output Current (XC6414 Series)	I _{ROUTMAX}	V _{IN} = V _{ROUT(T)} + 2.0V		500	-	-	mA	1
VR _{OUT} <2.5V		V _{IN} = V _{ROUT(T)} + 2.0V		400	-	-	mA	1
VR Load Regulation	Δ V _{ROUT}	1mA ≤ I _{ROUT} ≤ 100mA		-	15	50	mV	1
Dropout Voltage (*4)	V _{dif1}	I _{ROUT} =30mA		E-1			mV	1
	V _{dif2}	I _{ROUT} =100mA		E-2			mV	1
Supply Current	I _{DD}	V _{IN} =V _{ROUT(T)} + 1.0V V _{ROUT} ≤ 0.90V, V _{IN} =2.0V		-	35	70	μ A	2
VR Line Regulation	Δ V _{ROUT} / (Δ V _{IN} · V _{OUT})	V _{ROUT(T)} + 1.0V ≤ V _{IN} ≤ 10.0V V _{ROUT} ≤ 0.90V, V _{IN} ≥ 2.0 I _{ROUT} =30mA V _{ROUT(T)} ≤ 1.75V, I _{ROUT} =10mA		-	0.01	0.20	% / V	1
Input Voltage	V _{IN}			2.0	-	10.0	V	-
VR Output Temperature Characteristics	Δ V _{ROUT} / (Δ Topr · V _{ROUT})	I _{ROUT} =30mA -40°C ≤ Topr ≤ 85°C		-	± 100	-	ppm /°C	1
VR Ripple Rejection Rate	PSRR	V _{IN} =[V _{ROUT(T)} + 1.0]V + 0.5Vp-pAC When V _{ROUT(T)} ≤ 1.25V → V _{IN} =2.25V + 0.5Vp-pAC I _{ROUT} =50mA, f=10kHz		-	65	-	dB	3
VR Current Limit (XC6413 Series)	I _{RLIM}	V _{IN} =V _{ROUT(T)} +1.0V When V _{ROUT} ≤ 2.1V, V _{IN} =3.1V		300	380	-	mA	1
VR _{OUT} <1.8V				-	380	-	mA	1
VR Current Limit (XC6414 Series)	I _{RLIM}	V _{IN} =V _{ROUT(T)} + 2.0V		500	600	-	mA	1
VR _{OUT} <2.5V				-	600	-	mA	1
VR Short Circuit Current (XC6413 Series)	I _{RSHORT}	V _{IN} =V _{ROUT(T)} + 1.0V When V _{ROUT(T)} ≤ 2.1V, V _{IN} =3.1V		-	30	-	mA	1
VR Short Circuit Current (XC6414 Series)	I _{RSHORT}	V _{IN} =V _{ROUT(T)} + 2.0V When V _{ROUT(T)} ≤ 1.5V, V _{IN} =3.1V		-	30	-	mA	1
VD Detect Voltage	V _{DF(E)}			× 0.98	V _{DF (T)}	× 1.02	V	4
VD Hysteresis Range	V _{HYS}			V _{DF (T)} × 0.02	V _{DF (T)} × 0.05	V _{DF (T)} × 0.08	V	4
VD Output Current	I _{DOUT}	V _{DOUT} = 0.5V	V _{IN} = 2.0V	2.0	5.0	-	mA	5
			V _{IN} = 3.0V	4.0	7.5	-		
			V _{IN} = 4.0V	5.0	9.5	-		
			V _{IN} = 5.0V	6.0	10.5	-		
			V _{IN} = 6.0V	6.5	11.5	-		
			V _{IN} = 7.0V	7.0	12.5	-		
			V _{IN} = 8.0V	7.5	13.5	-		
			V _{IN} = 9.0V	8.0	14.5	-		
V _{IN} = 10.0V	8.5	14.5	-					
VD Detect Voltage Temperature Characteristics	Δ V _{DF} / (Δ Topr · V _{DF})	-40°C ≤ Topr ≤ 85°C		-	± 100	-	ppm /°C	4
Delay Resistance	Delay	V _{IN} =7.0V, C _d =0V Delay Resistance=7.0V/Delay Current		1.0	2.0	2.5	MΩ	6

NOTE

- *1 Unless otherwise stated, $V_{IN}=V_{ROUT(T)}+1.0\text{V}$. $V_{ROUT} \leq 0.9\text{V}$: $V_{IN}=2.0\text{V}$
- *2 $V_{ROUT(T)}$: Nominal VR output voltage
- *3 $V_{OUT(E)}$: Effective VR output voltage
(ie. the VR output voltage when " $V_{ROUT(T)}+1.0\text{V}$ " is provided at the V_{IN} pin while maintaining a certain I_{ROUT} value).
- *4 $V_{dif} = \{V_{IN1}^{(*)} - V_{ROUT1}^{(*)}\}$
- *5 V_{ROUT} : A voltage equal to 98% of the VR output voltage whenever a stabilized ($V_{ROUT(T)}+1.0\text{V}$) is input with each I_{ROUT} .
- *6 V_{IN1} is the input voltage when V_{OUT1} , which appears as Input voltage is gradually decreased.

ELECTRICAL CHARACTERISTICS

VR Dropout Voltage

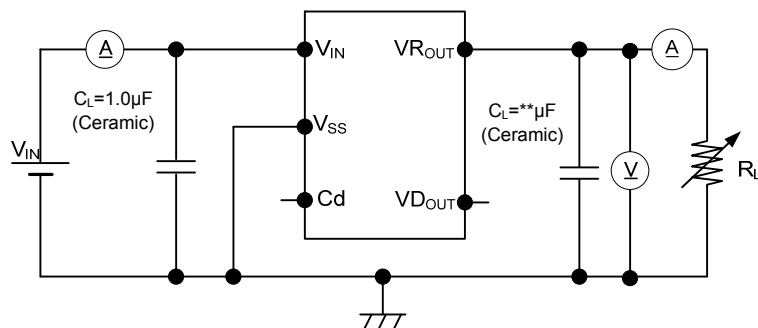
XC6413/XC6414 Type FY

SYMBOL	E-0		E-1		E-2	
NOMINAL DETECT VOLTAGE OUTPUT VOLTAGE	OUTPUT VOLTAGE DETECT VOLTAGE (V)		DROPOUT VOLTAGE 1 (mV) (I _{OUT} =30mA)		DROPOUT VOLTAGE 2 (mV) (I _{OUT} =100mA)	
			Ta=25°C		Ta=25°C	
	V _{ROUT(T)} V _{DF(T)}	V _{ROUT} / V _{DF}	V _{dif1}	V _{dif1}	V _{dif2}	V _{dif2}
	MIN.	MAX.	TYP.	MAX.	TYP.	MAX.
0.90	0.870	0.930	1050	1100	1150	1200
1.00	0.970	1.030	1000	1100	1050	1200
1.10	1.070	1.130	900	1000	950	1100
1.20	1.170	1.230	800	900	850	1000
1.30	1.270	1.330	700	800	750	900
1.40	1.370	1.430	600	700	650	800
1.50	1.470	1.530	500	600	550	700
1.60	1.568	1.632	400	500	500	600
1.70	1.666	1.734	300	400	400	500
1.80	1.764	1.836	200	300	300	400
1.90	1.862	1.938	120	150	280	380
2.00	1.960	2.040	80	120	240	350
2.10	2.058	2.142	80	120	240	330
2.20	2.156	2.244	80	120	240	330
2.30	2.254	2.346	80	120	240	310
2.40	2.352	2.448	80	120	240	310
2.50	2.450	2.550	70	100	220	290
2.60	2.548	2.652	70	100	220	290
2.70	2.646	2.754	70	100	220	290
2.80	2.744	2.856	70	100	220	270
2.90	2.842	2.958	70	100	220	270
3.00	2.940	3.060	60	90	200	270
3.10	3.038	3.162	60	90	200	250
3.20	3.136	3.264	60	90	200	250
3.30	3.234	3.366	60	90	200	250
3.40	3.332	3.468	60	90	200	250
3.50	3.430	3.570	60	90	200	250
3.60	3.528	3.672	60	90	200	250
3.70	3.626	3.774	60	90	200	250
3.80	3.724	3.876	60	90	200	250
3.90	3.822	3.978	60	90	200	250
4.00	3.920	4.080	60	80	180	230
4.10	4.018	4.182	60	80	180	230
4.20	4.116	4.284	60	80	180	230
4.30	4.214	4.386	60	80	180	230
4.40	4.312	4.488	60	80	180	230
4.50	4.410	4.590	60	80	180	230
4.60	4.508	4.692	60	80	180	230
4.70	4.606	4.794	60	80	180	230
4.80	4.704	4.896	60	80	180	230
4.90	4.802	4.998	60	80	180	230
5.00	4.900	5.100	50	70	160	210
5.10	4.998	5.202	50	70	160	210
5.20	5.096	5.304	50	70	160	210
5.30	5.194	5.406	50	70	160	210
5.40	5.292	5.508	50	70	160	210
5.50	5.390	5.610	50	70	160	210
5.60	5.488	5.712	-	-	-	-
5.70	5.586	5.814	-	-	-	-
5.80	5.684	5.916	-	-	-	-
5.90	5.782	6.018	-	-	-	-
6.00	5.880	6.120	-	-	-	-

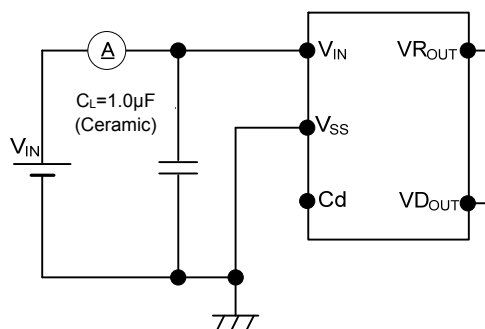
TEST CIRCUITS

XC6413/XC6414 Type FY

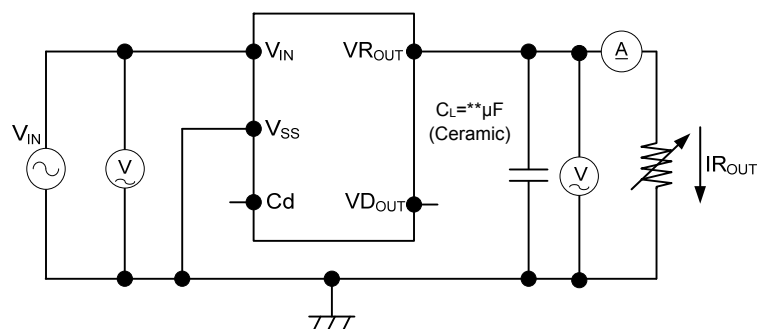
● Circuit 1



● Circuit 2



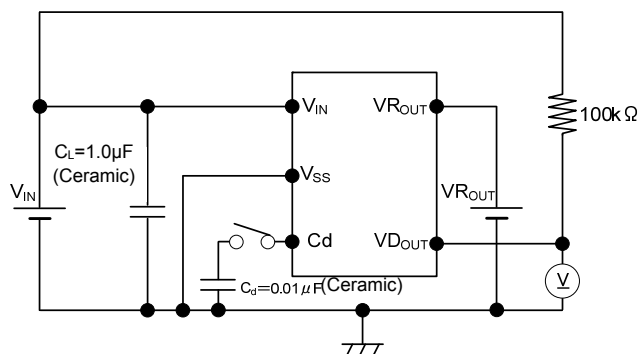
● Circuit 3



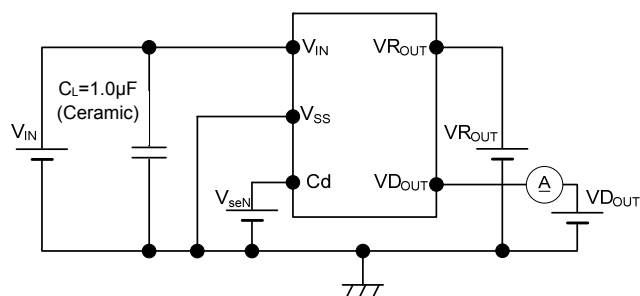
■ TEST CIRCUITS (Continued)

XC6413/XC6414 Type FY

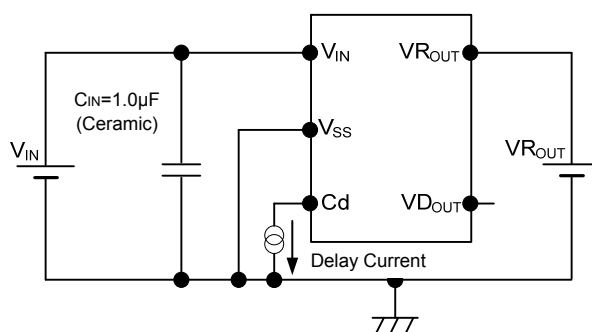
● Circuit 4



● Circuit 5



● Circuit 6



*Output Capacitor Corresponding Chart

VR _{OUT}	C _L
0.9 ~ 1.2V	More than 4.7 μF
1.3 ~ 1.7V	More than 2.2 μF
1.8 ~ 5.5V	More than 1.0 μF

■ OPERATIONAL EXPLANATION

<Output Voltage Regulator Control>

The voltage, divided by resistors R1 & R2 which are connected to the V_{ROUT} pin is compared with the internal reference voltage by the error amplifier. The P-channel MOSFET, which is connected to the V_{ROUT} pin, is then driven by the subsequent output signal.

The output voltage at the V_{ROUT} pin is controlled & stabilized by negative feedback.

The current limit circuit and short circuit protection operate in relation to the level of output current.

<Detector Function with the XC6413/XC6414 Series>

The series' detector function monitors the voltage divided by resistors R3 & R4 which are connected to the V_{ROUT} pin, as well as monitoring the voltage of the internal reference voltage source via the comparator.

The V_{DSEN} pin has options (please refer to the Selection Guide, item 4). A 'Low' signal level can be output from the V_{DOUT} pin when the V_D pin voltage level goes below the detect voltage. As V_{DOUT} is an open-drain N-channel output, a pull-up resistor of about $220k\Omega$ is needed to achieve a voltage output. Because of hysteresis at the detector function, output at the V_{DOUT} pin will invert when the detect voltage level increases above the release voltage (105% of the detect voltage).

By connecting the C_{delay} pin to a capacitor (C_d), the XC6413/XC6414F series can apply a delay time to V_{DOUT} voltage when releasing voltage.

The delay time can be calculated from the internal resistance, R_{delay} ($2M\Omega$, TYP., fixed) and the value of C_d as per the following equation.

$$Delay\ Time = C_{delay} \times R_{delay} \times 0.7$$

● R_{delay} : $1.0 \sim 3.5M\Omega$, TYP: $2.0M\Omega$

C_d	DELAY TIME (TYP.)	DELAY TIME
$0.01\ \mu F$	14.0msec	7.00 ~ 24.5msec
$0.022\ \mu F$	30.8msec	15.4 ~ 53.9msec
$0.047\ \mu F$	65.8msec	32.90 ~ 115.15msec
$0.10\ \mu F$	140msec	70.0 ~ 245msec
$0.22\ \mu F$	308msec	154 ~ 539msec
$0.47\ \mu F$	658msec	329.0 ~ 1151.5msec
$1.0\ \mu F$	1400msec	700.0 ~ 2450msec

<Low ESR Capacitors>

With the XC6413/XC6414 series regulator, a stable output voltage is achievable even if low ESR capacitors are used, as a phase compensation circuit is built-in to the regulator. In order to ensure the effectiveness of the phase compensation, we suggest that an output capacitor (C_L) be connected as close as possible, between the output pin (V_{ROUT}) and the V_{SS} pin. Please use an output capacitor (C_L) with a capacitance, based on the chart below.

We also suggest an input capacitor (C_{IN}) of $1\ \mu F$: this should be connected between V_{IN} and V_{SS} in order to stabilize input power source.

● Output Capacitor Corresponding Chart

V_{ROUT}	C_L
0.9 ~ 1.2V	More than $4.7\ \mu F$
1.3 ~ 1.7V	More than $2.2\ \mu F$
1.8 ~ 5.5V	More than $1.0\ \mu F$

<Current Limiter, Short-Circuit Protection>

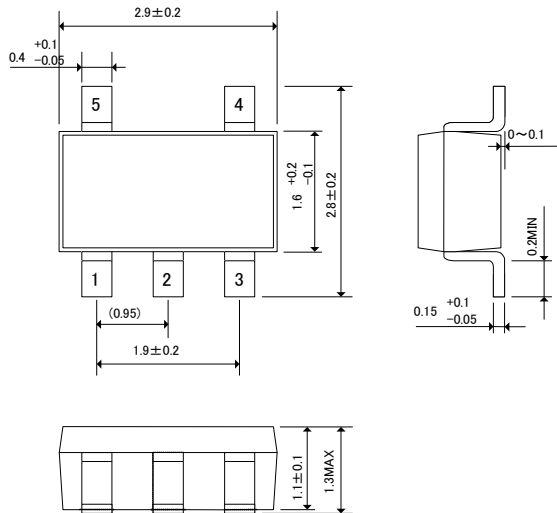
The XC6413/XC6414 series regulator offers a combination of current limit and circuit protection by means of a built-in fixed current limiter circuit and a foldback circuit. When the load current reaches the current limit level, the fixed current limiter circuit operates and output voltage drops. As a result of this drop in output voltage, the foldback circuit operates, the output voltage drops further and output current decreases. When the output pin is shorted, a current of about 50mA flows.

■NOTES ON USE

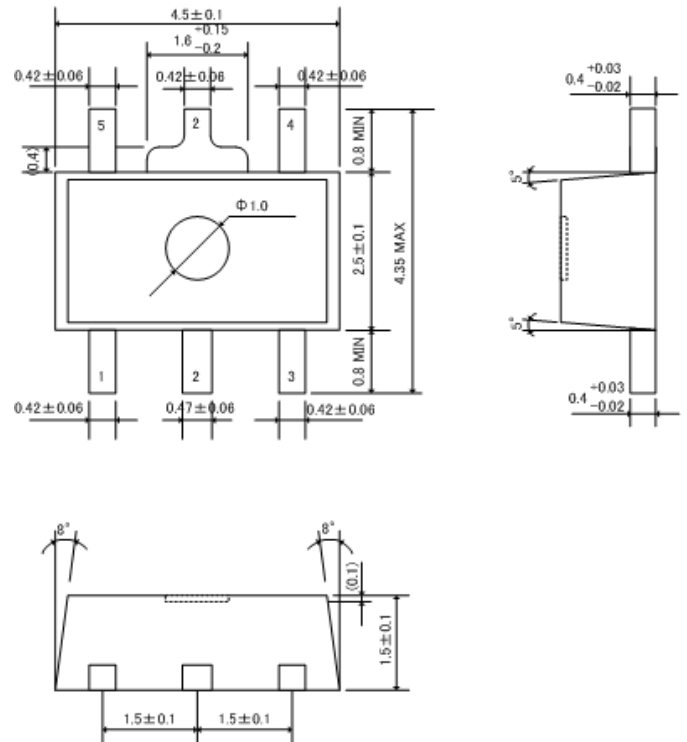
1. Please use this IC within the stated absolute maximum ratings. The IC is liable to malfunction should the ratings be exceeded.
2. Where wiring impedance is high, operations may become unstable due to noise and/or phase lag depending on output current. Please strengthen V_{IN} and V_{SS} wiring in particular.
3. Please wire the input capacitor (C_{IN}) and the output capacitor (C_L) as close to the IC as possible.
Should rapid input fluctuation or load fluctuation occur, please increase the capacitor value such as C_{IN} or C_L to stabilize the operation.
4. Torex places an importance on improving our products and its reliability.
However, by any possibility, we would request user fail-safe design and post-aging treatment on system or equipment.

PACKAGING INFORMATION

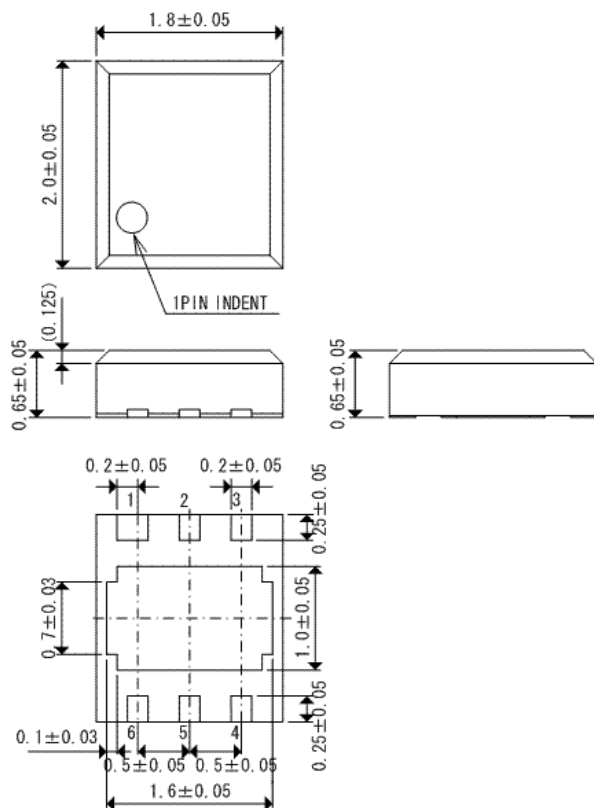
● SOT-25



● SOT89-5

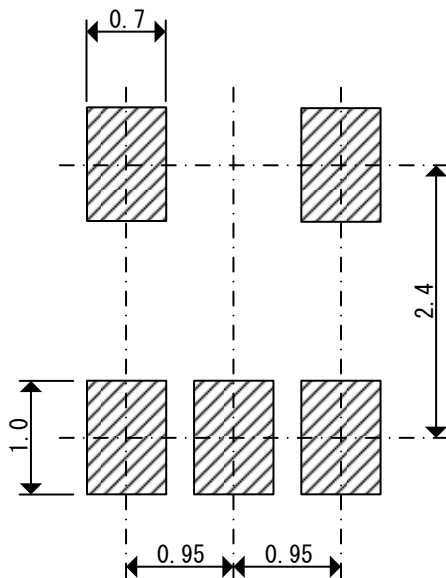


● USP-6B

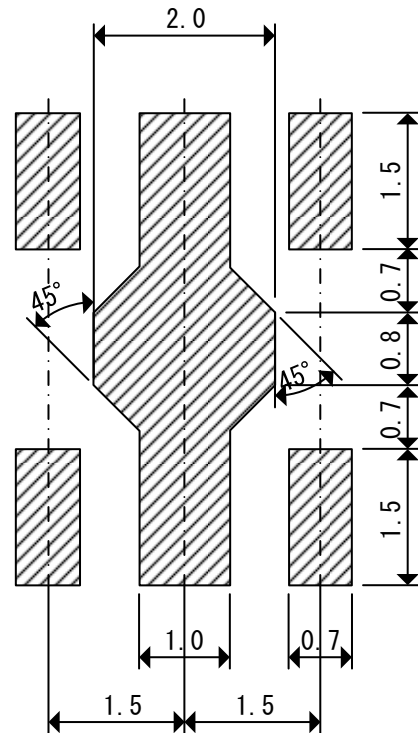


■ PACKAGING INFORMATION(Continued)

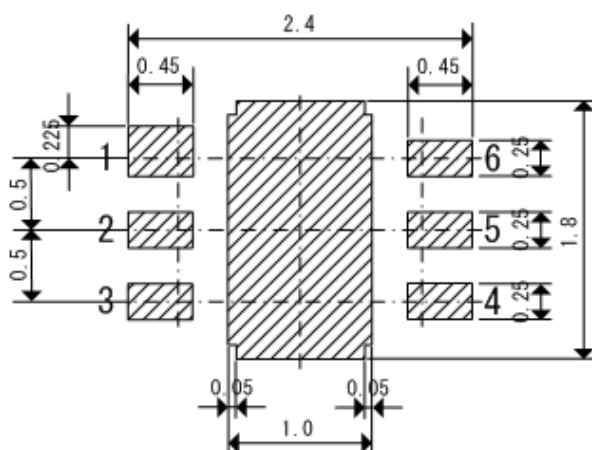
●SOT-25 Reference Pattern Layout



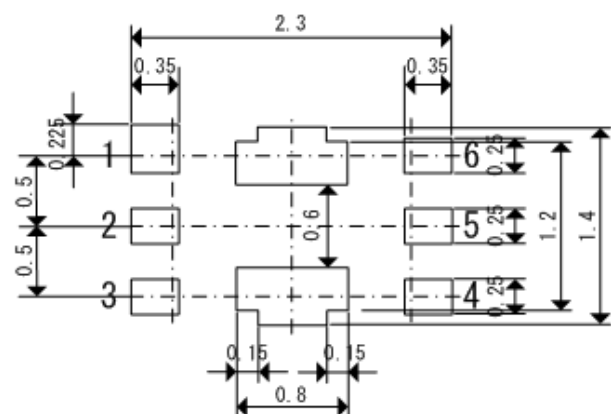
●SOT-89-5 Reference Pattern Layout



●USP-6B Reference Pattern Layout



●USP-6B Reference Metal Mask Design



■ PACKAGING INFORMATION(Continued)

● SOT-25 Power Dissipation (40mm x 40mm Standard board)

Power dissipation data for the SOT-25 is shown in this page.

The value of power dissipation varies with the mount board conditions.

Please use this data as the reference data taken in the following condition.

1. Measurement Condition

Condition: Mount on a board

Ambient: Natural convection

Soldering: Lead (Pb) free

Board: Dimensions 40 x 40 mm

(1600 mm² in one side)

Copper (Cu) traces occupy 50% of the board

area in top and back faces

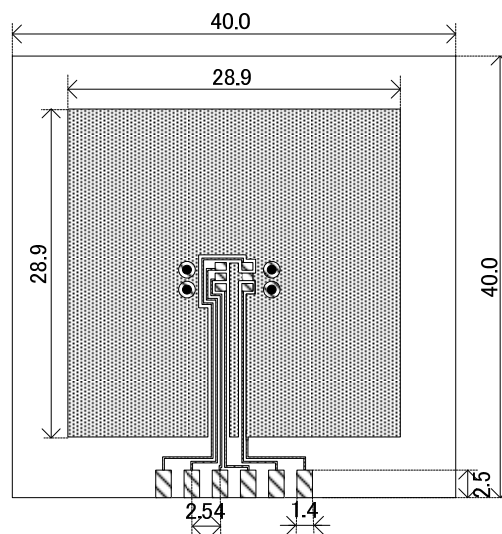
Package heat-sink is tied to the copper traces

(Board of SOT-26 is used.)

Material: Glass Epoxy (FR-4)

Thickness: 1.6mm

Through-hole: 4 x 0.8 Diameter

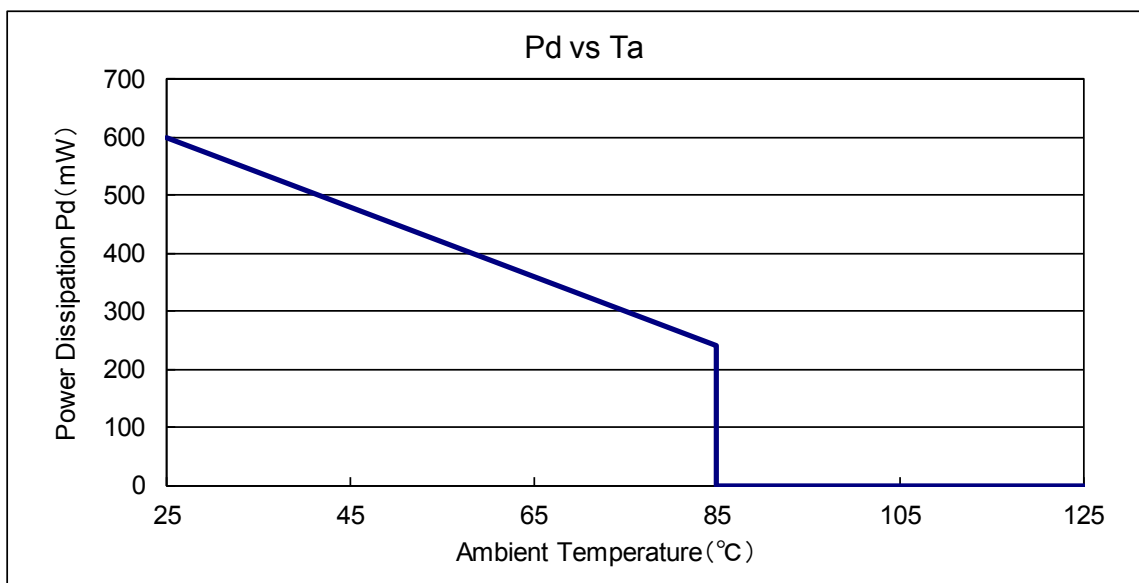


Evaluation Board (Unit: mm)

2. Power Dissipation vs. Ambient Temperature

Board Mount (T_J max = 125°C)

Ambient Temperature (°C)	Power Dissipation Pd (mW)	Thermal Resistance (°C/W)
25	600	166.67
85	240	



■ PACKAGING INFORMATION(Continued)

● USP-6B Power Dissipation (40mm x 40mm Standard board)

Power dissipation data for the USP-6B is shown in this page.

The value of power dissipation varies with the mount board conditions.

Please use this data as the reference data taken in the following condition.

1. Measurement Condition

Condition: Mount on a board

Ambient: Natural convection

Soldering: Lead (Pb) free

Board: Dimensions 40 x 40 mm

(1600 mm² in one side)

Copper (Cu) traces occupy 50% of the board

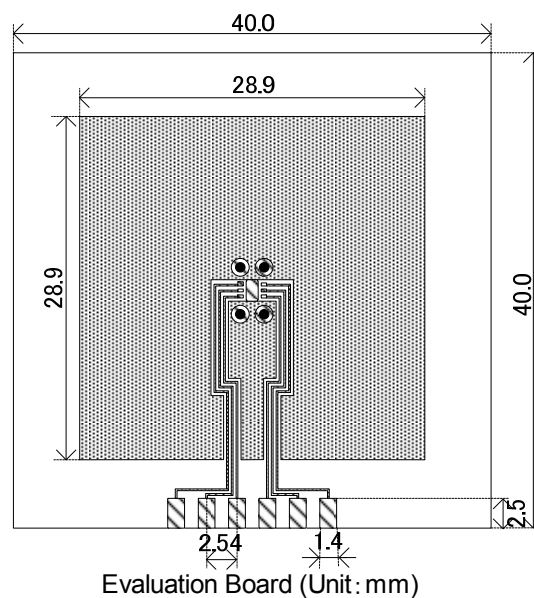
area in top and back faces

Package heat-sink is tied to the copper traces

Material: Glass Epoxy (FR-4)

Thickness: 1.6mm

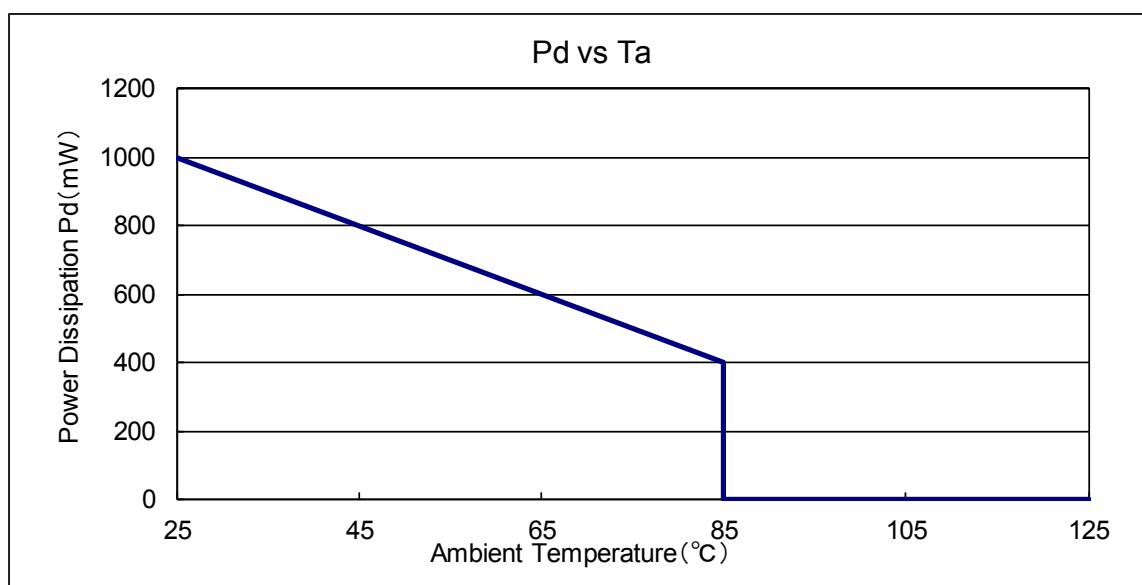
Through-hole: 4 x 0.8 Diameter



2. Power Dissipation vs. Ambient Temperature

Board Mount (T_j max = 125°C)

Ambient Temperature (°C)	Power Dissipation Pd (mW)	Thermal Resistance (°C/W)
25	1000	100.00
85	400	



■ PACKAGING INFORMATION(Continued)

● SOT-89-5 Power Dissipation (JEDEC)

Power dissipation data for the SOT-89-5 is shown in this page.

The value of power dissipation varies with the mount board conditions.

Please use this data as one of reference data taken in the described condition.

1. Measurement Condition (Reference data)

Condition : Mount on a board

Ambient : Natural convection

Soldering : Lead (Pb) free

Board : The board using 4 copper layer.

(76.2mm×114.3mm・・Area: about 8700mm²)

1st layer : No copper foil (Signal layer)

2nd layer : 70mm×70mm_Connected to heat-sink.

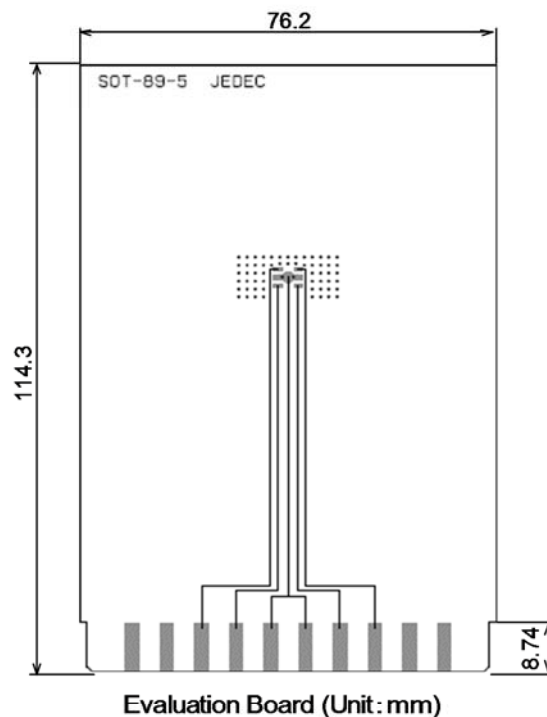
3rd layer : 70mm×70mm_Connected to heat-sink.

4th layer : No copper foil (Signal layer)

Material : Glass Epoxy (FR-4)

Thickness : 1.6mm

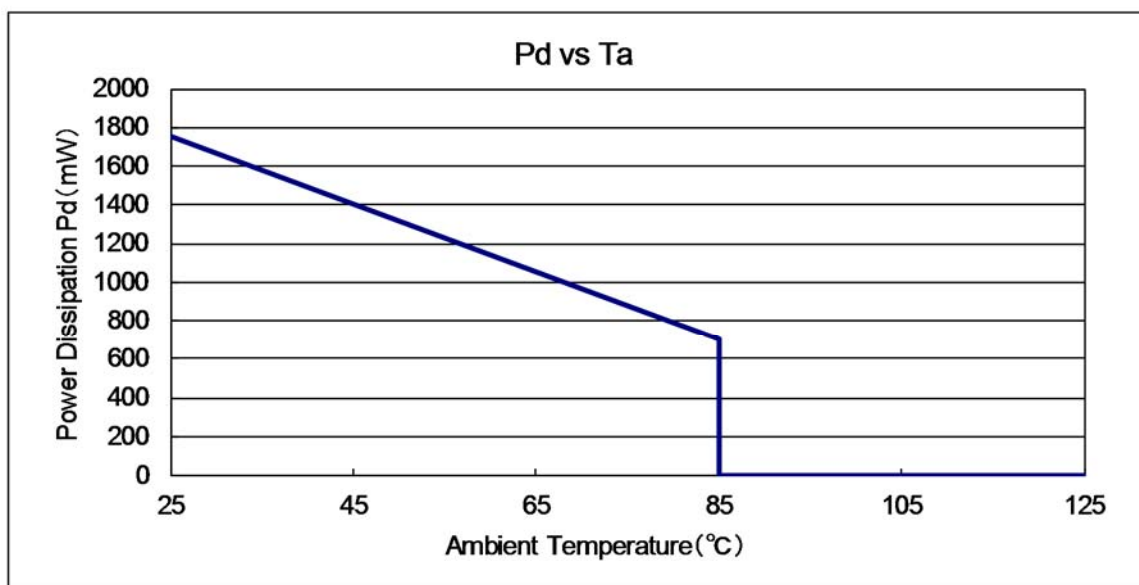
Through-hole : φ0.2mm x 60pcs



2. Power Dissipation vs. Ambient Temperature

Board Mount (T_j max = 125°C)

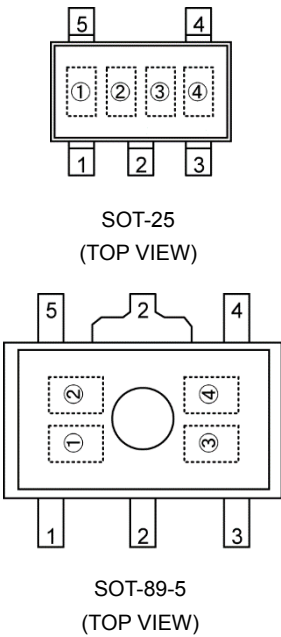
Ambient Temperature (°C)	Power Dissipation Pd (mW)	Thermal Resistance (°C/W)
25	1750	57.14
85	700	



■

MARKING RULE

●SOT-25, SOT-89-5



① represents product series

MARK	PRODUCT SERIES
<u>D</u>	XC6413xxxxxx
<u>E</u>	XC6414xxxxxx

②③ represents internal sequential number

01~09, 10~99, A0~A9, B0~B9, Z9~

(G, I, J, O, Q, W excepted)

The two digits show a combination of “Operational Function”,
“Type of Regulator” and “Output Voltage & Detect Voltage”.

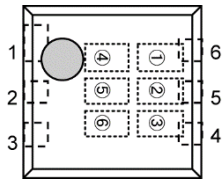
The same digits are used between the type of packages.

④ represents production lot number

0~9, A~Z repeated. (G, I, J, O, Q, W excluded.)

MARKING RULE (Continued)

● USP-6B



USP-6B
(TOP VIEW)

① represents product series

MARK	PRODUCT SERIES
6	XC6413xxxxxx
7	XC6414xxxxxx

② represents optional functions

MARK	OPTIONAL FUNCTION	PRODUCT SERIES
F	Cd Pin	XC6413/XC6414Fxxxxx

③ represents product type

MARK	EN/CE FUNCTION	VD SENSE PIN	VD OUTPUT LOGIC	PRODUCT SERIES
Y	Nonfunctional	V_{ROUT}/V_{SEN}	Detect L	XC6413/XC6414xYxxxx

④,⑤ represents output voltage

ex.)

MARK		OUTPUT VOLTAGE		PRODUCT SERIES
④	⑤	$V_{ROUT}(V)$	$V_{DOUT}(V)$	
0	1	3.2	2.5	XC6413/XC6414xx01xx

⑥ represents production lot number

0 to 9, A to Z repeated (G, I, J, O, Q, W excluded)

Note: No character inversion used.

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