ETR03005-008b

# Low ESR Cap. Compatible Positive Voltage Regulators

### ■GENERAL DESCRIPTION

The XC6206 series are highly precise, low power consumption, 3 terminal, positive voltage regulators manufactured using CMOS and laser trimming technologies. The series provides large currents with a significantly small dropout voltage.

The XC6206 consists of a current limiter circuit, a driver transistor, a precision reference voltage and an error correction circuit. The series is compatible with low ESR ceramic capacitors. The current limiter's foldback circuit operates as a short circuit protection as well as the output current limiter for the output pin.

Output voltages are internally by laser trimming technologies. It is selectable in 0.1V increments within a range of 1.2V to 5.0V. SOT-23, SOT-89 and USP-6B packages are available.

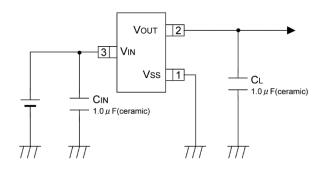
### ■ APPLICATIONS

- Smart phones / Mobile phones
- Portable game consoles
- Digital still cameras / Camcorders
- Digital audio equipments
- Reference voltage sources
- Multi-function power supplies

### ■FEATURES

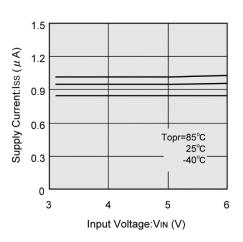
Maximum Output Current	: 200mA (3.0V type)
Dropout Voltage	: 250mV @ 100mA (3.0V type)
Maximum Operating Voltage	: 6.0V
Output Voltage Range	: 1.2V ~ 5.0V (0.1V increments)
Highly Accurate	:±2%@Vout≧1.5V
	<u>+</u> 30mV@Vout<1.5V
	( <u>+</u> 1% @Vo∪T <u>≥</u> 2.0V)
Low Power Consumption	: 1.0µA (TYP.)
Low ESR Capacitor	: Ceramic capacitor compatible
Protection	: Current Limit Circuit Built-in
<b>Operating Ambient Temperature</b>	9: -40°C ∼ 85°C
Packages	: SOT-23
	SOT-89
	USP-6B
Environmentally Friendly	: EU RoHS Compliant, Pb Free

### ■TYPICAL APPLICATION CIRCUIT

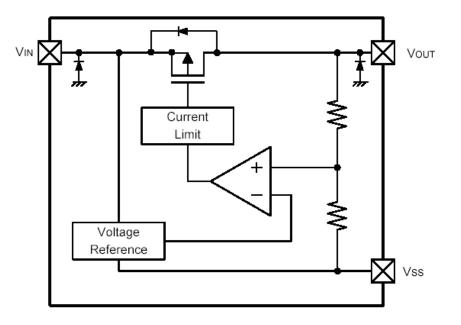


### ■ TYPICAL PERFORMANCE CHARACTERISTICS

XC6206P302



### ■BLOCK DIAGRAM



\*Diodes inside the circuit are an ESD protection diode and a parasitic diode.

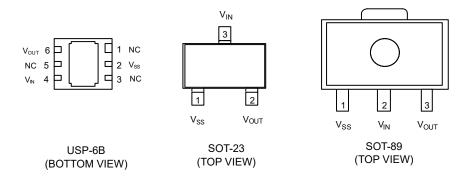
### ■PRODUCT CLASSIFICATION

●Ordering Information XC6206P①②③④⑤−⑥<sup>(\*1)</sup>

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
12	Output Voltage	12~50	e.g. Vout: 3.0V→①=3, ②=0
٩	Accuracy	2	<u>+</u> 2% (V <sub>OUT</sub> ≧1.5V), <u>+</u> 30mV (Vo∪T<1.5V)
3	3 Accuracy	1	<u>+</u> 1% (Vout≧2.0V)
	Daakagaa	MR-G	SOT-23 (3,000pcs/Reel)
45-6	Packages	PR-G	SOT-89 (1,000pcs/Reel)
	(Order Unit)		USP-6B (3,000pcs/Reel)

(\*1) The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

### ■ PIN CONFIGURATION



\*The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release.

If the pad needs to be connected to other pins, it should be connected to the pin number 4 ( $V_{IN}$ ).

### ■ PIN ASSIGNMENT

P	IN NUMBER		PIN NAME	FUNCTIONS
SOT-23	SOT-89	USP-6B		FUNCTIONS
1	1	2	Vss	Ground
3	2	4	Vin	Power Input
2	3	6	Vout	Output
-	-	1, 3, 5	NC	No Connection

### ■ABSOLUTE MAXIMUM RATINGS

				Ta=25°C	
PARAMET	PARAMETER		RATINGS	UNITS	
Input Volta	Input Voltage		-0.3 ~ 7.0	V	
Output Cu	Output Current IOUT		500 (*1)	mA	
Output Vol	tage	Vout	-0.3 ~ V <sub>IN</sub> + 0.3	V	
	SOT-23		250 (IC only)		
	301-23	Pd	500(40mm x 40mm Standard board) <sup>(*2)</sup>	- mW	
Dower Dissinction	SOT-89		500 (IC only)		
Power Dissipation			1000(40mm x 40mm Standard board) (*2)		
			120 (IC only)		
	USP-6B		1000(40mm x 40mm Standard board) (*2)		
Operating Ambient	Operating Ambient Temperature		-40 ~ 85	٥C	
Storage Temperature		Tstg	-55 ~ 125	٥C	

 $^{(*1)}I_{OUT} \leq Pd / (V_{IN}-V_{OUT})$ 

(\*2) The power dissipation figure shown is PCB mounted and is for reference only. Please refer to PACKAGING INFORMATION for the mounting condition.

### ELECTRICAL CHARACTERISTICS

Ta=25°C PARAMETER SYMBOL CONDITIONS MAX. UNITS MIN. TYP. CIRCUIT -0.03 +0.03 VOUT(T)<1.5V Output Voltage IOUT=30mA (Standard)<sup>(\*2)</sup> Vout(t)≧1.5V ×0.98 ×1.02 Vout(e)<sup>(\*3)</sup> Vout(t)<sup>(\*4)</sup> V 2 Output Voltage IOUT=30mA V<sub>OUT(T)</sub>≧2.0V ×0.99 ×1.01 (High Accuracy)(\*2) 1 Supply Current -1.0 3.0 IDD μΑ V<sub>OUT(T)</sub>≦1.8V, 1mA≦Iouт≦50mA Load Regulation ΔVουτ E-1<sup>(\*5)</sup> 2 mV Vout(t)>1.8V, 1mA≦Iout≦100mA Vdif1<sup>(\*6)</sup> E-2<sup>(\*5)</sup> Dropout Voltage 1 IOUT=30mA \_ Vout(t)≦1.8V, Iout=60mA 2 mV Vdif2(\*6) E-3<sup>(\*5)</sup> Dropout Voltage 2 \_ V<sub>OUT(T)</sub>>1.8V, I<sub>OUT</sub>=100mA  $V_{OUT(T)}$ <4.5V,  $V_{OUT(T)}$ +1.0V $\leq V_{IN} \leq 6.0V$ , Iout=30mA ΔVουτ/ 2 Line Regulation 0.05 0.25 %/V \_  $(\Delta V_{IN} \cdot V_{OUT})$ V<sub>OUT(T)</sub>≧4.5V,  $5.5V \le V_{IN} \le 6.0V$ , I<sub>OUT</sub>=30mA Maximum Output E-4<sup>(\*5)</sup> 2 Vout≧Vout(E)× 0.9 mΑ **I**OUTMAX \_ \_ Current Short Circuit Vout=Vss 2 E-5<sup>(\*5)</sup> ISHORT \_ mΑ \_ Current Input Voltage Vin 1.8 6.0 V 2 - $\Delta V_{OUT}/$ **Output Voltage** lout=30mA, 2 Temperature (∆Topr • ±100 ppm/°C -40°C≦Topr≦85°C Characteristics Vout)

\*1: Unless otherwise stated,  $V_{IN} = V_{OUT(T)} + 1.0V$ 

\*2: (Standard):±2% (1.5V≦V<sub>OUT(T)</sub>) , ±0.03V (1.5V>V<sub>OUT(T)</sub>) (High Accuracy):±1% (2.0V≦V<sub>OUT(T)</sub>)

\*3:  $V_{OUT(E)}$  :Effective output voltage.

\*4: V<sub>OUT(T)</sub> :Nominal voltage

\*5: For E-1,E-2,E-3,E-4,E-5, Please refer to Electrical Characteristics Chart.

\*6: Vdif =VIN1 -VOUT1

 $V_{OUT1}$  :A voltage equal to 98% of the output voltage whenever an amply stabilized {V\_{OUT(T)} + 1.0V} is input with each  $I_{OUT}$ .

VIN1 :The input voltage when VOUT1 appears as input voltage is gradually decreased.

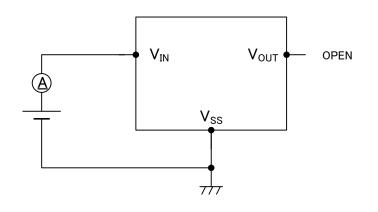
\*7: The low ESR capacitors use that is more than  $1.0\mu$ F as C<sub>L</sub> is possible.

# ELECTRICAL CHARACTERISTICS (Continued) Electrical Characteristics Chart

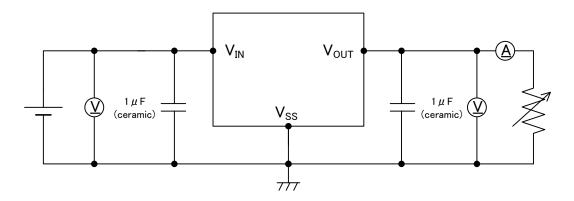
	E-1	E-	2	E	-3	E-4	E-5
NOMINAL VOLTAGE	LOAD REGULATION	DROPOUT VOLTAGE1			POUT TAGE2	MAX. OUTPUT CURRENT	SHORT CURRENT
	⊿V <sub>о∪т</sub> (mV)	V <sub>dif1</sub> (	(mV)	V <sub>dif2</sub>	(mV)	I <sub>OUTMAX</sub> (mA)	I <sub>SHORT</sub> (mA)
Vout(t)	MAX.	TYP.	MAX.	TYP.	MAX.	MIN.	TYP.
1.2		460	760	700	960		
1.3	40	400	650	700	900	<u></u>	180
1.4		350	590	500		60	
1.5		300	510	580	860		
1.6		250	450	450	040		155
1.7	45	200	410	450	810	00	
1.8		150	390			80	
1.9					780		
2.0							130
2.1							
2.2	50					120	
2.3				350			
2.4		100	370				
2.5					710		
2.6						150	
2.7	55						
2.8							
2.9							
3.0							
3.1	00						
3.2	60						
3.3							
3.4		75	350	250	680	200	
3.5 3.6							
3.0	65						100
3.8	00						
3.9							
4.0							
4.1							
4.2	70						
4.3	-						
4.4		• -					
4.5		60	320	200	630	250	
4.6							
4.7	75						
4.8							
4.9							
5.0	80	50	290	175	600		

### TEST CIRCUITS

Circuit ①



Circuit 2

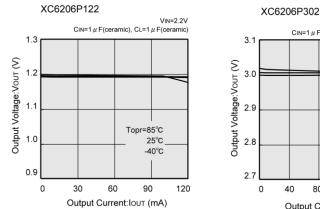


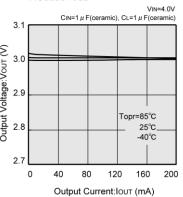
### ■NOTES ON USE

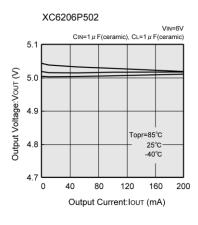
- 1. For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be exceeded.
- 2. Where wiring impedance is high, operations may become unstable due to noise and/or phase lag depending on output current. Please strengthen V<sub>IN</sub> and V<sub>SS</sub> wiring in particular
- 3. Please wire the input capacitor (C<sub>IN</sub>) and the output capacitor (C<sub>L</sub>) as close to the IC as possible.
- 4. Capacitances of these capacitors (C<sub>IN</sub>, C<sub>L</sub>) are decreased by the influences of bias voltage and ambient temperature. Care shall be taken for capacitor selection to ensure stability of phase compensation from the point of ESR influence.
- 5. When it is used in a quite small input / output dropout voltage, output may go into unstable operation. Please test it thoroughly before using it in production.
- 6. Torex places an importance on improving our products and their reliability. We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

### ■TYPICAL PERFORMANCE CHARACTERISTICS

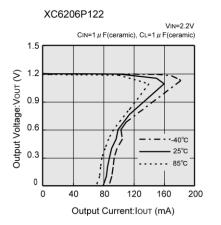
#### (1) Output Voltage vs. Output Current

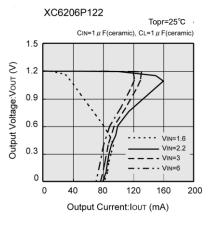


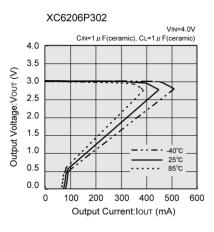


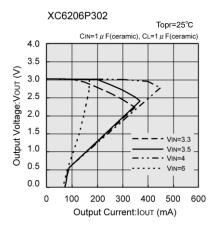


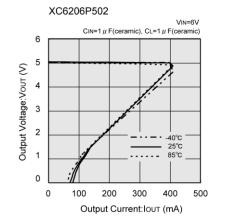
(2) Current Limit

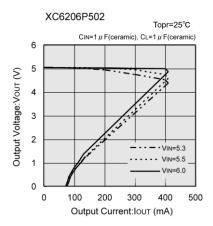






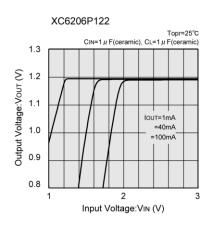


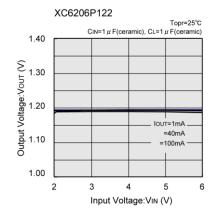


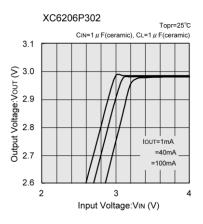


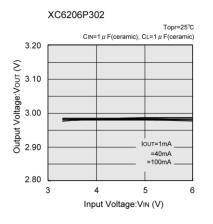
# TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

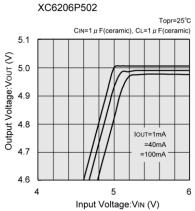
#### (3) Output Voltage vs. Input Voltage

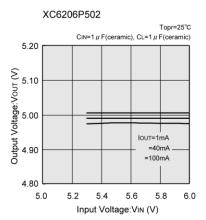




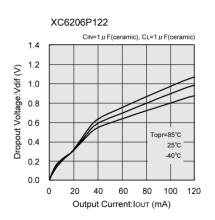


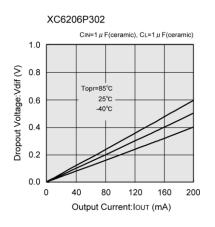


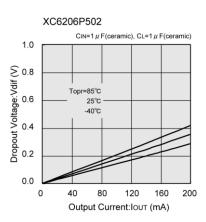




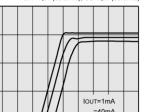
#### (4) Dropout Voltage vs. Output Current





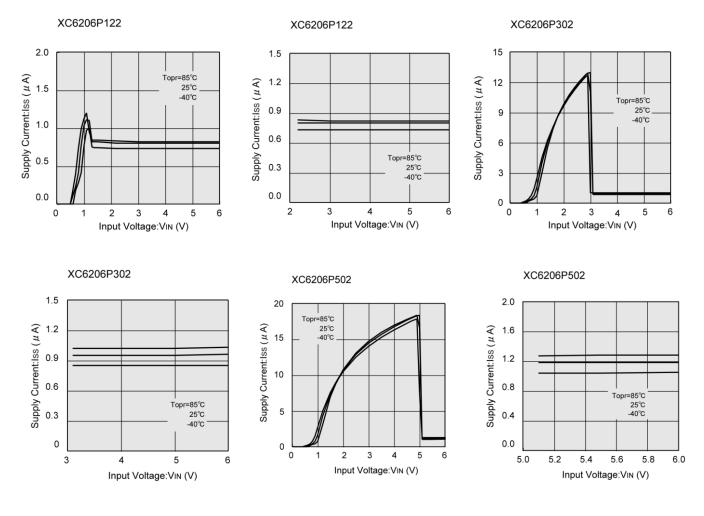




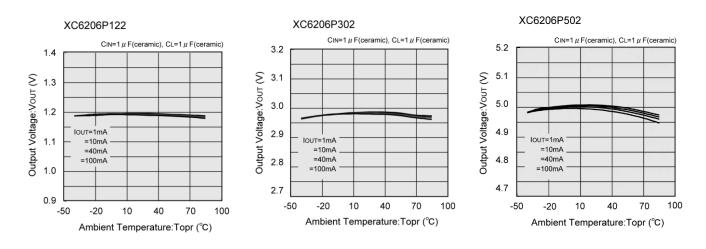


### ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

#### (5) Supply Current vs. Input Voltage

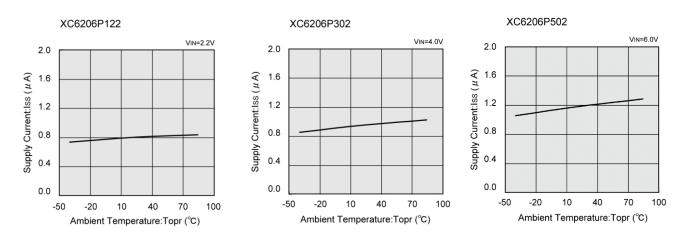


#### (6) Output Voltage vs. Ambient Temperature

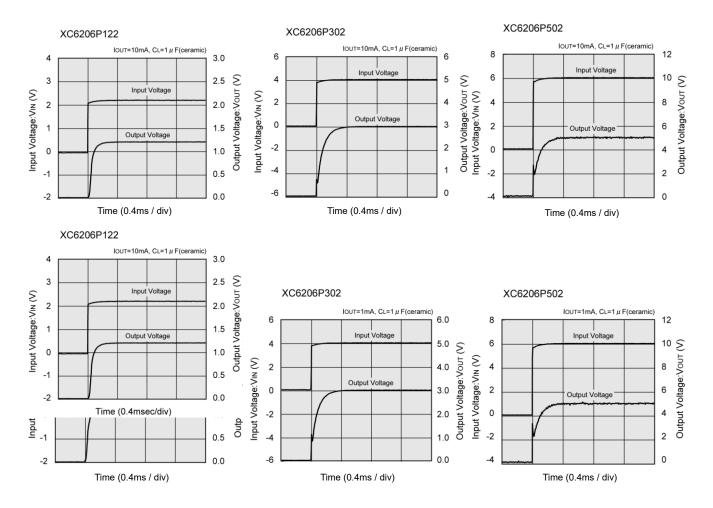


### ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

#### (7) Output Voltage vs. Ambient Temperature

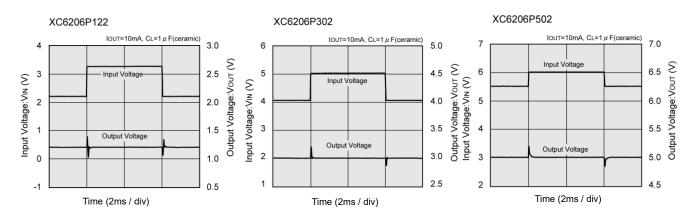


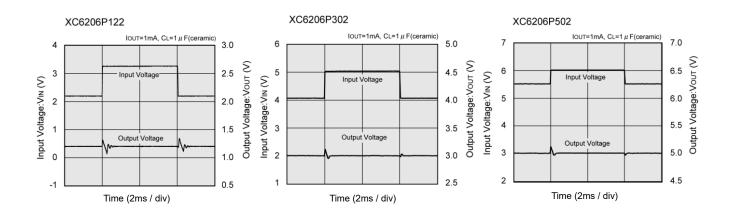
#### (8) Input Transient Response 1

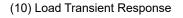


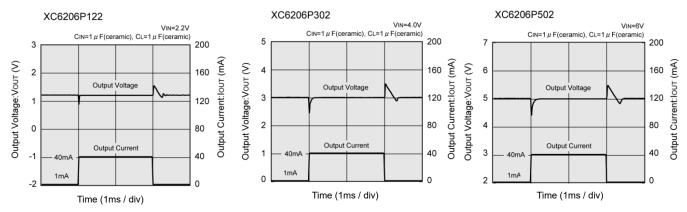
### ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(9) Input Transient Response 2



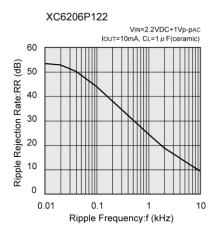


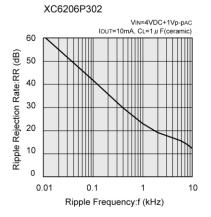


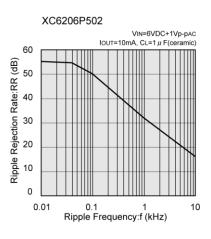


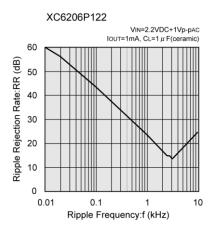
### ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

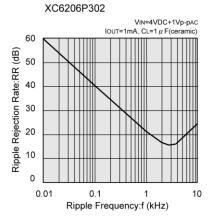
#### (11) Ripple Rejection Rate

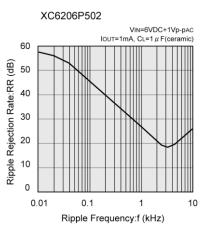












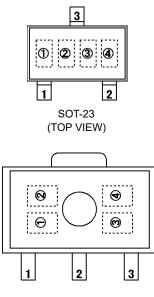
### ■ PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS
SOT-23	SOT-23 PKG	SOT-23 Power Dissipation
SOT-89	<u>SOT-89 PKG</u>	SOT-89 Power Dissipation
USP-6B	USP-6B PKG	USP-6B Power Dissipation

### MARKING RULE

#### ●SOT-23, SOT-89



SOT-89 (TOP VIEW)

#### ① represents product number

MARK	PRODUCT SERIES
6	XC6206P*****

2 represents 3 pins regulator

MA	PRODUCT SERIES	
VOLTAGE=0.1 ~ 3.0V	VOLTAGE = $3.1 \sim 6.0V$	PRODUCT SERIES
5	6	XC6206P*****

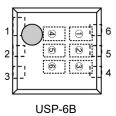
#### ③ represents output voltage

MARK	VC	VOLTAGE (V)		MARK	OUTPL	JT VOLTA	GE (V)
0	-	3.1	-	F	1.6	4.6	-
1	-	3.2	-	Н	1.7	4.7	-
2	-	3.3	-	К	1.8	4.8	-
3	-	3.4	-	L	1.9	4.9	-
4	-	3.5	-	М	2.0	5.0	-
5	-	3.6	-	N	2.1	-	-
6	-	3.7	-	Р	2.2	-	-
7	-	3.8	-	R	2.3	-	-
8	-	3.9	-	S	2.4	-	-
9	-	4.0	-	Т	2.5	-	-
А		4.1	-	U	2.6	-	-
В	1.2	4.2	-	V	2.7	-	-
С	1.3	4.3	-	Х	2.8	-	-
D	1.4	4.4	-	Y	2.9	-	-
E	1.5	4.5	-	Z	3.0	-	-

④ represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

#### OUSP-6B



(TOP VIEW)

#### 12 represents product number

MA	RK	
1	PRODUCT SERIES	
0	6	XC6206P***D*

#### ③ represents 3 pins regulator

MARK	PRODUCT SERIES
Р	XC6206P***D*

#### (4)(5) represents output voltage

MA	RK		PRODUCT SERIES
4	5	OUTPUT VOLTAGE(V)	PRODUCT SERIES
3	3	3.3	XC6206P33*D*
5	0	5.0	XC6206P50*D*

6 represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

- 1. The product and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date.
- 2. The information in this datasheet is intended to illustrate the operation and characteristics of our products. We neither make warranties or representations with respect to the accuracy or completeness of the information contained in this datasheet nor grant any license to any intellectual property rights of ours or any third party concerning with the information in this datasheet.
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- 5. Although we make continuous efforts to improve the quality and reliability of our products; nevertheless Semiconductors are likely to fail with a certain probability. So in order to prevent personal injury and/or property damage resulting from such failure, customers are required to incorporate adequate safety measures in their designs, such as system fail safes, redundancy and fire prevention features.
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