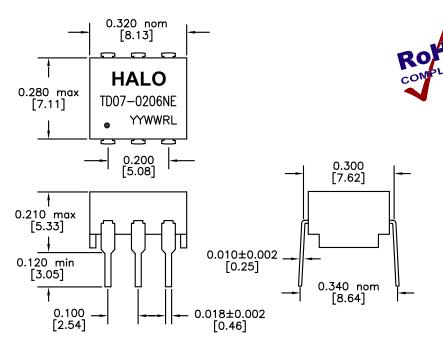
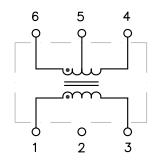
US PATENT NO. 5,656,985 6,297,720B1 6,297,721B1 6,320,489B1 6,344,785B1 6,662,431B1



DIMENSIONS: Inch [mm]

TOLERANCES: ±0.005 IF NOT SPECIFIED



PART NO.: TD07-0206NERL

6PIN DIP TRANSFORMER

DESIGNED FOR USE IN T3,DS3,E3 AND STS-1 INTERFACES

RETURN LOSS MEETS G.703 REQUIREMENTS

Rohs Compliant

COMPATIBLE TO LEAD-FREE WAVE SOLDERING PROCESS FOR THROUGH HOLE COMPONENTS

OPERATING TEMPERATURE 0-70°C

ELECTRICAL SPECIFICATIONS @25° C

TURNS RATIO

P1-3:P6-4

1:2CT±2%

OCL P1-P3

19µH min

LL P1-3

0.06µH max

BANDWIDTH (75Ω)

0.250-500MHz typ

ISOLATION

1,500Vrms



HALO/PBL

CALIFORNIA, USA KOWLOON, HONG KONG SINGAPORE

	TITLE	ISOLATION TRANSFORMER			SIGNATURES		DATE	REV.	DESC.	DATE
ı	FOR	T3/DS3/E3/STS-1			DRAWN	PETER LU	1/2/08	Α	FIRST ISSUE	1/2/08
+	DART NO	10/000/10/010-1				LEI KEONG	1/2/08			
	PART NO. TD07-0206NERL				APPROVED	PETER LU	1/2/08			
	SCALE	NONE	PAGE	1 of 1	FILE	TD07-0206NERL.DWG				

Mouser Electronics

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