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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RJK4013DPE

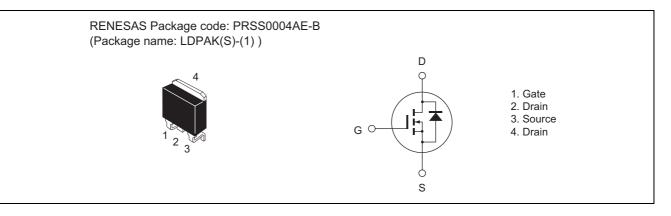
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G1513-0200 Rev.2.00 Jul 02, 2009

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$ Item Symbol Ratings Unit Drain to source voltage V_{DSS} 400 V V Gate to source voltage V_{GSS} ±30 17 Drain current A I_D Note1 Drain peak current 51 A Body-drain diode reverse drain current I_{DR} 17 А Body-drain diode reverse drain peak current I_{DR (pulse)} 51 А I_{AP}^{Note3} Avalanche current 6 A E_{AR}^{Note3} 2 Avalanche energy mJ Pch Note2 W Channel dissipation 100 Channel to case thermal impedance θch-c 1.25 °C/W Channel temperature Tch 150 °C °C Storage temperature Tstg -55 to +150

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. Value at Tc = 25°C

3. STch = 25° C, Tch $\leq 150^{\circ}$ C



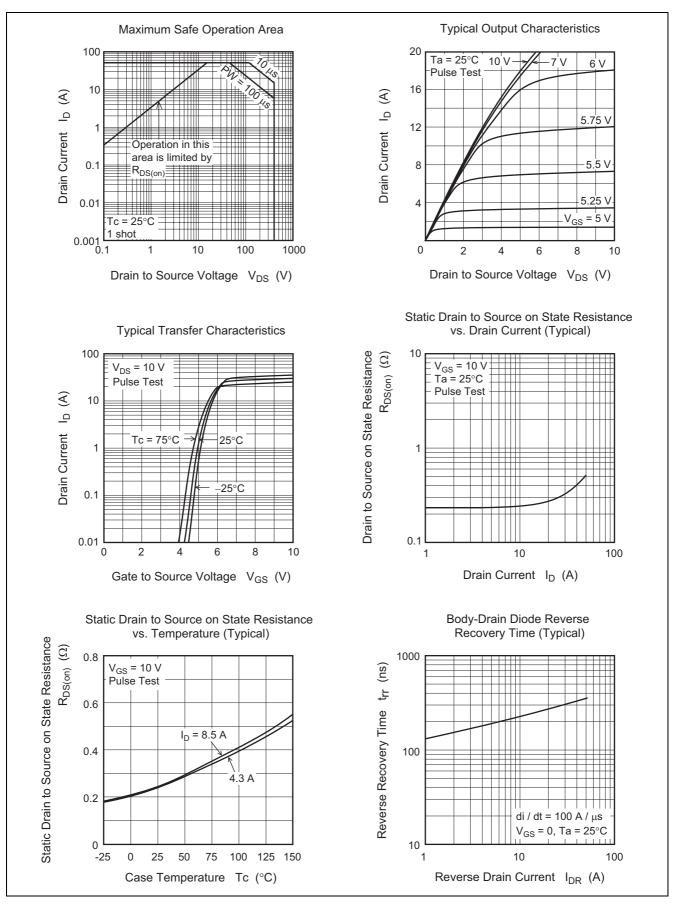
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Мах	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	400		—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	1	μΑ	$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}		—	±0.1	μΑ	$V_{GS} = \pm 30$ V, $V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	_	0.25	0.30	Ω	$I_D = 8.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	1450	_	pF	$V_{DS} = 25 V$ $V_{GS} = 0$ f = 1 MHz
Output capacitance	Coss	_	175	—	pF	
Reverse transfer capacitance	Crss	_	21	—	pF	
Turn-on delay time	t _{d(on)}	_	33	—	ns	I _D = 8.5 A
Rise time	tr	_	28	_	ns	$V_{GS} = 10 V$ $R_L = 23.5 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t _{d(off)}	_	84	_	ns	
Fall time	t _f	_	15	_	ns	
Total gate charge	Qg	—	38	—	nC	V _{DD} = 320 V
Gate to source charge	Qgs	_	8	—	nC	V _{GS} = 10 V I _D = 17 A
Gate to drain charge	Qgd	_	17	—	nC	
Body-drain diode forward voltage	V _{DF}		0.9	1.5	V	$I_F = 17 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t _{rr}	_	260	_	ns	$I_F = 17 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

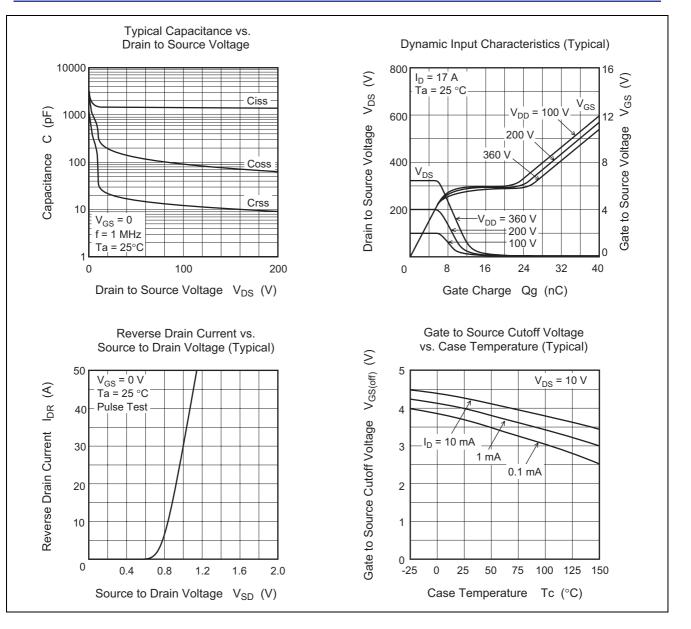
Notes: 4. Pulse test



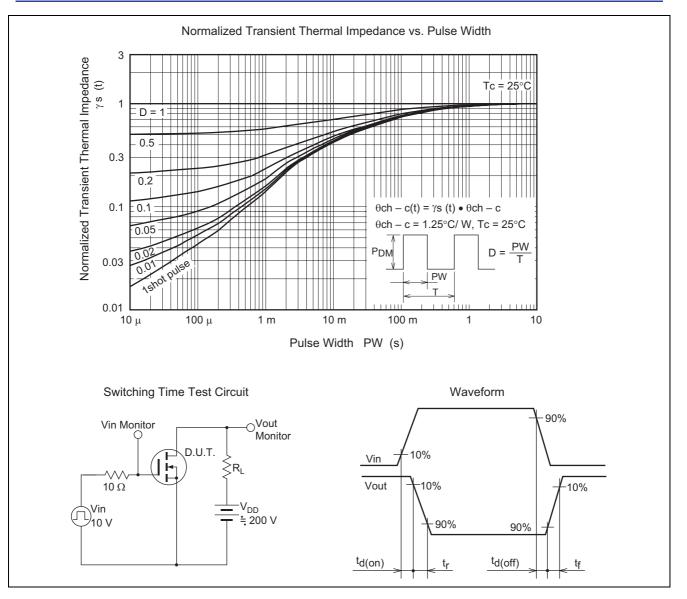
Main Characteristics



RENESAS



RENESAS



RENESAS

Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]	Unit: mm
DPAK(S)-(1)	SC-83	PRSS0004AE-B LDPA	K(S)-(1) / LDPAK(S)-(1)V	1.30g	Onit. Initi
	(c) (c) (c) (c) (c) (c) (c) (c) (c) (c)	± 0.3 (7) + 0.3 (7) + 0		└── ↓││{Ź· ́́∠ く〉 ⟨♪ く〉	-

Ordering Information

Part No.	Quantity	Shipping Container
RJK4013DPE-00-J3	1000 pcs	Taping



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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd. Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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