

RX65N Group

Cloud Kit for RX65N Microcontroller Group CK-RX65N Quick Start Guide

Renesas RX Family RX600 Series

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Rev. 1.00 May 2022

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(Rev.5.0-1 October 2020)

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1. Precaution against Electrostatic Discharge (ESD)

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2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power is supplied until the power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

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This Cloud Kit is only intended for use in a laboratory environment under ambient temperature and humidity conditions. A safe separation distance should be used between this and any sensitive equipment. Its use outside the laboratory, classroom, study area, or similar such area invalidates conformity with the protection requirements of the Electromagnetic Compatibility Directive and could lead to prosecution.

The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. There is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures:

• Ensure attached cables do not lie across the equipment.

- · Reorient the receiving antenna.
- Increase the distance between the equipment and the receiver.
- Connect the equipment into an outlet on a circuit different from that which the receiver is connected.
- · Power down the equipment when not in use.
- · Consult the dealer or an experienced radio/TV technician for help.
- Note: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken:

- The user is advised that mobile phones should not be used within 10 m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Cloud Kit does not represent an ideal reference design for an end product and does not fulfill the regulatory standards for an end product.



Renesas RX Family

CK-RX65N

Contents

1.	Introduction	7
1.1	Assumptions and Advisory Notes	7
2.	Kit Contents	7
3.	Overview of the Quick Start Example Project	8
3.1	Quick Start Example Project Flow	8
4.	Running the Quick Start Example Project	9
4.1	Connecting and Powering Up the CK-RX65N Board	9
4.2	Running the Quick Start Example Project	10
5.	Customizing the Quick Start Example Project	16
5.1	Downloading and Installing Software and Development Tools	16
5.2	Downloading and Importing the Quick Start Example Project	17
5.3	Modifying, Generating, and Building the Quick Start Example Project	22
5.4	DHCP and Static IP settings	24
5.5	Setting Up Debug Connection between the CK-RX65N board and HostPC	24
5.6	Downloading and Running the Modified Quick Start Example Project	25
5.7	Firewall Dialogue	26
6.	Next Steps	27
7.	Website and Support	27
Rev	vision History	28

Figures

Figure 1.	CK-RX65N Kit Contents	7
Figure 2.	Quick Start Example Project Flow	8
Figure 3.	Connecting the CK-RX65N Board to the Host PC via USB Serial Port	9
Figure 4.	USB Serial Device in Windows Device Manager	10
Figure 5.	Selecting the Serial Port on Tera Term	11
Figure 6.	Select Communication Settings	11
Figure 7.	Welcome and Main Menu	12
Figure 8.	Kit Information	12
Figure 9.	Loading Sensor List	13
Figure 10.	Sensor List with All Data	13



Figure 11.	Web Server	14
Figure 12.	Using DHCP	14
Figure 13.	Using Static IP	15
Figure 14.	Browser View	15
Figure 15.	Next Steps	16
Figure 16.	Creating a New Workspace	17
Figure 17.	Launching the Workspace	17
Figure 18.	Importing the Project	18
Figure 19.	Importing Existing Projects into the Workspace	18
Figure 20.	Clicking Next to Import Existing Projects into the Workspace	19
Figure 21.	Selecting the Root Directory	20
Figure 22.	Finishing Importing the Quick Start Example Project	21
Figure 23.	Opening the Configurator	22
Figure 24.	Opening the Configurator	22
Figure 25.	Modifying the Configuration Settings	23
Figure 26.	Generate Code from Smart Configurator	23
Figure 27.	Building the Project	23
Figure 28.	Successful Build Output	23
Figure 29.	DHCP Setting in FreeRTOS_Kernel Component	24
Figure 30.	Changing the MAC, IP, and netmask	24
Figure 31.	Connecting the CK-RX65N Board to the Host PC via USB Debug Port	24
Figure 32.	Selecting the Debug Option	25
Figure 33.	Selecting the Debug Image	25
Figure 34.	Opening the Debug Perspective	26
Figure 35.	Executing the Project	26



1. Introduction

This Quick Start Guide (QSG) provides:

- An overview of the Quick Start example project that the CK-RX65N board comes pre-programmed with.
- Instructions for running the Quick Start example project.
- Instructions for importing, modifying, and building the Quick Start example project using Flexible Integration Technology (FIT) and e² studio Integrated Development Environment (IDE).

1.1 Assumptions and Advisory Notes

- 1. Tool experience: It is assumed that the user has prior experience working with IDEs such as e2 studio and terminal emulation programs such as Tera Term.
- 2. Subject knowledge: It is assumed that the user has basic knowledge about microcontrollers, embedded systems, and FIT to modify the example project described in this document.
- 3. Prior to running the Quick Start example project or programming the CK-RX65N board, default jumper settings must be used. Refer to the CK-RX65N user's manual for the default jumper settings.
- 4. The screen shots provided throughout this document are for reference. The actual screen content may differ depending on the version of software and development tools used.

2. Kit Contents

The following components are included in the kit:

- 1. CK-RX65N v1 board
- 2. RYZ014A PMOD (CAT-M1 Cellular Kit)
- 3. SIM card (CAT-M1 Cellular Kit)
- 4. USB Micro B to USB A adapter cable
- 5. USB Micro B to USB A cable x2
- 6. Antenna (CAT-M1 Cellular Kit)

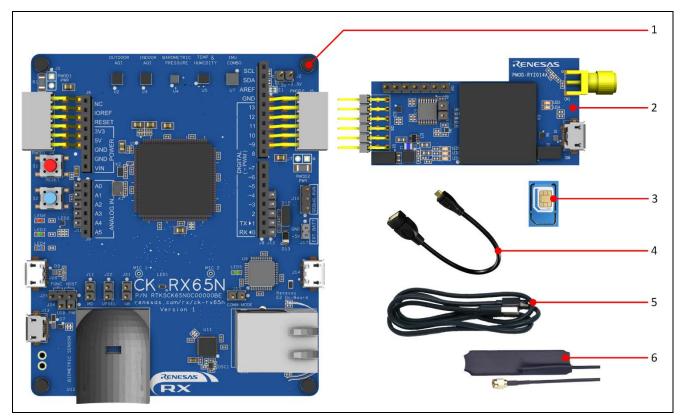


Figure 1. CK-RX65N Kit Contents



3. Overview of the Quick Start Example Project

The Quick Start example project allows the user to change the frequency of the on-board user LED3 (blue) using the user button (S2). The supported frequencies are 1 Hz, 5 Hz, and 10 Hz.

When the CK-RX65N board running the Quick Start example project is connected to a host PC via USB as a Full Speed CDC Device, the kit information, MCU die temperature, and user LED blinking frequency are displayed on a terminal console.

3.1 Quick Start Example Project Flow

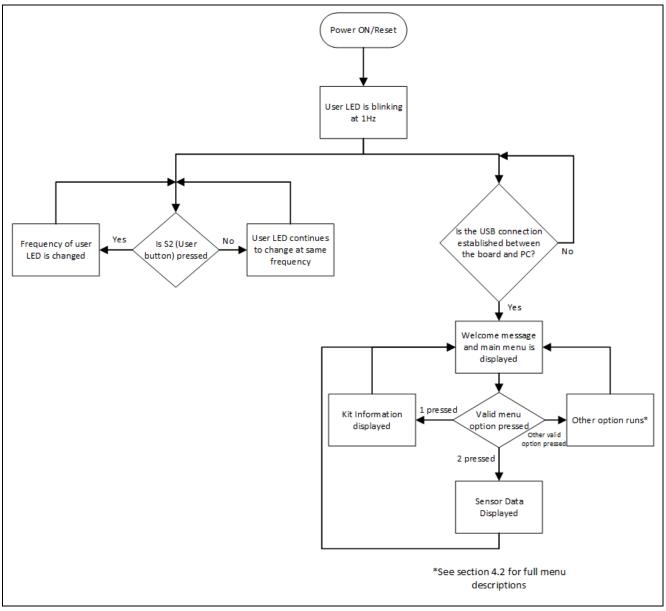


Figure 2. Quick Start Example Project Flow



4. Running the Quick Start Example Project

This section lists the requirements and instructions to power up the CK-RX65N board and run the Quick Start example project.

Hardware Requirements

- CK-RX65N board
- Micro USB device cable x2
- A PC with at least 2 USB ports
- A router with at least 1 available full duplex Ethernet port*
- Ethernet cable

* The PHY implemented on the Cloud Kit does not support half-duplex operation.

Software Requirements

- Windows® 10 operating system
- USB Serial Drivers (included in Windows 10)
- Tera Term (or similar) terminal console application

4.1 Connecting and Powering Up the CK-RX65N Board

- 1. Check that:
 - A. J25 is set to link pins 2-3
 - B. J24 link is closed
 - C. J16 is set to link pins 2-3 (RUN)
- 2. Connect J14 and J20 on the CK-RX65N board to USB ports on the host PC using the 2 micro USB cables supplied.
- Power LED (LED1) on the CK-RX65N board lights up white, indicating that the CK-RX65N board is powered on.
- Note: If the CK-RX65N board is not powered through the Debug port (J14) the current available to the board may be limited to 100 mA.

When using the supplied RYZ014A Pmod module with other code (found here: <u>RYZ014A - LTE Cat-M1 Cellular loT Module | Renesas</u>) be aware that this Pmod has a maximum operating current of 480 mA dependent upon the LTE band, Tx/Rx settings, and network coverage. Please ensure that the host board can supply sufficient power or provide supplemental USB power via CN4 on the Pmod to avoid RF instability.

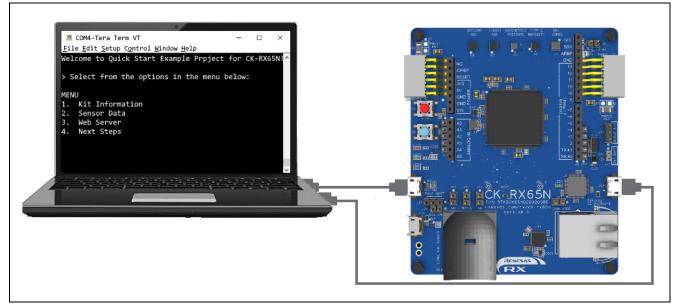


Figure 3. Connecting the CK-RX65N Board to the Host PC via USB Serial Port



4.2 Running the Quick Start Example Project

To run the Quick Start example project, use the following instructions:

- 1. On power up or RESET, the four user LEDs will take on the following states:
 - LED2 RGB Off
 - LED3 Green Steady, full intensity
 - LED4 Blue Blinking at 1 Hz frequency
 - LED6 Red Off
- Press the user button (S2) on the CK-RX65N board to change the blinking frequency of the user LED4 (blue). With every press of the first user button (S2), the frequency will switch from 1 Hz to 5 Hz to 10 Hz and cycle back.
- 3. On the host PC, open Windows Device Manager. Expand **Ports (COM & LPT)**, locate **USB Serial Device (COMxx)** and note down the COM port number for reference in the next step.

📇 Device Manager	_	×
Eile <u>A</u> ction <u>V</u> iew <u>H</u> elp		
✓ 昔 MyPC		
> I Audio inputs and outputs		
> 😹 Batteries		
> 📓 Biometric devices		
> 🚯 Bluetooth		
>		
> 🛄 Computer		
> Disk drives		
> 🔙 Display adaptors		
Firmware		
> 🏧 Human Interface Devices		
> 🕎 Jungo Connectivity		
> 🔤 Keyboards		
> 📗 Mice and other pointing devices		
> Monitors		
> 🚅 Network adapters		
> 📃 Portable Devices		
🗸 🛱 Ports (COM & LPT)		
🛱 Intel(R) Active Management Technology - SOL (COM3)		
🛱 USB Serial Device (COM4)		
> 🚍 Print queues		
> 🔲 Processors		
> 📲 Security devices		
> 🔚 Sensors		
> 📲 Software components		
> Software devices		
> 🐗 Sound, video and game controll		
> 🍇 Storage controllers		
> 🏣 System devices		
> 🏺 Universal Serial Bus controllers		
> 🏺 USB Connector Managers		

Figure 4. USB Serial Device in Windows Device Manager



4. Open Tera Term, select Serial and COMxx: USB Serial Device (COMxx) and click OK.

○TCP/ <u>I</u> P	Hos <u>t</u> :	myhost.exam	iple.com		~
		⊻ Hist <u>o</u> ry	TOD		
	Service:	⊖ Te <u>I</u> net	TCP <u>p</u> or	t#; ZZ	
		<u>s</u> sh	SSH <u>v</u> ersion:	SSH2	\sim
		○ Other	Proto <u>c</u> ol:	UNSPEC	~
⊚ S <u>e</u> rial	Po <u>r</u> t:	COM4: USB S	Serial Device (CC)M4)	~
• 5 <u>e</u> riai	PO <u>r</u> t.	COM4. 03B .		/114)	J

Figure 5. Selecting the Serial Port on Tera Term

5. Using the **Setup** menu pull-down, select **Serial port...** and ensure that the Baud rate is set to 115200, data is set to 8 bit, parity is set to none, and stop is set to 1 bit, as shown below.

Tera Term: Serial port setup		×
Port:	COM4 ~	ок
<u>B</u> aud rate:	<mark>115200</mark> ~	
<u>D</u> ata:	8 bit 🗸	Cancel
P <u>a</u> rity:	none ~	
<u>S</u> top:	1 bit ~	<u>H</u> elp
<u>F</u> low control:	none ~	
Transmit delay	<u>c</u> har <mark>0</mark> m	isec/ <u>l</u> ine

Figure 6. Select Communication Settings



6. Complete the connection. The 'welcome and main menu' screen will be displayed.

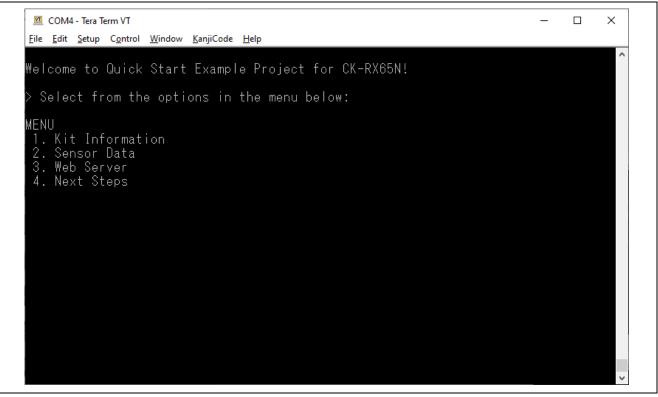


Figure 7. Welcome and Main Menu

7. Press 1 to display the **Kit Information** including the kit name, part number, MCU ID, MCU die temperature, and the user LED's current blinking frequency.

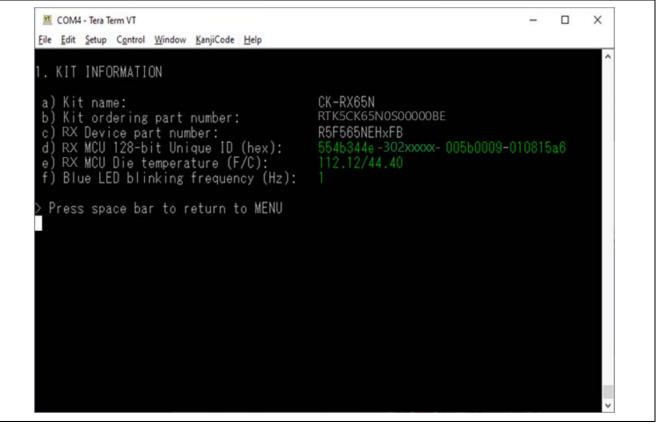


Figure 8. Kit Information



- 8. Press space to return to the 'welcome and main menu' screen.
- 9. Press **2** to display the **Sensor Data**. It will display a list of all of the on board sensors along with their readings. Some of these sensors may take some time to provide data, the data will be output as soon as it is available.

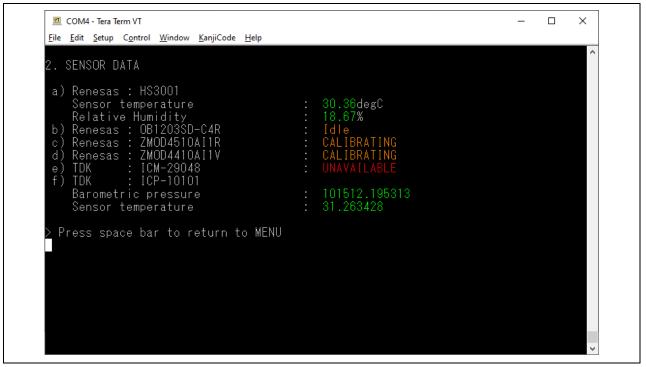


Figure 9. Loading Sensor List

The following figure shows the output when all data is available.

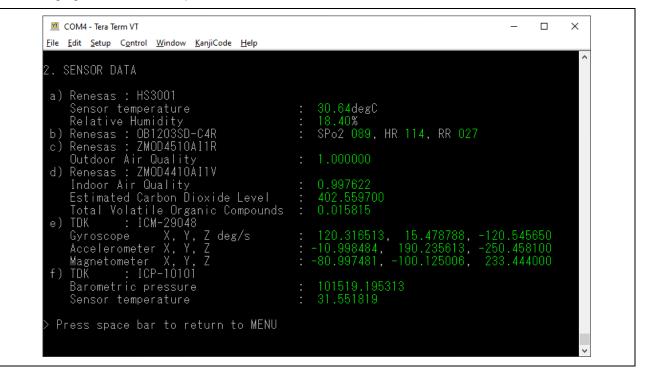


Figure 10. Sensor List with All Data



- 10. Press space to return to the 'welcome and main menu' screen.
- 11. Press **3** to display the **Web Server**. This application hosts a web server on the CK-RX65N kit showing communication with the host PC as a remote client.

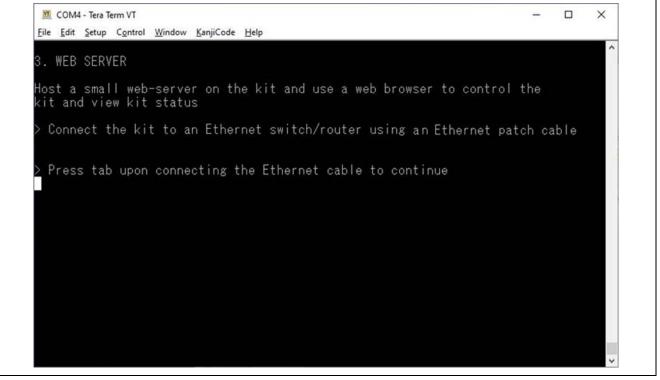


Figure 11. Web Server

12. Connect the Ethernet cable and press tab.

The CK-RX65N as supplied, is configured to use DHCP for IP address resolution. Upon successful connection, the following is displayed.



Figure 12. Using DHCP



If the DHCP fails to resolve a route or the DHCP server has been disabled, the application uses the static IP defined in the project. The following should be displayed.



Figure 13. Using Static IP

- Note: If desired, the user may configure DHCP/static IP and MAC address using the project configuration (see section 5.4). Save the configuration and re-build, download the project to see the effects of the changes.
- 13. Once a successful network connection is established, open the web browser on the host PC. Type the IP address of the CK-RX65N kit as shown in the Tera Term window in the address bar of the web browser. The following should be displayed in the web browser.



Figure 14. Browser View



14. In Tera Term, press **space** to return to the 'welcome and main menu' screen.

15. Press 4 to display Next Steps.

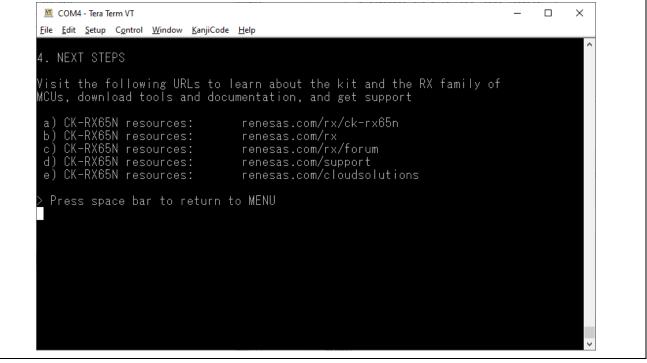


Figure 15. Next Steps

Press **space** to return to the 'welcome and main menu' screen.

5. Customizing the Quick Start Example Project

This section lists the requirements and instructions for customizing the Quick Start example project.

Hardware Requirements

- CK-RX65N board
- Micro USB device cable x2
- A PC with at least 2 USB ports
- A router with at least 1 available full duplex Ethernet port*
- Ethernet cable

Software Requirements

- Windows[®] 10 operating system
- e² studio IDE
- Quick Start example project

5.1 Downloading and Installing Software and Development Tools

Before the Quick Start example project can be modified, it is necessary to download and install software and development tools on the host PC.

E2OB USB drivers, and e² studio are bundled in a downloadable platform installer available on the Renesas website at <u>renesas.com/rx/ck-rx65n</u>.

There is no need to download and install software, development tools, and drivers separately.



5.2 Downloading and Importing the Quick Start Example Project

- 1. Download and extract the Quick Start example project to a local directory on the host PC.
 - The Quick Start example project (source code and project files) is available in the CK-RX65N Example Projects Bundle that is available in the Downloads tab of CK-RX65N webpage at <u>renesas.com/rx/ck-rx65n</u>
 - Download and extract the example projects bundle (r20an0680eu0100-ck-rx65nexampleprojects.zip) to a local directory on the host PC.
 - Browse to the Quick Start example project at r20an0680eu0100-ck-rx65nexampleprojects\ck_rx65n_quickstart\quickstart_ck_rx65n_ep
- 2. Launch e² studio.
- 3. Browse to the Workspace where the project file is to be imported. Enter the name in the Workspace dialog box to create a new workspace.

e² studio Launcher		\times
Select a directory as workspace		
e ² studio uses the workspace directory to store its pre-	ferences and development artifacts.	
<u> </u>		
Workspace: C:\temp	✓ <u>B</u> rowse	2
Use this as the default and do not ask again		
• <u>R</u> ecent Workspaces		
	Launch Cano	rel

Figure 16. Creating a New Workspace

4. Click Launch.

e ² studio Launcher		×
Select a directory as workspace		
e ² studio uses the workspace directory to store its pr	references and development a	artifacts.
<u>W</u> orkspace: [♥] C:\temp	~	<u>B</u> rowse
Use this as the default and do not ask again		
• <u>R</u> ecent Workspaces		
	Launch	Cancel
	Launch	Cancel

Figure 17. Launching the Workspace

5. Click **Import** from the **File** drop-down menu.

۵,	New Open File Open Projects from File S Recent Files	System	Alt+Shift+N >
	Close Editor Close All Editors		Ctrl+W Ctrl+Shift+W
	Save Save As Save All Revert		Ctrl+S Ctrl+Shift+S
8 2	Move Rename Refresh Convert Line Delimiters T	<u>-</u> 0	F2 F5 >
ð	Print		Ctrl+P
	Import Export	Import	
	Properties		Alt+Enter
	Switch Workspace Restart Exit		>

Figure 18. Importing the Project

6. In the Import dialog box, select General, and then select Existing Projects into Workspace.

Select Choose import wizard.	 ×	
Select an import wizard: type filter text General General CMSIS Pack CMSIS Pack CMSIS Pack Existing Projects into Workspace File System Preferences Projects from Folder or Archive Rename & Import Existing C/C++ Project into Workspace Renesas CS+ Project for CA78K0R/CA78K0 Renesas CS+ Project for CC-RX and CC-RL C/C++ C/	×	
? < Back Next > Finish	Cancel	

Figure 19. Importing Existing Projects into the Workspace



7. Click Next.

Figure 20. Clicking Next to Import Existing Projects into the Workspace



8. Click **Select root directory** and click **Browse** to go to the location of the Quick Start example project folder.

Import			×
Import Projects			<u>`</u>
Select a directory to search for existing Eclipse projects.			_1
Select root directory:	~	Brows	e
O Select archive file:	~	Brows	e
Projects:			
		Select	All
		Deselec	t All
		Refre	sh
Options]	
Search for nested projects			
Copy projects into workspace Close newly imported projects upon completion			
Hide projects that already exist in the workspace			
Working sets			
Add project to working sets		New	
Working sets:	~	Select	
Rack Next >	Finish	Canc	el

Figure 21. Selecting the Root Directory



9. Select the Quick Start example project and click **Finish.**

🔄 Import		_	
mport Projects Select a directory to sea	arch for existing Eclipse projects.		
Select roo <u>t</u> directory:	C:\temp\rx\mp\	~	B <u>r</u> owse
Select <u>a</u> rchive file:		~	B <u>r</u> owse
Projects: ✓ rx65n_ck_quicksta	art (C:\temp\rx\mp\quickstart_ck_rx65n_ep)		Select All
			Deselect All
			R <u>e</u> fresh
Options Search for nested pr	ojects		
Search for nested pr Copy projects into w Close newly imported	orkspace d projects upon completion		
Search for nested pr Copy projects into w Close newly imported	orkspace		
Search for nested pr Copy projects into w Close newly importe Hide projects that al	vorkspace d projects upon completion ready exist in the workspace		Ne <u>w</u>
 Search for nested pr Copy projects into w Close newly importe Hide projects that al Working sets 	vorkspace d projects upon completion ready exist in the workspace	~	Ne <u>w</u> S <u>e</u> lect
Search for nested pr Copy projects into w Close newly importe Hide projects that al Working sets Add project to worl	vorkspace d projects upon completion ready exist in the workspace	~	

Figure 22. Finishing Importing the Quick Start Example Project



5.3 Modifying, Generating, and Building the Quick Start Example Project

This section provides instructions to modify the Quick Start example project. The Quick Start example project can be modified by editing the source code and reconfiguring the properties of the MCU peripherals, pins, clocks, interrupts, and so forth.

- Note: The specific modifications that can be performed to the Quick Start example project are not prescribed in this QSG.
- Once the Quick Start example project is imported, click the quickstart_ck_rx65n_ep.scfg file to open the configurator. The configurator provides an easy to use interface to configure the properties of the MCU peripherals.

		0-4-9 3 4 8 0-0	▶・ が・2110日月1月1日(2112) ● 1111 ● 1111 ● 1111 ● 1111 ● 1111 ● 1111	Q 행 R C/C++ Smart Configurator
	@ quickstart_ck_x65n_ep.scfg ×	T D MCU/MPU Package × T D		
 ✓ Größ, ck. quickstart [HardwareDebug] I Binaires Ø Irc Ø Irc I HardwareDebug I HardwareDebug I HardwareDebug I guickstart, ck. nof5n, ep.rcpc I guickstart, ck. nof5n, ep. HardwareDebug I nof5n, ck. guickstart.rcpc 	Overview information		5) Generate C	ode - Generate Report
	- General Information			v ^
	Image: Coverview Get an operation of the features provided by Smart Configurator. Application Code Software Components Image: Configurator elated videos Image: Configurator elated videos Software Components Image: Configurator elated videos Image: Configurator elated videos What's new Image: Configurator elated videos Image: Configurator elated videos Image: Configurator elated videos What's new Image: Configurator elated videos Image: Configurator elated videos Image: Configurator elated videos What's new Image: Configurator elated videos Image: Configurator elated videos Image: Configurator elated videos What's new Image: Configurator elated videos Image: Configurator elated videos Image: Configurator elated videos MCU Hardware Image: Configurator elated videos Image: Configurator elated videos Application Notes Image: Configurator elated videos Image: Configurator elated videos Application Notes Image: Configurator elated videos Image: Configurator elated videos Application Notes Image: Configurator elated videos			
	- Current Configuration			
	Selected board/device: RSF565NEHxFB (F	IOM size: 2MB, RAM size: 640KB, Pin cou	int: 144)	
	Generated location (PROJECT_LOC\): src1	smc_gen	Edit	
	Selected components:			
	Component O ADC Driver (r_s12ad_nx)	Version Configuration 4.90 r_s12ad_rx(used) 6.20 r_bsp(used)		<u>^</u>
	Abc Driver (Strady) Board Support Packages, (r_bsp) Compare Match Timer FineMIOS Kernel Overview Board Clocks System Compone	2.2.0 Config_CMT1(CN 1.0.102 FreeRTOS Kernel		
Common and the second se	Board Support Packages. (r_bsp) Compare Match Timer FreeRTOS. Kernel	2.2.0 Config_CMT1(CN 1.0.102 FreeRTC/S Kernel Interrupts	Ginorf)	
Console × consoles to display at this time.	Board Support Packages. (r_bsp) Compare Match Timer FreeRTOS. Kernel	2.2.0 Config_CMT1(CN 1.0.102 FreeRTC/S Kernel Interrupts		
Console ×	Board Support Packages. (r_bsp) Compare Match Timer FreeRTOS. Kernel	2.2.0 Config_CMT1(CN 1.0.102 FreeRTC/S Kernel Interrupts		

Figure 23. Opening the Configurator

- 2. For example, in the **Components** tab of the configurator, the user can click to select a module to modify its configuration settings, as required. Figure 25 illustrates modifying the **IRQ** configuration.
- Note: To access the component properties, the **view** must be set to **Smart Configurator**. Using the Open Perspective button, if necessary.

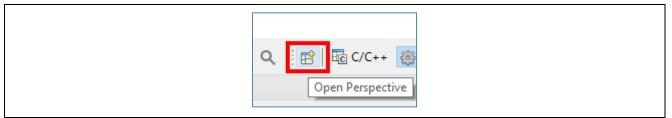


Figure 24. Opening the Configurator



Renesas RX Family

Project Explorer × E 😵 🖓 💈 = 🗆								MCU/MPU Package ×
> & Binaries	Software component configuration Generate Report					and the second second		
si Includes de src	Components 👜 🖾 🗄 🔆 🕂 🛸 💌	Configure					0 1	AND CONTRACTORS OF THE OWNER.
HardwareDebug	÷ 5	Software interrupt setting						
> 😂 trash	type filter text	Software interrupt	Prior	ty Level 15 (highest)				
quickstart_ck_rx65n_ep.rcpc	Startup Seneric	Software interrupt 2	Prior	V Level 15 (highest)				
e uickstart_ck_rx65n_ep HardwareDebug	Seneric Seneric	NMI pin interrupt setting						
rx65n_ck_quickstart.rcpc		NMI pin interrupt	Detection by	Falling edge	Expital filter No filter 0	(MHz)		
	Config_ICU	IRQ0 setting						
		IRQ0	Detection typ	c Low level	Digital filter No filter 0	(941Hz)		
			Prior	Level 15 (highest)				
		IRQ1 setting						Renesas
		IRQ1	☑ IRQ1 Detection type	n type Rising edge v Digital filter No filter v	Digital filter No filter 👻 0	 (MHz) 		·
			Priori	ty Level 2 ~				
		IRQ2 setting						A CHARLE MARKED MILLION
		IRQ2	Detection by	Low level	Digital filter No filter 0	(MH20		
			Prior	V Level 15 (highest)				
		IRQ3 setting						
		IRQ3	Detection by	c Low level	Cigital filter No filter 0	(5,6Hz)		
			Phon	V Level 15 (highest) -			2	
	Overview Board Clocks System Components Pir	s Interrupts						Legend
Console ×		H 🛛 🔸		Configuration Problems ×				8.1 -
consoles to display at this time.				0 items				
				Description	Type			

Figure 25. Modifying the Configuration Settings

3. After the desired modifications are made, click Generate Code.

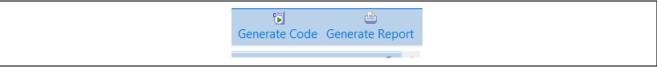


Figure 26. Generate Code from Smart Configurator

- 4. Modify the source files in the **/src** folder as needed and save the changes.
- 5. Build the project by clicking the build icon.



Figure 27. Building the Project

6. A successful build produces an output as follows.

Converting the DWARF information.... Constructing the output ELF image.... Saving the ELF output file rx65n_ck_quickstart.x 'Build complete.' 12:42:49 Build Finished. 0 errors, 0 warnings. (took 23s.930ms)

Figure 28. Successful Build Output



5.4 DHCP and Static IP settings

To modify the Ethernet based sample to enable/disable DHCP modify the file src/frtos_config/FreeRTOSIPConfig.h and set ipconfigUSE_DHCP as either 1 or 0 to enable or disable

	ess and gateway address from a DHCP server. If DS+TCP will use a static IP address. The
	atic IP address even when ipconfigUSE_DHCP is
* set to 1 if a valid configuration	cannot be obtained from a DHCP server for any
0	used is that passed into the stack by the
<pre>* FreeRTOS_IPInit() function call. *</pre>	*/
#define ipconfigUSE_DHCP	1
#define ipconfigDHCP_REGISTER_HOSTNAM	ME 1
<pre>#define ipconfigDHCP_USES_UNICAST</pre>	1
#define ipconfigDHCP SEND DISCOVER AF	FTER AUTO IP 1

Figure 29. DHCP Setting in FreeRTOS_Kernel Component

If DHCP is disabled, set the static IP address, net-mask and gateway address, to suit your local network.

In quickstart_ck_rx65n_ep.scfg, go to the components tab and select freeRTOS_Kernel. In that section, you can change the MAC address, IP address, and netmask as shown in the following figure.

Drivers	# intqHIGHER_PRIORITY	(configMAX_PRIORITIES - 3)	
> 🧽 Middleware	# Mac address 0	0x01	
V 🗁 RTOS	# Mac address 1	0x12	
V 😂 RTOS Kernel	# Mac address 2	0x13	
FreeRTOS_Kernel	# Mac address 3	0x10	
- 🗁 RTOS Object	# Mac address 4	0x15	
FreeRTOS_Object	# Mac address 5	0x11	
	# IP addresss configuration 0	192	
	# IP addresss configuration 1	168	
	# IP addresss configuration 2	0	
	# IP addresss configuration 3	200	
	# netmask configuration 0	255	
	# netmask configuration 1	255	
	# netmask configuration 2	255	
	# netmask configuration 3	0	
	# Use task notifications	🗹 Enable	
	# Record stack high address	Disable	
	# Number of thread local storage pointers	0	
	# Support dynamic allocation	🕼 Enable	
	# Support static allocation	Disable	
			~
	Macro definition into HIGHER PRIORITY		2
erview Board Clocks System Componen			

Figure 30. Changing the MAC, IP, and netmask

5.5 Setting Up Debug Connection between the CK-RX65N board and Host PC

To program the modified Quick Start example project on to the CK-RX65N board, a debug connection is necessary between the CK-RX65N board and host PC.

1. Connect the USB cable to micro-B USB debug port (J14) of the CK-RX65N board.

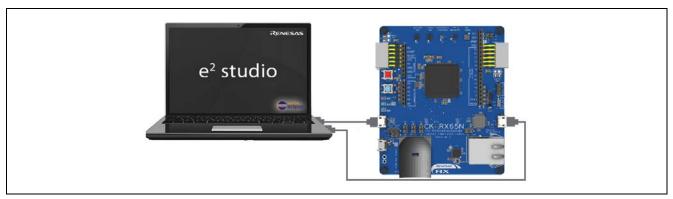


Figure 31. Connecting the CK-RX65N Board to the Host PC via USB Debug Port



2. Verify that the debug LED (LED5) stops blinking and lights up green indicating that the E2OB drivers are detected by the CK-RX65N board.

5.6 Downloading and Running the Modified Quick Start Example Project

1. In e² studio, click the drop-down menu for the debug icon, select **Debug Configurations** option.

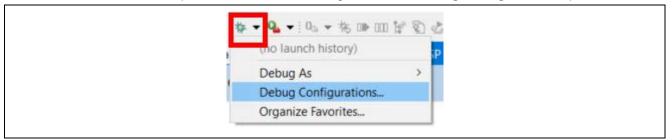


Figure 32. Selecting the Debug Option

2. In the dialogue, on the left-hand pane, expand the **Renesas GDB Hardware Debugging** and select the built image to debug. In this case, the **rx65n_ck_quickstart HardwareDebug**.

Debug Configurations					
reate, manage, and run configu	rations				Ś
Image: Second	Name: rx65n_ck_quickstart HardwareDebug Imain Imain Project: rx65n_ck_quickstart C/C++ Application: HardwareDebug/rx65n_ck_quickstart.x Build (if required) before launching Build Configuration; Select Automatically O Enable auto build Image: Use workspace settings Image: Select Automatically	0	Variables ble auto build ire Workspace Set	Search Project	Browse
< >> >> >> >> >> >> >> >> >> >> >> >> >>				Re <u>v</u> ert	Appl <u>y</u>
?				<u>D</u> ebug	Close

Figure 33. Selecting the Debug Image



5.7 Firewall Dialogue

- 1. A firewall warning may be displayed for 'e2-server-gdb.exe'. Check the 'Private networks, such as my home or work network' box and click 'Allow access'.
- 2. A user account control dialog may be displayed. Enter the administrator password and click **Yes**.
- 3. A "confirm Perspective Switch" dialog box may appear. Click Yes.

Confirm Perspective Switch					
2	This kind of launch is configured to open the Debug perspective when it suspends.				
	This Debug perspective supports application debugging by providing views for displaying the debug stack, variables and breakpoints.				
	Switch to this perspective?				
Remember my decision					
	<u>S</u> witch <u>N</u> o				

Figure 34. Opening the Debug Perspective

4. Press **F8** or click **Resume** icon to begin executing the project.





5. The modified Quick Start example project is programmed into the CK-RX65N board and is running. The project can be paused, stopped, or resumed using the debug controls.



6. Next Steps

- To learn more about the CK-RX65N kit, refer to the CK-RX65N user's manual and design package available in the Documents and Download tabs respectively of the CK-RX65N webpage at renesas.com/rx/ck-rx65n
- Renesas provides several example projects that demonstrate different capabilities of the RX MCUs. These example projects can serve as a good starting point for users to develop custom applications. Example projects (source code and project files) for CK-RX65N kit are available in the CK-RX65N Example Projects Bundle. The example projects bundle is available in the Downloads tab of CK-RX65N webpage.
 - Download and extract the example projects bundle (r20an0680eu0100-ck-rx65nexampleprojects.zip) to a local directory on the host PC.
 - Refer to the list of all example projects (r20an0680eu0100-ck-rx65n-exampleprojects.pdf) available inside the example projects bundle.
 - Browse to the desired example project (for example: adc_ck_rx65n_ep) in the example projects bundle (r20an0680eu0100-ck-rx65nexampleprojects\ck_rx65n\adc\adc_ck_rx65n_ep)
 - If needed RX Driver Package can be downloaded separately from the RDP repository on GitHub at: github.com/renesas/rx-driver-package
 - The archived versions of the source code of the example projects are available the example project repository.

7. Website and Support

Visit the following URLs to learn about the kit and the RX family of microcontrollers, download tools and documentation, and get support.

CK-RX65N Resources RX Product Information RX Product Support Forum Renesas Support Cloud Solutions renesas.com/rx/ck-rx65n renesas.com/rx renesas.com/rx/forum renesas.com/support renesas.com/cloudsolutions



Revision History

		Description		
Rev.	Date	Page	Summary	
1.00	May.26.22	_	Initial release	



CK-RX65N – Quick Start Guide

Publication Date: May.26.22

Published by: Renesas Electronics Corporation

CK-RX65N – Quick Start Guide



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