

RV1S9207A

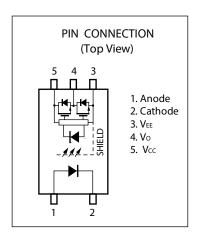
0.6 A OUTPUT CURRENT, HIGH CMR, IGBT GATE DRIVE, 5-PIN SSOP (LSSO5) PHOTOCOUPLER

DESCRIPTION

The RV1S9207A is an optical coupled isolator containing an AlGaAs LED on the input side and a photo diode, a signal processing circuit and power output MOS FETs on the output side on one chip. The RV1S9207A is designed specifically for high common mode transient immunity (CMR) and high switching speed. It is suitable for driving IGBTs and MOS FETs.

FEATURES

- Small and long creepage (8.2 mm MIN, LSSO5)
- Peak output current (0.6 A MAX., 0.4 A MIN.)
- High speed switching (t_{PLH}, t_{PHL} = 150 ns MAX.)
- High common mode transient immunity (CM_H, CM_L = ± 50 kV/µs MIN.)
- Operating Ambient Temperature (125 °C MAX.)
- High isolation voltage (BV = 5 000 Vr.m.s.)
- Embossed tape product : RV1S9207ACCSP-10Yx#KC0 : 3 500 pcs/reel
- Pb-Free product
- Safety standard
 - UL : UL1577, Double protection
 - CSA : CAN/CSA-C22.2 No.62368-1, Reinforced insulation
 - VDE : DIN EN 60747-5-5 (Option)



APPLICATIONS

- IGBT, Power MOS FET Gate Driver
- Industrial inverter
- AC Servo

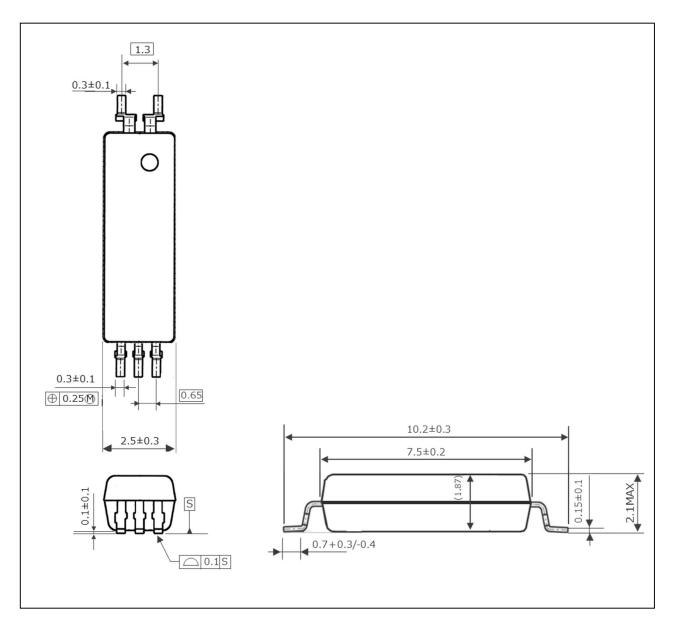
R08DS0220EJ0100

Rev.1.00

Mar 29,2021



PACKAGE DIMENSIONS (UNIT : mm)



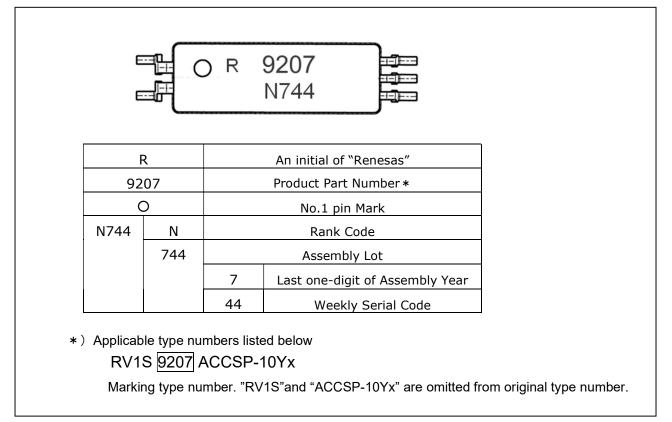
Weight :0.075g (Typ.)

PHOTOCOUPLER CONSTRUCTION

Parameter	MIN.
Air Distance	8.2 mm
Creepage Distance	8.2 mm
Isolation Distance	0.15 mm



MARKING EXAMPLE



ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{*1}
RV1S9207ACCSP -10YC	RV1S9207ACCSP -10YC#SC0	Pb-Free and Halogen Free	20 pcs (Tape 20 pcs cut)	Standard products (UL, CSA approved)	RV1S9207A
	RV1S9207ACCSP -10YC#KC0	(Ni/Pd/Au)	Embossed Tape 3 500 pcs/reel		
RV1S9207ACCSP -10YV	RV1S9207ACCSP -10YV#SC0		20 pcs (Tape 20 pcs cut)	UL, CSA, DIN EN 60747-5-5	
	RV1S9207ACCSP -10YV#KC0		Embossed Tape 3 500 pcs/reel	approved	

Notes:*1. For the application of the Safety Standard, following part number should be used.



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current	IF	20	mA
	Peak Transient Forward Current	IF (TRAN)	1.0	Α
	(Pulse Width < 1 us)			
	Reverse Voltage	VR	5	V
	Power Dissipation Derating	⊿P _D /°C	1.2 (T _A ≧ 110 °C)	mW/°C
	Power Dissipation	PD	45	mW
Detector	High Level Peak Output Current *2	I _{OH (PEAK)}	0.6	A
	Low Level Peak Output Current *2	IOL (PEAK)	0.6	Α
Supply Voltage		V_{CC} - V_{EE}	0 to 35	V
Output Voltage		Vo	0 to Vcc	V
	Power Dissipation Derating	⊿Pc/°C	3.9 (T _A ≧ 85 °C)	mW/°C
Power Dissipation		Pc	250	mW
Isolation Voltage ^{*1}		BV	5 000	Vr.m.s.
Operating Frequency		f	250	kHz
Operating A	Operating Ambient Temperature		-40 to +125	°C
Storage Temperature		T _{stg}	–55 to +150	°C

Notes: *1. AC voltage for 1 minute at T_A = 25 °C, RH = 60 % between input and output. Pins 1-2 shorted together, 3-5 shorted together.

*2. Maximum pulse width = 10 μ s, Maximum duty cycle = 0.5 %

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	$V_{\text{CC}}-V_{\text{EE}}$	10		30	V
Forward Current (ON)	I _{F (ON)}	8	10	12	mA
Forward Voltage (OFF)	V _{F (OFF)}	-2		0.8	V
Operating Ambient Temperature	T _A	-40		125	°C



ELECTRICAL CHARACTERISTICS (at RECOMMENDED OPERATING CONDITIONS, V_{EE} = GND, unless otherwise specified)

	Parameter	Symbol	Conditions	MIN.	TYP.*	MAX.	Unit
Diode	Forward Voltage	VF	I _F = 10 mA, T _A = 25 °C	1.35	1.56	1.75	V
	Reverse Current	IR	V _R = 3 V, T _A = 25 °C			10	μA
	Input Capacitance	Ct	V _F = 0 V, f = 1 MHz, T _A = 25 °C		30		pF
Detector	High Level Output Current	Іон	$V_0 = (V_{CC} - 4 V)^{*2}$	0.2			А
			Vo = (Vcc - 10 V) *3	0.4			
	Low Level Output Current	lol	$V_{O} = (V_{EE} + 2.5 V)^{*2}$	0.2			Α
			$V_{O} = (V_{EE} + 10 \text{ V})^{*3}$	0.4			
	High Level Output Voltage	Vон	I ₀ = -100 mA ^{*4}	Vcc -	Vcc -		V
				3.0 V	1.5 V		
	Low Level Output Voltage	Vol	l _o = 100 mA		0.25	1.0	V
	High Level Supply Current	Іссн	V₀ = Open, I _F = 10 mA		1.4	2.0	mA
	Low Level Supply Current	Iccl	V_{O} = Open, V_{F} = 0 to 0.8 V		1.3	2.0	mA
	UVLO Threshold	VUVLO+	Vo > 5 V, I⊧ = 10 mA		8.6	9.8	V
		Vuvlo-		6.8	8.2		
	UVLO Hysteresis	UVLOHYS			0.4		
Coupled	Threshold Input Current $(L \rightarrow H)$	IFLH	I ₀ = 0 mA, V ₀ > 5 V		2.2	5.0	mA
	Threshold Input Voltage $(H \rightarrow L)$	VFHL	I ₀ = 0 mA, V ₀ < 5 V	0.8			V

Notes: *1. Typical values at T_A = 25 °C, $V_{CC} - V_{EE}$ = 30 V.

*2. Maximum pulse width = 50 μ s, Maximum duty cycle = 0.2 %.

*3. Maximum pulse width = 10 μ s, Maximum duty cycle = 0.5 %.

*4. V_{OH} is measured with the pulse load current in this testing (Maximum pulse width = 2 ms, Maximum duty cycle = 20 %).

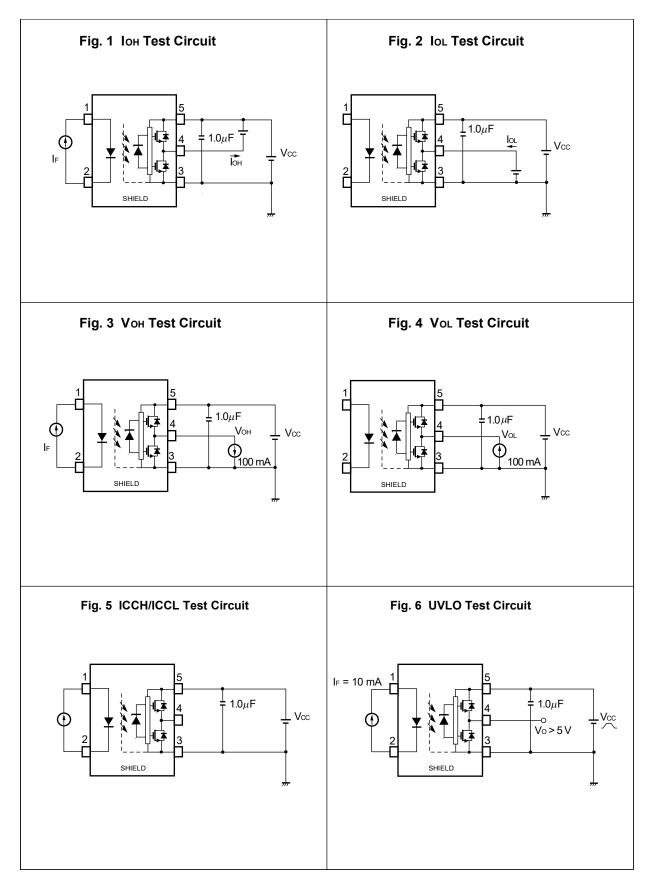
SWITCHING CHARACTERISTICS (at RECOMMENDED OPERATING CONDITIONS, V_{EE} = GND, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Propagation Delay Time (L \rightarrow H)	t _{PLH}	$R_g = 47 \Omega, C_g = 3 nF,$	50	100	150	ns
Propagation Delay Time ($H \rightarrow L$)	t _{PHL}	f = 50 kHz,	50	90	150	ns
Pulse Width Distortion (PWD)	tphl-tplh	Duty Cycle = 50 %,		5	50	ns
Propagation Delay Difference Between Any Two Parts (PDD)	t _{PHL} —t _{PLH}	I _F = 10 mA, V _{CC} = 30 V	-80		80	ns
Rise Time	tr			6		ns
Fall Time	t _f			7		ns
Common Mode Transient Immunity at High Level Output	CM⊦	V _{CC} = 30 V, I _F =10 mA, T _A = 25 °C, V _{CM} = 1.5 kV	50			kV/ <i>μ</i> s
Common Mode Transient Immunity at Low Level Output	CM∟	V _{CC} = 30 V, I _F = 0 mA, T _A = 25 °C, V _{CM} = 1.5 kV	50			kV/ <i>µ</i> s

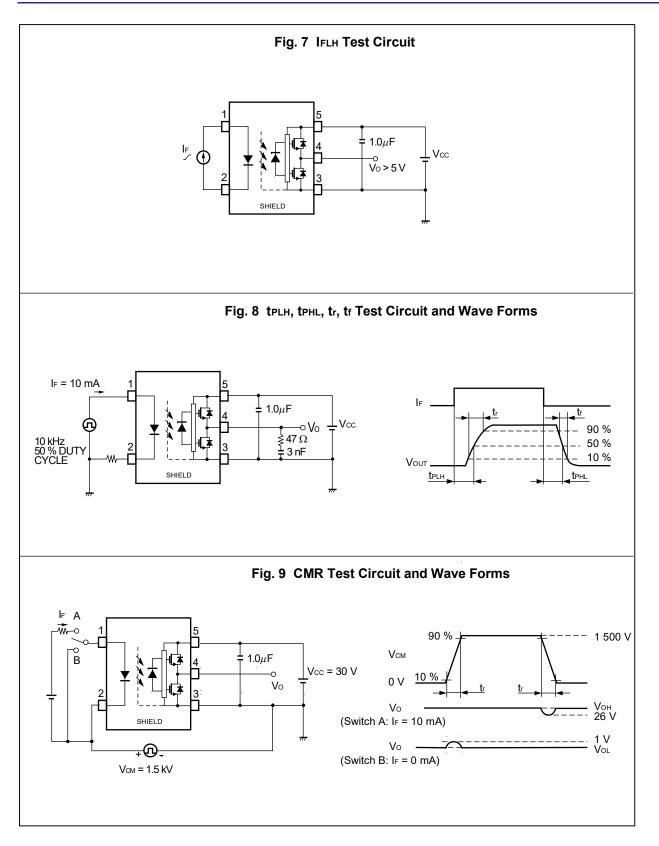
Notes: *1. Typical values at $T_A = 25 \text{ °C}$, $V_{CC}-V_{EE} = 30 \text{ V}$.

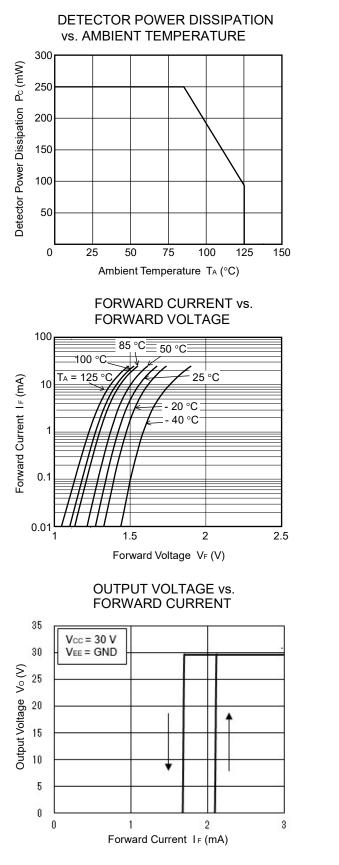


TEST CIRCUIT

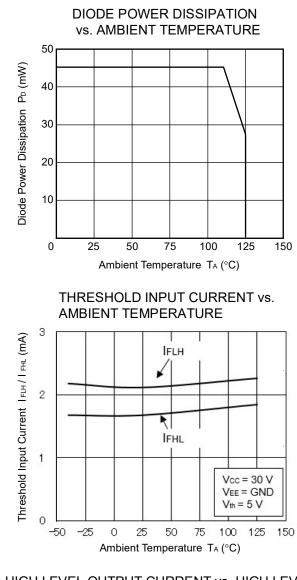




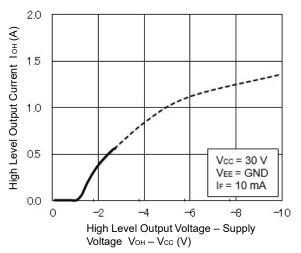


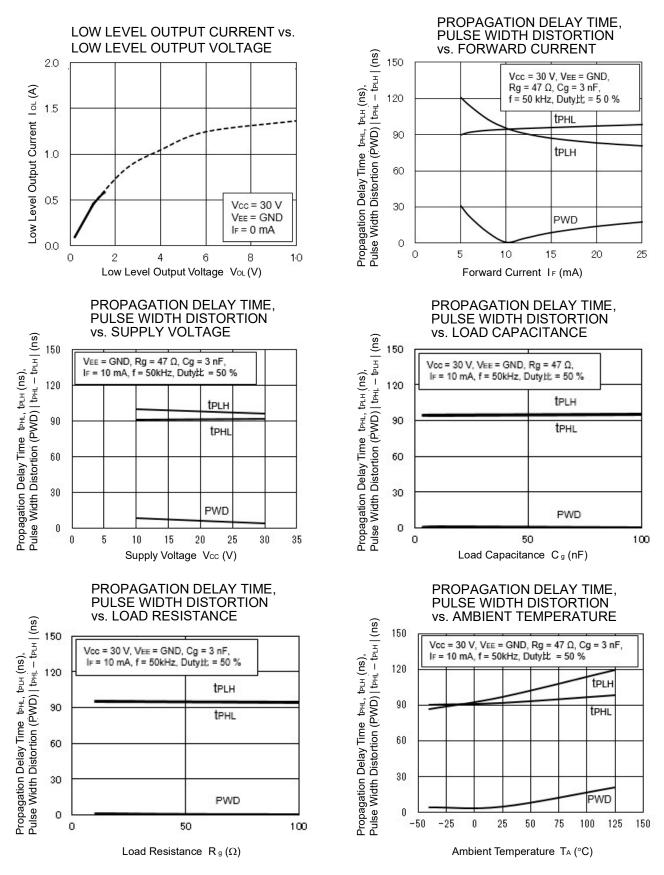






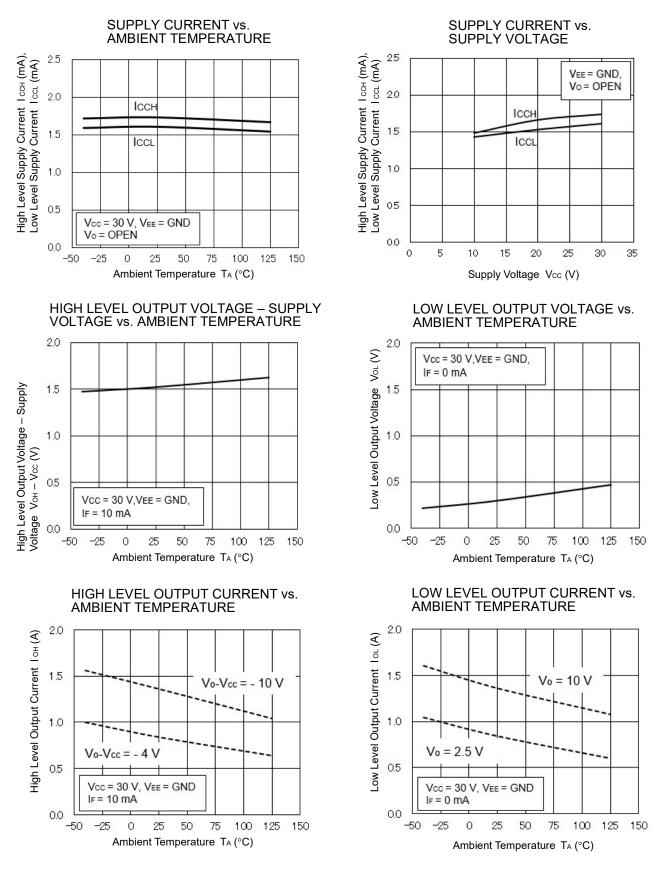
HIGH LEVEL OUTPUT CURRENT vs. HIGH LEVEL OUTPUT VOLTAGE – SUPPLY VOLTAGE







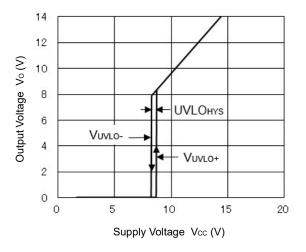




Remark The graphs indicate nominal characteristics.



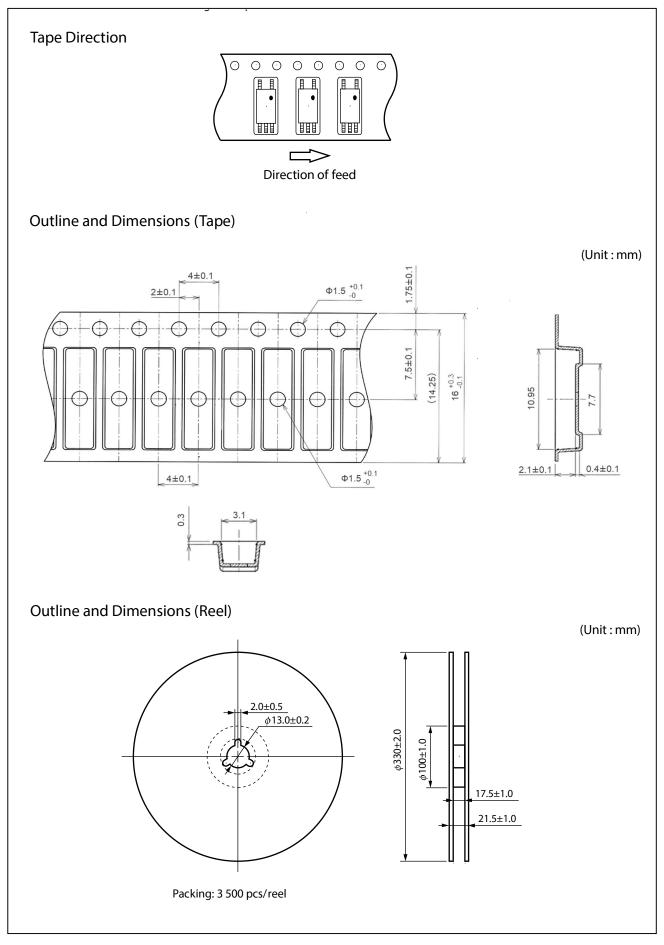
OUTPUT VOLTAGE vs. SUPPLY VOLTAGE





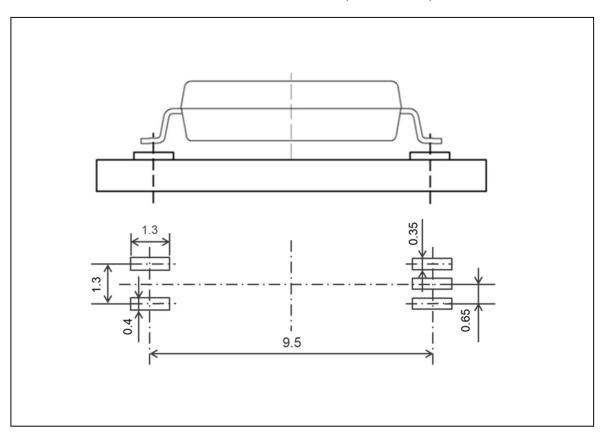


TAPING SPECIFICATIONS (UNIT : mm)





RECOMMENDED MOUNT PAD DIMENSIONS (UNIT : mm)



Remark All dimensions in this figure must be evaluated before use.



NOTES ON HANDLING

- 1. Recommended soldering conditions
 - (1) Infrared reflow soldering
 - Peak reflow temperature
 - Time of peak reflow temperature
 - Time of temperature higher than 220°C •
 - Time to preheat temperature from 120 to 180°C
 - Number of reflows
 - Flux

120±30 s

10 seconds or less

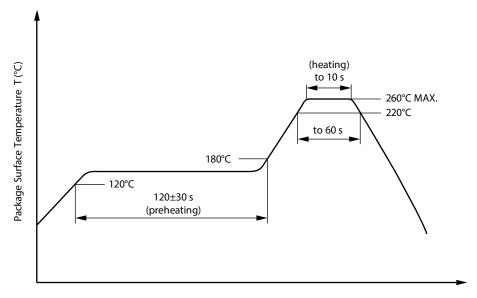
60 seconds or less

Three

Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

260°C or below (package surface temperature)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
 - Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

- 350°C or below Peak Temperature (lead part temperature)
 - 3 seconds or less
- Time (each pins) Rosin flux containing small amount of chlorine Flux
 - (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)
- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead
- (b) Please be sure that the temperature of the package would not be heated over 100°C

(4) Cautions

•

- Flux Cleaning
 - Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.
- Do not use fixing agents or coatings containing halogen-based substances.
- 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.



USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. Board designing
 - (1) By-pass capacitor of more than 1.0 μ F is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
 - (2) When designing the printed wiring board, ensure that the pattern of the IGBT collectors/emitters is not too close to the input block pattern of the photocoupler.

If the pattern is too close to the input block and coupling occurs, a sudden fluctuation in the voltage on the IGBT output side might affect the photocoupler's LED input, leading to malfunction or degradation of characteristics.

(If the pattern needs to be close to the input block, to prevent the LED from lighting during the off state due to the abovementioned coupling, design the input-side circuit so that the bias of the LED is reversed, within the range of the recommended operating conditions, and be sure to thoroughly evaluate operation.)

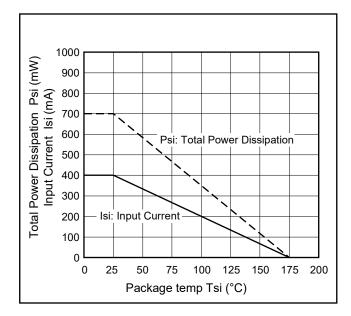
- 3. Make sure the rise/fall time of the forward current is 0.5 μ s or less.
- 4. In order to avoid malfunctions, make sure the rise/fall slope of the supply voltage is $3 V/\mu s$ or less.
- 5. Avoid storage at a high temperature and high humidity.



SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

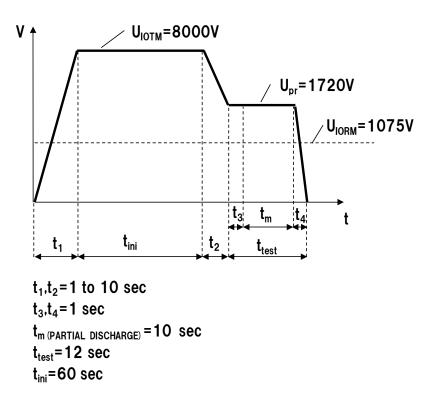
Parameter	Symbol	Rating	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/125/21	
Dielectric strength			
maximum operating isolation voltage	UIORM	1 075	V_{peak}
Test voltage (partial discharge test, procedure a for type test and random	U _{pr}	1 720	V_{peak}
test)			
U_{pr} = 1.6 × $U_{IORM.}$, P_d < 5 pC			
Test voltage (partial discharge test, procedure b for all devices)	Upr	2 016	V _{peak}
U_{pr} = 1.875 × $U_{IORM.}$, P_d < 5 pC	Opr	2010	v peak
Highest permissible overvoltage	UIOTM	8 000	V _{peak}
Degree of pollution (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303-11))	СТІ	400	
Material group (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		П	
Storage temperature range	T _{stg}	-55~+150	°C
Operating temperature range	T _A	-40~+125	°C
Isolation resistance, minimum value			
V_{IO} = 500 V dc at T _A = 25°C	Ris MIN.	10 ¹²	Ω
V _{IO} = 500 V dc at T _A MAX. at least 100°C	Ris MIN.	10 ¹¹	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	175	°C
Current (input current I _F , Psi = 0)	lsi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistanceV _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	10 ⁹	Ω

Dependence of maximum safety ratings with package temperature

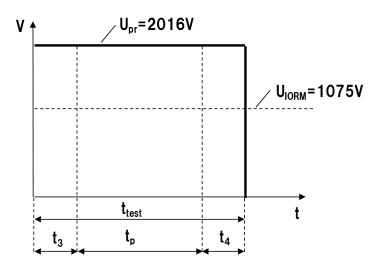




Method a) Destructive Test, Type and Sample Test



Method b) Non-destructive Test, 100% Production Test



 $t_3, t_4 = 0.1 \text{ sec}$ $t_p (PARTIAL DISCHARGE) = 1.0 \text{ sec}$ $t_{test} = 1.2 \text{ sec}$

Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.
	• Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	Do not burn, destroy, cut, crush, or chemically dissolve the product.
	• Do not lick the product or in any way allow it to enter the mouth.

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