

PS9924

HIGH CMR, 10 Mbps OPEN COLLECTOR OUTPUT TYPE, 8-PIN LSDIP PHOTOCOUPLER FOR CREEPAGE DISTANCE OF 15 mm

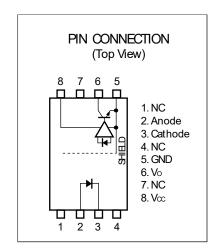
R08DS0059EJ0300 Rev.3.00 Jun. 30, 2021

DESCRIPTION

The PS9924 is an optical coupled high-speed, active low type isolator containing an AlGaAs LED on the input side and a photodiode and a signal processing circuit on the output side on one chip.

FEATURES

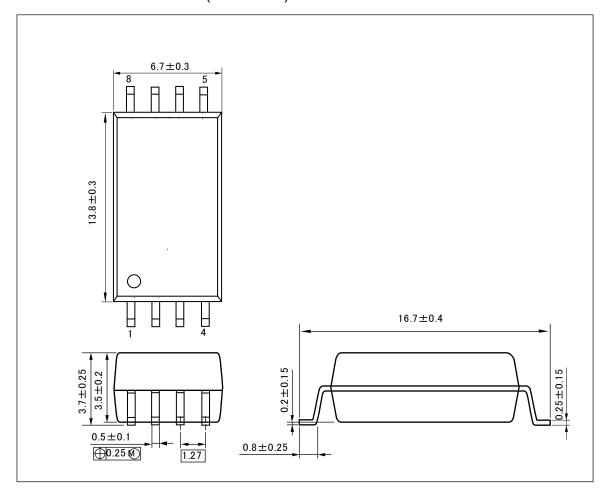
- Long creepage distance (15 mm MIN.)
- High common mode transient immunity (CM_H, CM_L = \pm 15 kV/ μ s MIN.)
- High-speed response (t_{PHL} = 100 ns MAX., t_{PLH} = 100 ns MAX.)
- Low power consumption (V_{CC} = 3.3/5V)
- 8-pin LSDIP (Long Creepage SDIP) type
- Embossed tape product: PS9924-F3: 1 000 pcs/reel
- Pb-Free and Halogen Free product
- Safety standards
 - UL approved: UL1577, Double protection
 - CSA approved: CAN/CSA-C22.2 No.62368-1, Reinforced insulation
 - SEMKO approved (Creepage Distance:14.5 mm *): EN 62368-1, IEC 62368-1, Reinforced insulation * Creepage Distance:15 mm – in process
 - VDE approved: DIN EN 60747-5-5 (Option)



APPLICATIONS

- Industrial inverter
- Solar inverter

PACKAGE DIMENSIONS (UNIT: mm)

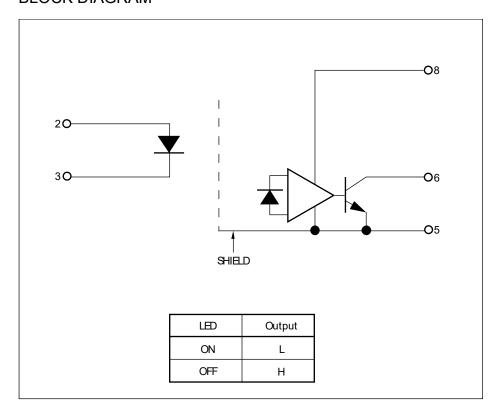


Weight: 0.642g (typ.)

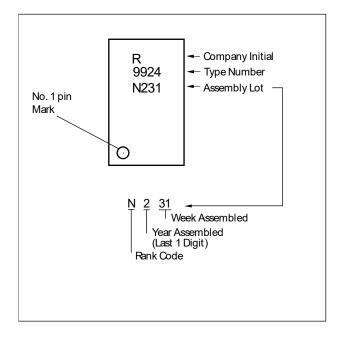
PHOTOCOUPLER CONSTRUCTION

Parameter	MIN.
Air Distance	14.5 mm
Creepage Distance	15 mm
Isolation Distance	0.4 mm

BLOCK DIAGRAM



MARKING EXAMPLE



ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{*1}
PS9924	PS9924-Y-AX	Pb-Free and	10 pcs (Tape 10 pcs cut)	Standard products	PS9924
PS9924-F3	PS9924-Y-F3-AX	Halogen Free	Embossed Tape 1 000	(UL, CSA, SEMKO	
		(Ni/Pd/Au)	pcs/reel	approved)	
PS9924-V	PS9924-Y-V-AX		10 pcs (Tape 10 pcs cut)	UL, CSA, SEMKO,	
PS9924-V-F3	PS9924-Y-V-F3-AX		Embossed Tape 1 000	DIN EN 60747-5-5	
			pcs/reel	approved	

Note: *1. For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit	
Diode	Forward Current	l _F	25	mA	
	Reverse Voltage	VR	5	V	
	Power Dissipation*1	PD	45	mW	
Detector	Supply Voltage	Vcc	7	V	
	Output Voltage	Vo	7	V	
	Output Current	lo	25	mA	
	Power Dissipation *2	Pc	250	mW	
Isolation Voltage*3		BV	7 500	Vr.m.s.	
Operating Ambient Temperature		TA	-40 to +110	°C	
Storage Temperature		T _{stg}	-55 to +125	°C	

Notes: *1. Reduced to 0.8 mW/°C at T_A = 85°C or more.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Forward Voltage	V _{F(OFF)}	-2		0.8	V
High Level Forward Current	I _{F(ON)}	8	10	12	mA
Supply Voltage	Vcc	2.7		5.5	٧
Pull-up Resistor	RL	330		4k	Ω

^{*2.} Reduced to 5.2 mW/°C at T_A = 85°C or more.

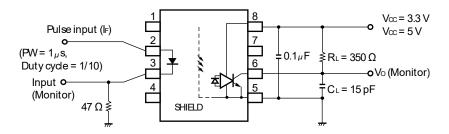
^{*3} AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output. Pins 1-4 shorted together, 5-8 shorted together.

ELECTRICAL CHARACTERISTICS (T_A = -40 to +110 °C, unless otherwise specified)

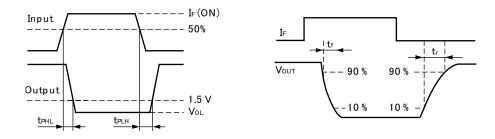
	Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	I _F = 10 mA, T _A = 25 °C	1.3	1.56	1.8	V
	Reverse Current	I _R	V _R = 3 V, T _A = 25 °C			10	μA
	Terminal Capacitance	Ct	$f = 1 \text{ MHz}, V_F = 0 \text{ V}, T_A = 25 ^{\circ}\text{C}$		30		pF
Detector	High Level Output Current	Іон	$V_{CC} = V_{O} = 3.3 \text{ V}, V_{F} = 0.8 \text{ V}$		1	80	μA
			$V_{CC} = V_0 = 5.5 \text{ V}, V_F = 0.8 \text{ V}$		1	100	
	Low Level Output Voltage	V _{OL}	$V_{CC} = 3.3 \text{ V}, I_F = 10 \text{ mA},$		0.2	0.6	V
			I _{OL} = 13 mA				
			$V_{CC} = 5.5 \text{ V}, I_F = 10 \text{ mA},$				
			I _{OL} = 13 mA				
	High Level Supply Current	Icch Vcc = 3.3 V, I _F = 0 mA, Vo = open			2	7	mA
						_	
			$V_{CC} = 5.5 \text{ V}, I_F = 0 \text{ mA},$		3	7	
	Low Lovel Cumply Current	1	V ₀ = open		4	10	m A
	Low Level Supply Current	Iccl	$V_{CC} = 3.3 \text{ V, I}_{F} = 10 \text{ mA,}$ $V_{O} = \text{open}$		4	10	mA
			$V_{CC} = 5.5 \text{ V, I}_F = 10 \text{ mA,}$		5	10	1
			Vo = open			10	
Coupled	Threshold Input Voltage	I _{FHL}	$V_{CC} = 3.3 \text{ V}, \text{ R}_{L} = 350 \Omega,$		2	5	mA
	$(H \rightarrow L)$		V ₀ = 0.8 V				
			$V_{CC} = 5.0 \text{ V}, R_L = 350 \Omega,$	1			
			V _O = 0.8 V				
	Isolation Resistance	R _{I-O}	V _{I-O} = 1 kV _{DC} , RH = 40 to 60 %	10 ¹¹			Ω
	Isolation Capacitance	C _{I-O}	$V_{I-O} = 0 V, f = 1 MHz,$		1.0		pF
			T _A = 25 °C				
	Propagation Delay Time	t _{PHL}	V _{CC} = 3.3 V, T _A = 25 °C		45	75	ns
	$(H \rightarrow L)^{*2}$		I _F = 10 mA,				
			$R_L = 350 \Omega$, $T_A = -40 ^{\circ}C$ to			100	
			C _L = 15 pF 110 °C				
			$V_{CC} = 5 \text{ V}, \qquad T_A = 25 ^{\circ}\text{C}$		45	75	
			I _F = 10 mA,				
			$R_L = 350 \Omega$, $T_A = -40 ^{\circ}C$ to			100	
			C _L = 15 pF 110 °C				
	Propagation Delay Time	t _{PLH}	V _{CC} = 3.3 V, T _A = 25°C		40	75	ns
	$(L \rightarrow H)^{*2}$		I _F = 10 mA,				
			$R_L = 350 \Omega$, $T_A = -40 ^{\circ}C$ to			100	
			C _L = 15 pF 110 °C				
			$V_{CC} = 5 \text{ V}, \qquad T_A = 25 \text{ °C}$		40	75	
			I _F = 10 mA,				_
			$R_L = 350 \Omega$, $T_A = -40 ^{\circ}C$ to			100	
		1	C _L = 15 pF 110 °C		_		
	Pulse Width Distortion (PWD)*2	tphl-tplh	$V_{CC} = 3.3/5 \text{ V}, I_F = 10 \text{ mA},$ $R_L = 350 \Omega, C_L = 15 \text{ pF}$		5	35	ns
	Propagation Delay	t _{psk}	, 333, 32 10 pi			40	-
	Skew*2	·					
	Rise Time*2	t _r			20		
	Fall Time*2	t _f			5		
	Common Mode	СМн	V _{CC} = 3.3/5 V, I _F = 0 mA,	15	20		kV/μs
	Transient Immunity at		$V_0 > 2 \text{ V}, \text{ R}_L = 350 \Omega,$				
	High Level Output*3		V _{CM} = 1 kV, T _A = 25 °C				
	Common Mode	CM∟	$V_{CC} = 3.3/5 \text{ V}, I_F = 10 \text{ mA},$	15	20		kV/μs
	Transient Immunity at		$V_0 < 0.8 \text{ V}, R_L = 350 \Omega,$				
	Low Level Output*3		V _{CM} = 1 kV, T _A = 25 °C				

Notes: *1. Typical values at T_A = 25 °C

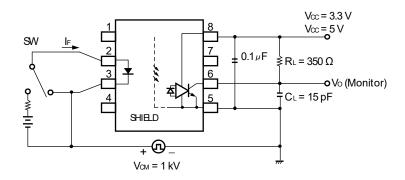
*2. Test circuit for propagation delay time

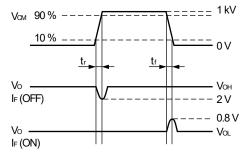


Remark C_L includes probe and stray wiring capacitance.



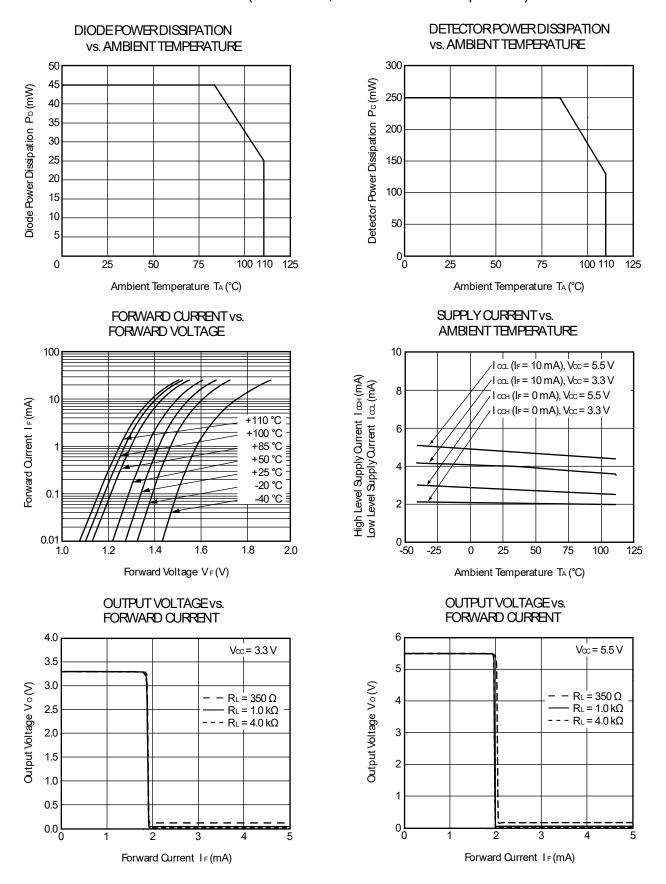
*3. Test circuit for common mode transient immunity



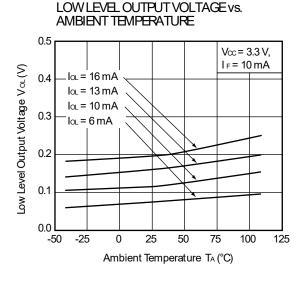


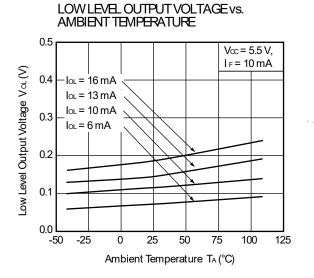
Remark CL includes probe and stray wiring capacitance.

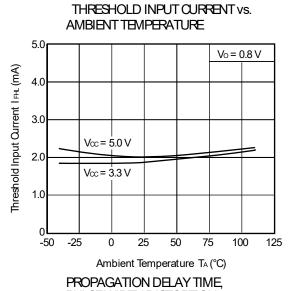
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise specified)

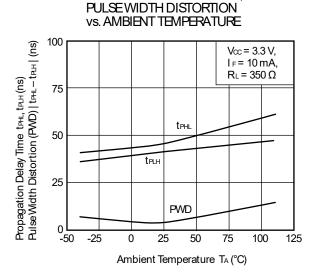


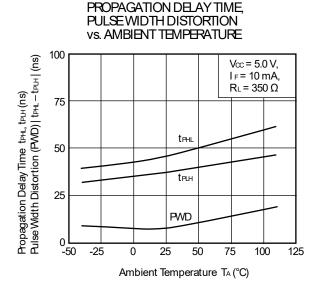
Remark The graphs indicate nominal characteristics.



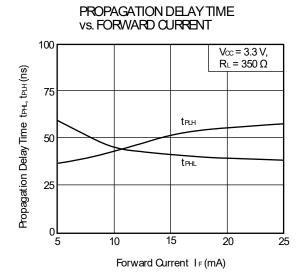








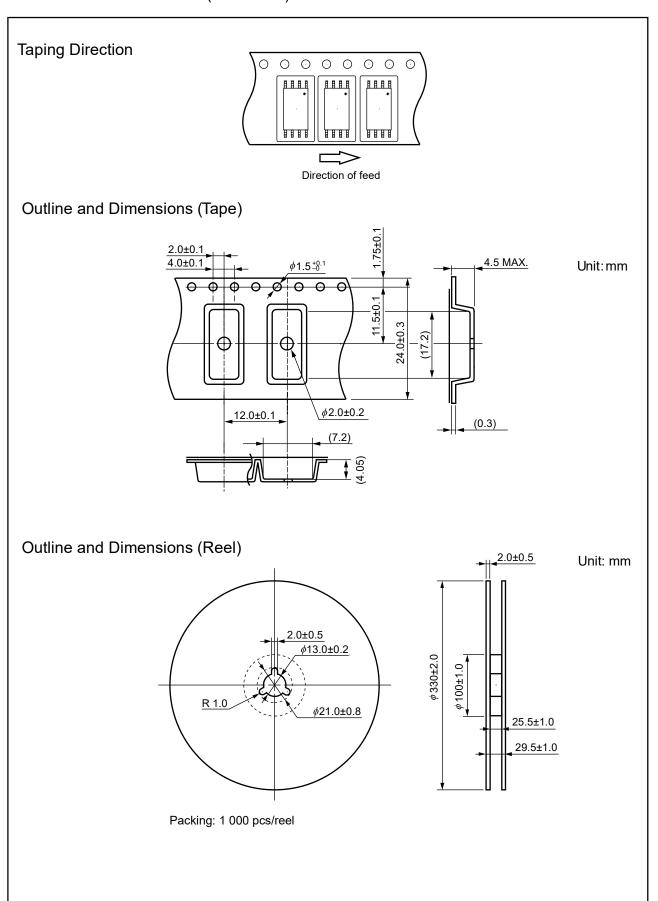
Remark The graphs indicate nominal characteristics.



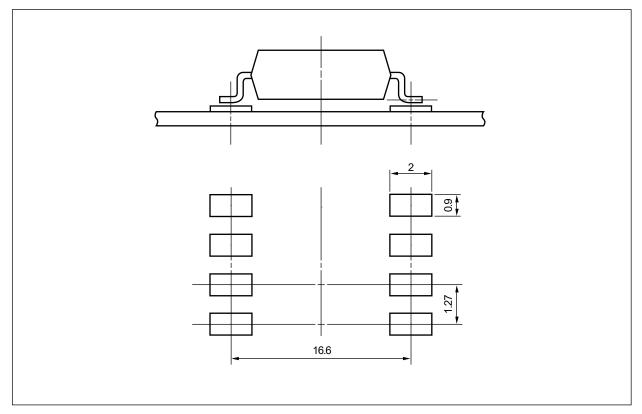
Forward Current IF (mA)

Remark The graphs indicate nominal characteristics.

TAPING SPECIFICATIONS (UNIT: mm)



RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



[8pin LSDIP]

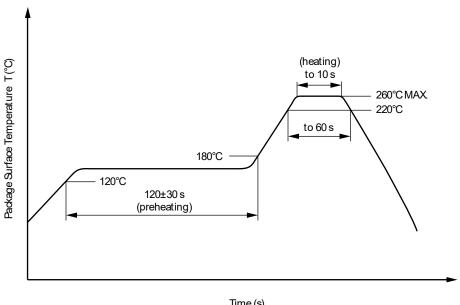
Remark All dimensions in this figure must be evaluated before use.

NOTES ON HANDLING

- 1. Recommended soldering conditions
 - (1) Infrared reflow soldering
 - Peak reflow temperature 260 °C or below (package surface temperature)
 - Time of peak reflow temperature 10 seconds or less Time of temperature higher than 220 °C 60 seconds or less
 - Time to preheat temperature from 120 to 180 °C $120 \pm 30 s$
 - Number of reflows Three
 - Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is

recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

Temperature 260 °C or below (molten solder temperature)

10 seconds or less

Preheating conditions 120 °C or below (package surface temperature)

Number of times One (Allowed to be dipped in solder including plastic mold portion.) Rosin flux containing small amount of chlorine (The flux with a maximum Flux

chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

350 °C or below Peak Temperature (lead part temperature) Time (each pins) 3 seconds or less

Flux Rosin flux containing small amount of chlorine

(The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead
- (b) Please be sure that the temperature of the package would not be heated over 110 °C

(4) Cautions

Flux Cleaning

Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.

Do not use fixing agents or coatings containing halogen-based substances.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

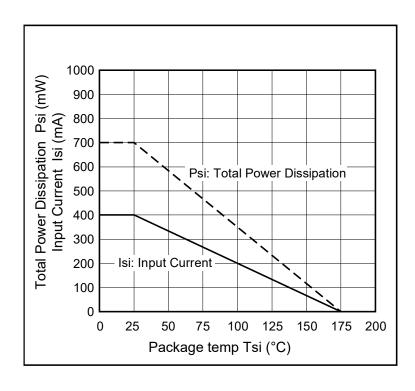
USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of more than 0.1 μ F is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Pin 1, 4 (which is an NC*1 pin) can either be connected directly to the GND pin on the LED side or left open.
 - Also, Pin 7 (which is an NC*1 pin) can either be connected directly to the GND pin on the detector side or left open. Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.
 - Note: *1. NC: Non-Connection (No Connection).
- 4. Avoid storage at a high temperature and high humidity.

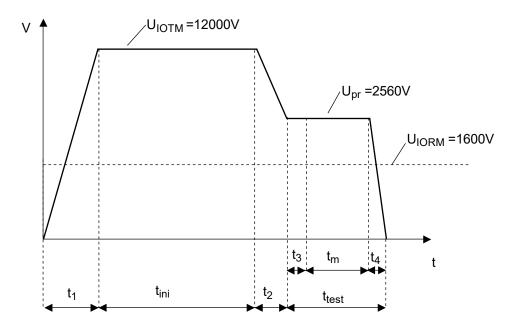
SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Rating	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/110/21	
Dielectric strength			
maximum operating isolation voltage	U_{IORM}	1 600	V_{peak}
Test voltage (partial discharge test, procedure a for type test and random test)	U_pr	2 560	V_{peak}
$U_{pr} = 1.6 \times U_{IORM.}, P_d < 5 pC$			
Test voltage (partial discharge test, procedure b for all devices)	U_pr	3 000	V _{peak}
$U_{pr} = 1.875 \times U_{IORM.}$, $P_d < 5 pC$			
Highest permissible overvoltage	Uютм	12 000	V _{peak}
Degree of pollution (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303-11))	CTI	175	
Material group (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		III a	
Storage temperature range	T _{stg}	-55 to +125	°C
Operating temperature range	TA	-40 to +110	°C
Isolation resistance, minimum value			
V _{IO} = 500 V dc at T _A = 25 °C	Ris MIN.	10 ¹²	Ω
V _{IO} = 500 V dc at T _A MAX. at least 100 °C	Ris MIN.	10 ¹¹	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	175	°C
Current (input current I _F , Psi = 0)	lsi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistance			
V _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	10 ⁹	Ω

Dependence of maximum safety ratings with package temperature



Method a Destructive Test, Type and Sample Test



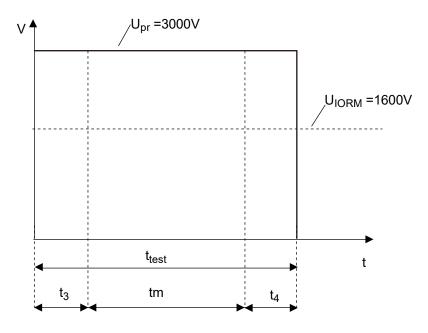
 $t_1, t_2 = 1 \text{ to } 10 \text{ sec}$

 $t_3, t_4 = 1 \text{ sec}$

 $t_{m(PARTIAL\ DISCHARGE)}$ = 10 sec

 $t_{\text{test}} = 12 \text{ sec}$ $t_{\text{ini}} = 60 \text{ sec}$

Method b Non-destructive Test, 100% Production Test



 $t_3, t_4 = 0.1 \text{ sec}$

 $t_{m(PARTIAL\ DISCHARGE)}$ = 1.0 sec

 $t_{test} = 1.2 sec$

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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