

# PS9905

2.5 A OUTPUT CURRENT, HIGH CMR, IGBT GATE DRIVE, 8-PIN LSDIP PHOTOCOUPLER FOR CREEPAGE DISTANCE OF 15 mm

# DESCRIPTION

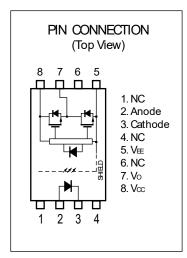
The PS9905 is optically coupled isolator containing an AlGaAs LED on the input side and a photo diode, a signal processing circuit and a power output transistor on the output side on one chip.

## FEATURES

- Long creepage distance (15 mm MIN.)
- Large peak output current (2.5 A MAX., 2.0 A MIN.)
- High speed switching ( $t_{PLH}$ ,  $t_{PHL}$  = 0.15  $\mu$ s MAX.)
- UVLO (Under Voltage Lock Out) protection with hysteresis
- High common mode transient immunity (CM<sub>H</sub>, CM<sub>L</sub> =  $\pm$  25 kV/µs MIN.)
- 8-pin LSDIP (Long Creepage SDIP) type
- Embossed tape product: PS9905-F3: 1 000 pcs/reel
- Pb-Free Product
- Safety standards
  - UL approved: UL1577, Double protection
  - CSA approved: CAN/CSA-C22.2 No.62368-1, Reinforced insulation
  - SEMKO approved (Creepage Distance:14.5 mm \*): EN 62368-1, IEC 62368-1, Reinforced insulation
    - \* Creepage Distance:15 mm in process
  - VDE approved: DIN EN 60747-5-5 (Option)

## APPLICATIONS

- IGBT, Power MOS FET Gate Driver
- Industrial inverter
- Solar inverter



Data Sheet

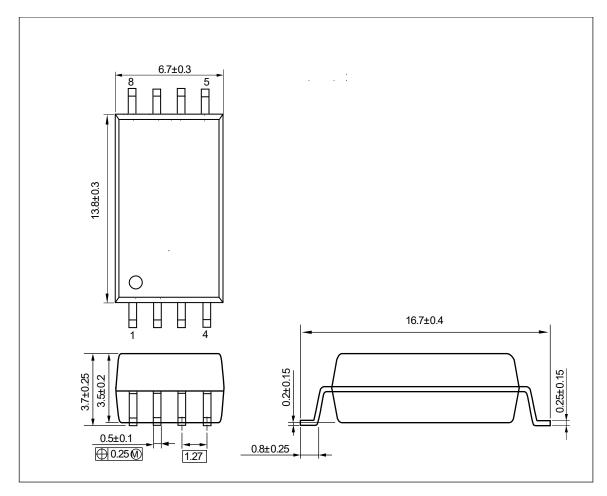
R08DS0058EJ0200

Rev.2.00

Jun. 30, 2021



# PACKAGE DIMENSIONS (UNIT: mm)



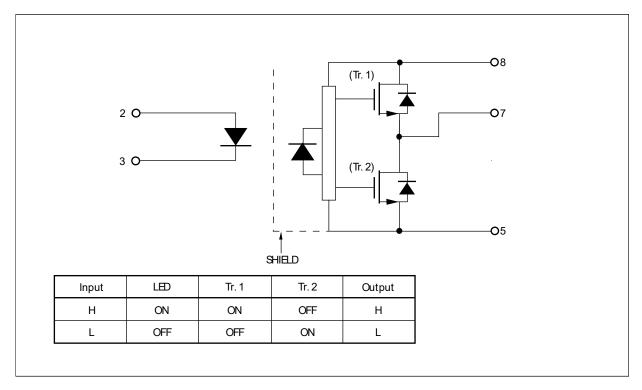
Weight: 0.642g (typ.)

# PHOTOCOUPLER CONSTRUCTION

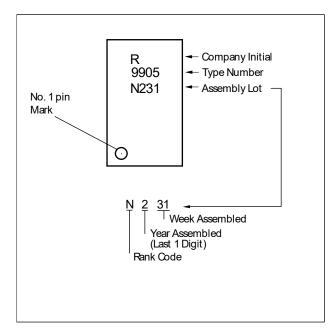
Parameter	MIN.
Air Distance	14.5 mm
Creepage Distance	15 mm
Isolation Distance	0.4 mm



# FUNCTIONAL DIAGRAM



# MARKING EXAMPLE





## ORDERING INFORMATION

Part Number	Order Number	Solder Plating	Packing Style	Safety Standard	Application
		Specification		Approval	Part Number <sup>*1</sup>
PS9905	PS9905-Y-AX	Pb-Free	10 pcs (Tape 10 pcs cut)	Standard products	PS9905
PS9905-F3	PS9905-Y-F3-AX	(Ni/Pd/Au)	Embossed Tape 1 000	(UL, CSA, SEMKO	
			pcs/reel	approved)	
PS9905-V	PS9905-Y-V-AX		10 pcs (Tape 10 pcs cut)	UL, CSA, SEMIKO,	
PS9905-V-F3	PS9905-Y-V-F3-AX		Embossed Tape 1 000	DIN EN60747-5-5	
			pcs/reel	approved	

Note: \*1. For the application of the Safety Standard, following part number should be used.

## ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current	lF	25	mA
	Peak Transient Forward Current (Pulse Width < 1 μs)	I <sub>F (TRAN)</sub>	1.0	A
	Reverse Voltage	VR	5	V
	Power Dissipation*1, *6	PD	45	mW
Detector	High Level Peak Output Current <sup>*2</sup>	IOH (PEAK)	2.5	A
	Low Level Peak Output Current*2	I <sub>OL (PEAK)</sub>	2.5	A
	Supply Voltage	(V <sub>CC</sub> - V <sub>EE</sub> )	0 to 35	V
	Output Voltage	Vo	0 to V <sub>CC</sub>	V
	Power Dissipation* <sup>3, *6</sup>	Pc	250	mW
Isolation V	′oltage <sup>*4</sup>	BV	7 500	Vr.m.s.
Operating	Frequency *5	f	50	kHz
Operating	Ambient Temperature	T <sub>A</sub>	-40 to +110	°C
Storage To	emperature	T <sub>stg</sub>	–55 to +125	°C

Notes: \*1. Derating to be set after 0.8 mW/°C at  $T_A = 85$  °C or more.

- \*2. Maximum pulse width = 10  $\mu$ s, Maximum duty cycle = 0.2 %
- \*3. Reduced to 5.2 mW/°C at T<sub>A</sub> = 85 °C or more
- \*4. AC voltage for 1 minute at  $T_A$  = 25 °C, RH = 60 % between input and output. Pins 1-4 shorted together, 5-8 shorted together.
- \*5.  $I_{OH (PEAK)} \leq 2.0 \text{ A} (\leq 0.3 \ \mu s), I_{OL (PEAK)} \leq 2.0 \text{ A} (\leq 0.3 \ \mu s)$
- \*6. Mounted on glass epoxy substrate of 75 mm  $\times$  115 mm  $\times$  t1.5 mm

# RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	(Vcc - Vee)	15		30	V
Forward Current (ON)	IF (ON)	10	12	14	mA
Forward Voltage (OFF)	VF (OFF)	-2		0.8	V
Operating Ambient Temperature	TA	-40		110	°C



# ELECTRICAL CHARACTERISTICS

# (V<sub>EE</sub> = GND, unless otherwise specified and refer to RECOMMENDED OPERATING CONDITIONS)

	Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	I <sub>F</sub> = 10 mA, T <sub>A</sub> = 25 °C	1.3	1.56	1.8	V
	Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 3 V, T <sub>A</sub> = 25 °C			10	μA
	Terminal Capacitance	Ct	f = 1 MHz, V <sub>F</sub> = 0 V, T <sub>A</sub> = 25 °C		30		pF
Detector	High Level Output Current	Іон	$V_0 = (V_{CC} - 4 V)^{*2}$	0.5	2.0		А
			$V_0 = (V_{CC} - 15 \text{ V})^{*3}$	2.0			
	Low Level Output Current	Iol	$V_0 = (V_{EE} + 2.5 V)^{*2}$	0.5	2.0		А
			$V_{O} = (V_{EE} + 15 \text{ V})^{*3}$	2.0			
	High Level Output Voltage	Vон	I <sub>0</sub> = –100 mA <sup>*4</sup>	$V_{CC}-3.0$	Vcc - 1.5		V
	Low Level Output Voltage	Vol	Io = 100 mA		0.1	0.5	V
	High Level Supply Current	Іссн	$V_0$ = open, I <sub>F</sub> = 12 mA		1.4	3.0	mA
	Low Level Supply Current	Iccl	$V_0$ = open, $V_F$ = -2 to +0.8 V		1.3	3.0	mA
	UVLO Threshold	VUVLO+	Vo > 5 V, I⊧ = 12 mA	10.8	12.3	13.4	V
		Vuvlo-		9.5	11.0	12.5	
	UVLO Hysteresis	<b>UVLO</b> <sub>HYS</sub>	V₀ > 5 V, I⊧ = 12 mA	0.4	1.3		V
Coupled	Threshold Input Current $(L \rightarrow H)$	Iflh	lo = 0 mA, Vo > 5 V		2.9	6.0	mA
	Threshold Input Voltage $(H \rightarrow L)$	Vfhl	lo = 0 mA, Vo < 5 V	0.8			V

Notes: \*1. Typical values at T<sub>A</sub> = 25 °C

- \*2. Maximum pulse width = 50  $\mu$ s, Maximum duty cycle = 0.5 %.
- \*3. Maximum pulse width = 10  $\mu$ s, Maximum duty cycle = 0.2 %.
- \*4. V<sub>OH</sub> is measured with the DC load current in this testing (Maximum pulse width = 2 ms, Maximum duty cycle = 20 %).



# SWITCHING CHARACTERISTICS

# (V\_{EE} = GND, unless otherwise specified and refer to RECOMMENDED OPERATING CONDITIONS)

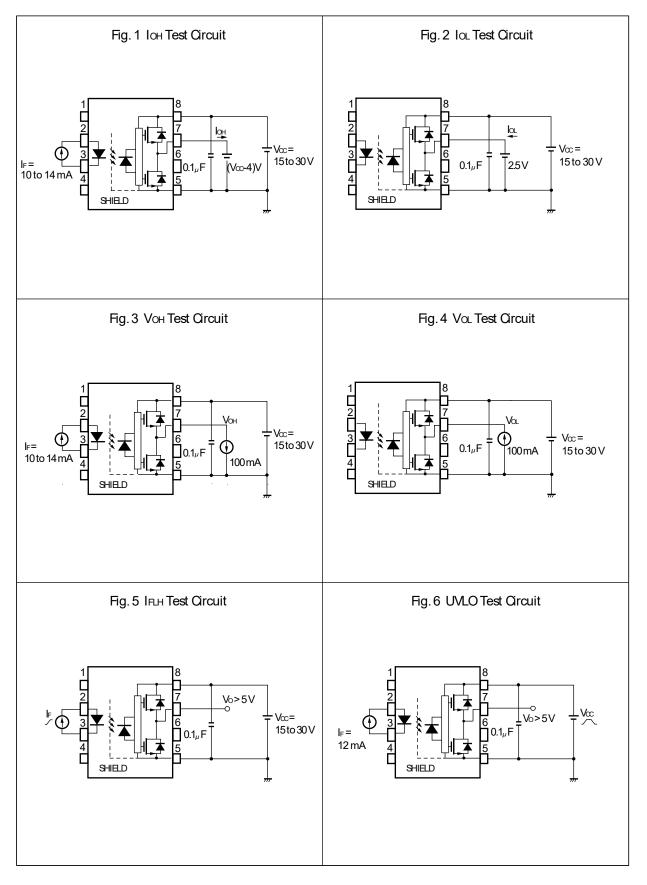
Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Propagation Delay Time (L $\rightarrow$ H)	<b>t</b> PLH	$R_g = 10 \Omega$ , $C_g = 10 nF^{*2}$ , f = 10 kHz,		0.09	0.15	μs
Propagation Delay Time $(H \rightarrow L)$	<b>t</b> PHL	Duty Cycle = 50 %, I <sub>F</sub> = 12 mA		0.1	0.15	μs
Pulse Width Distortion (PWD)	t <sub>PHL</sub> -t <sub>PLH</sub>			0.01	0.075	μs
Propagation Delay Time (Difference Between Any Two Products)	t <sub>PHL</sub> —t <sub>PLH</sub>		-0.1		0.1	μs
Rise Time	tr			50		ns
Fall Time	tr			50		ns
UVLO (Turn On Delay)	tuvlo on	V <sub>O</sub> > 5 V, I <sub>F</sub> = 12 mA		0.8		μs
UVLO (Turn Off Delay)	tuvlo off	V <sub>O</sub> < 5 V, I <sub>F</sub> = 12 mA		0.6		μs
Common Mode Transient Immunity at High Level Output	CM <sub>H</sub>	$T_{A} = 25 \text{ °C}, I_{F} = 12 \text{ mA}, V_{CC} = 30 \text{ V}, V_{O (MIN.)} = 26 \text{ V}, V_{CM} = 1.5 \text{ kV}$	25			kV/ <i>µ</i> s
Common Mode Transient Immunity at Low Level Output	CML	$ \begin{array}{l} T_{A} = 25 \ ^{\circ}\text{C}, \ I_{F} = 0 \ \text{mA}, \ V_{CC} = 30 \ \text{V}, \\ V_{O \ (\text{MAX.})} = 1 \ \text{V}, \ V_{CM} = 1.5 \ \text{kV} \end{array} $	25			kV/ <i>µ</i> s

Notes: \*1. Typical values at  $T_A = 25 \circ C$ 

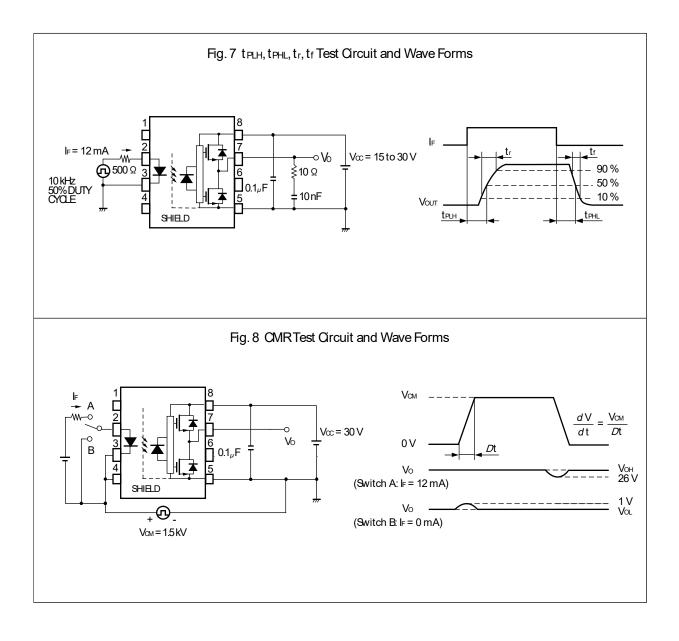
\*2. This load condition is equivalent to the IGBT load at 1 200 V / 75 A.



# **TEST CIRCUIT**

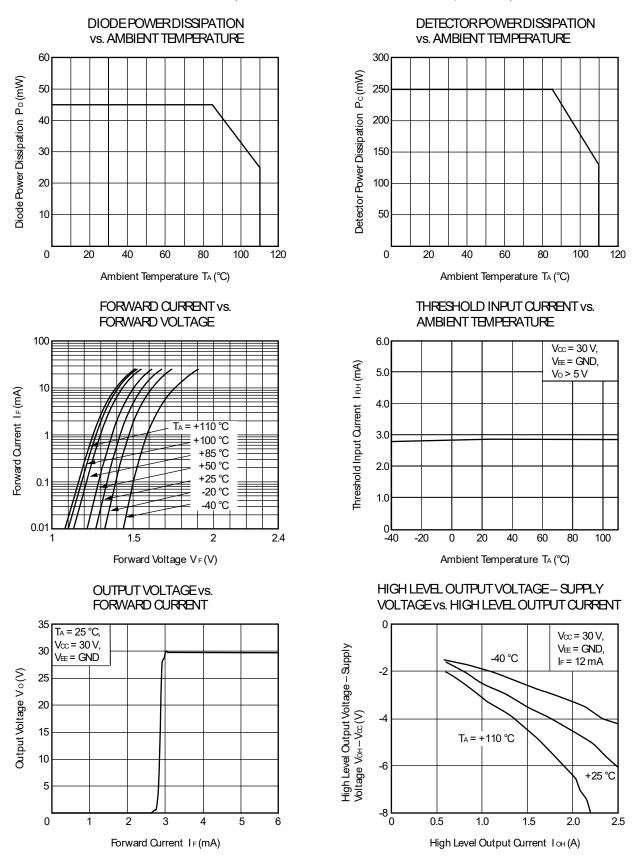


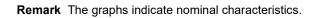


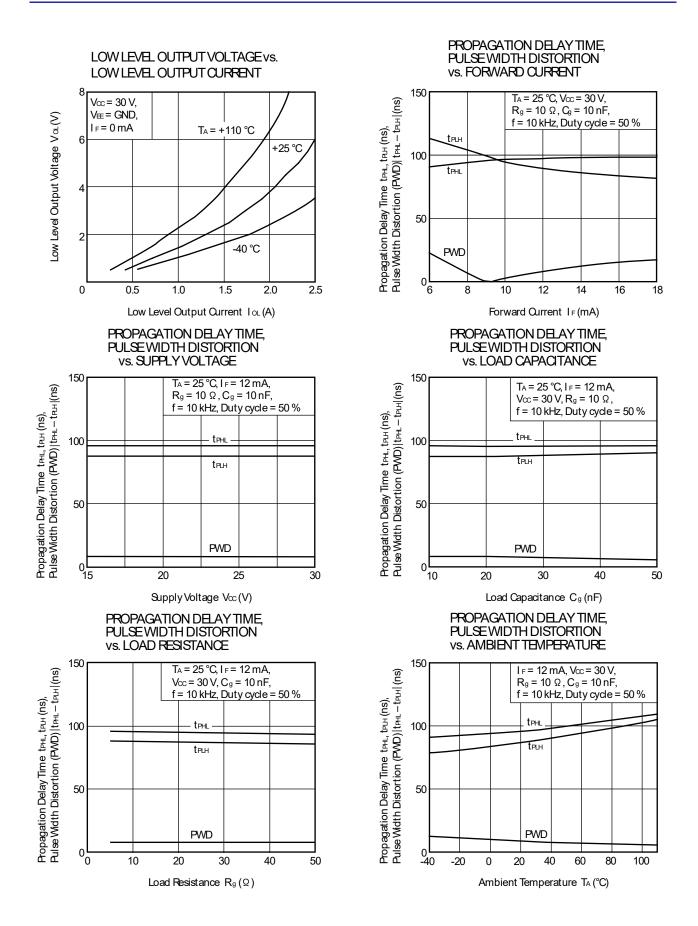




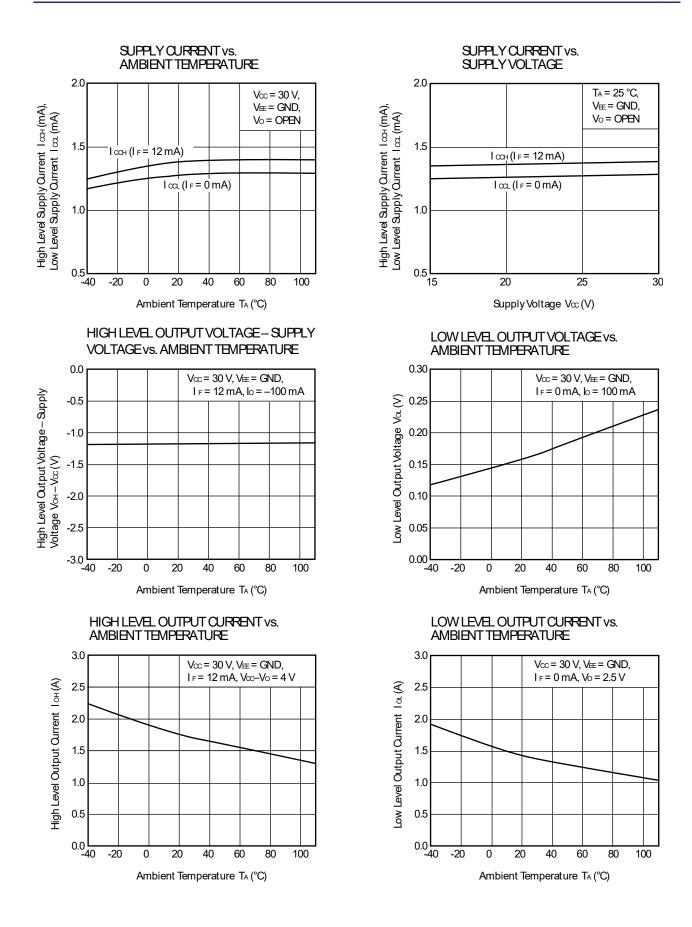
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise specified)



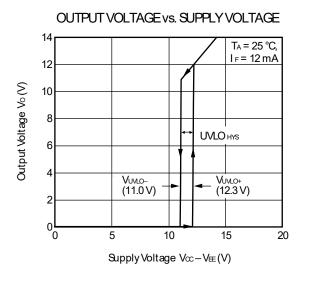




Remark The graphs indicate nominal characteristics.



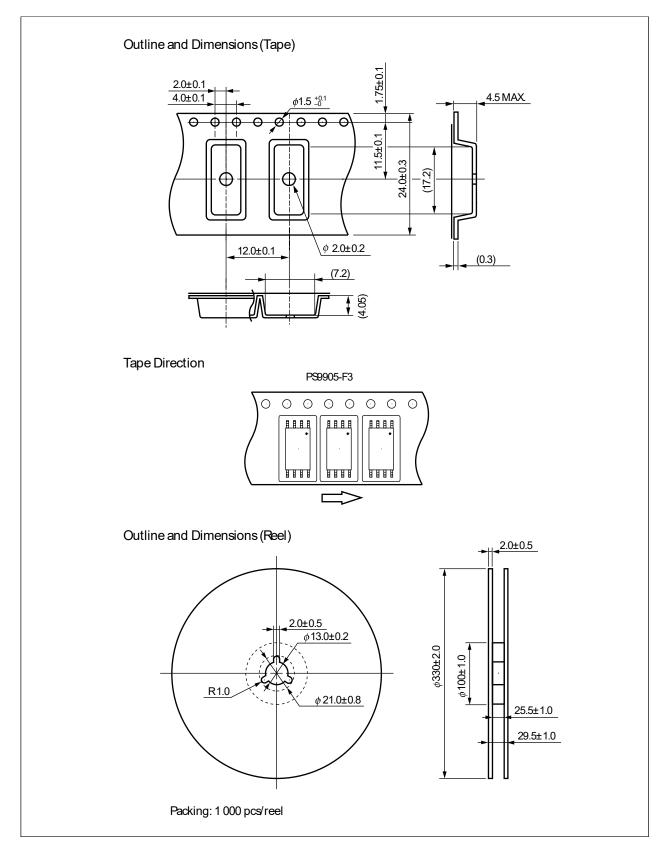
**Remark** The graphs indicate nominal characteristics.



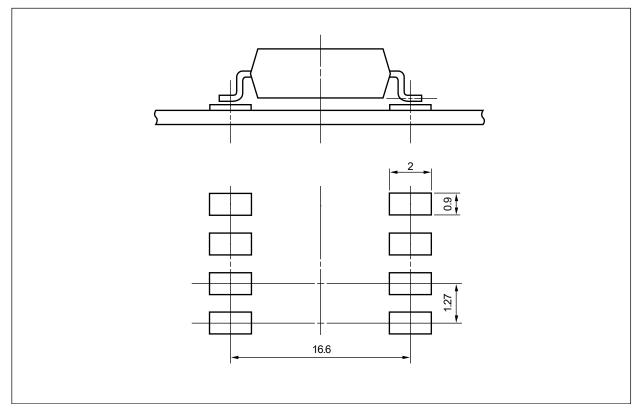
Remark The graphs indicate nominal characteristics.



# TAPING SPECIFICATIONS (UNIT: mm)



# RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



## [8pin LSDIP]

Remark All dimensions in this figure must be evaluated before use.



# NOTES ON HANDLING

- 1. Recommended soldering conditions
  - (1) Infrared reflow soldering
    - Peak reflow temperature
    - Time of peak reflow temperature
      - Time of temperature higher than 220 °C
    - Time to preheat temperature from 120 to 180 °C
    - Number of reflows
    - Flux

 $\begin{array}{l} \text{60 seconds or less} \\ \text{C} & 120 \pm 30 \text{ s} \end{array}$ 

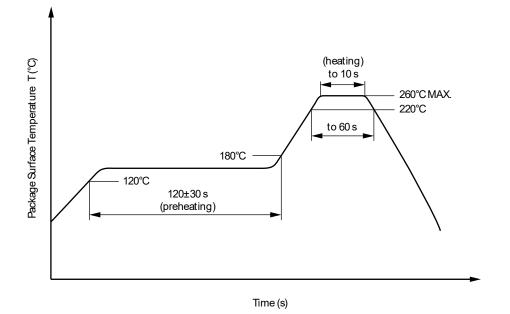
10 seconds or less

Three

Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

260 °C or below (package surface temperature)

## Recommended Temperature Profile of Infrared Reflow



#### (2) Wave soldering

- Temperature 260 °C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120 °C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

### (3) Soldering by Soldering Iron

- Peak Temperature (lead part temperature) 350 °C or below
  - e) 350 °C or below 3 seconds or less
  - Time (each pins) Flux Rosin flu
    - Rosin flux containing small amount of chlorine
    - (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)
- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead
- (b) Please be sure that the temperature of the package would not be heated over 110 °C

(4) Cautions

- Flux Cleaning
  - Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.
- Do not use fixing agents or coatings containing halogen-based substances.



### 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

## USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. Board designing
  - (1) By-pass capacitor of more than 0.1  $\mu$ F is used between V<sub>CC</sub> and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
  - (2) When designing the printed wiring board, ensure that the pattern of the IGBT collectors/emitters is not too close to the input block pattern of the photocoupler.

If the pattern is too close to the input block and coupling occurs, a sudden fluctuation in the voltage on the IGBT output side might affect the photocoupler's LED input, leading to malfunction or degradation of characteristics. (If the pattern needs to be close to the input block, to prevent the LED from lighting during the off state due to the abovementioned coupling, design the input-side circuit so that the bias of the LED is reversed, within the range of the recommended operating conditions, and be sure to thoroughly evaluate operation.)

(3) Pin 1, 4 (which is an NC<sup>\*1</sup> pin) can either be connected directly to the GND pin on the LED side or left open. Also, Pin 6 (which is an NC<sup>\*1</sup> pin) can either be connected directly to the GND pin on the detector side or left open.

Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.

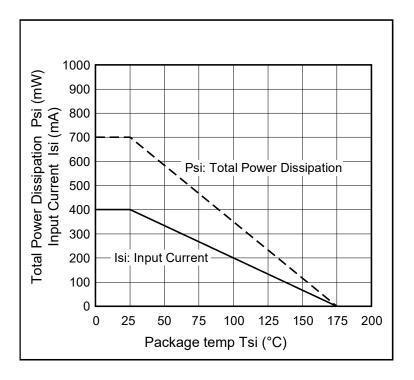
- Note: \*1. NC: Non-Connection (No Connection)
- 3. Make sure the rise/fall time of the forward current is 0.5  $\mu$ s or less.
- 4. In order to avoid malfunctions, make sure the rise/fall slope of the supply voltage is 3 V/ $\mu$ s or less.
- 5. Avoid storage at a high temperature and high humidity.



# SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

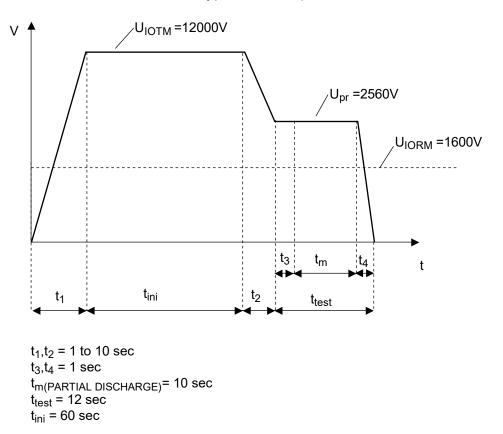
Parameter	Symbol	Rating	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/110/21	
Dielectric strength			
maximum operating isolation voltage	UIORM	1 600	V <sub>peak</sub>
Test voltage (partial discharge test, procedure a for type test and random test)	Upr	2 560	V <sub>peak</sub>
$U_{pr}$ = 1.6 × $U_{IORM.}$ , $P_d < 5 \text{ pC}$			
Test voltage (partial discharge test, procedure b for all devices)	Upr	3 000	$V_{peak}$
$U_{pr}$ = 1.875 × $U_{IORM.}$ , $P_d$ < 5 pC			
Highest permissible overvoltage	UIOTM	12 000	$V_{peak}$
Degree of pollution (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		III a	
Storage temperature range	T <sub>stg</sub>	-55 to +125	°C
Operating temperature range		-40 to +110	°C
Isolation resistance, minimum value			
V <sub>IO</sub> = 500 V dc at T <sub>A</sub> = 25 °C	Ris MIN.	10 <sup>12</sup>	Ω
$V_{IO}$ = 500 V dc at T <sub>A</sub> MAX. at least 100 °C	Ris MIN.	10 <sup>11</sup>	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	175	°C
Current (input current I <sub>F</sub> , Psi = 0)	lsi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistance			
$V_{IO}$ = 500 V dc at T <sub>A</sub> = Tsi	Ris MIN.	10 <sup>9</sup>	Ω

# Dependence of maximum safety ratings with package temperature

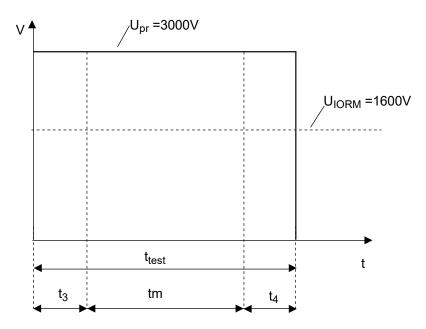




Method a Destructive Test, Type and Sample Test



Method b Non-destructive Test, 100% Production Test



 $t_{3}, t_{4} = 0.1 \text{ sec}$  $t_{m}(\text{PARTIAL DISCHARGE})= 1.0 \text{ sec}$  $t_{test} = 1.2 \text{ sec}$ 



Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.
	<ul> <li>Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.</li> </ul>
	<ol> <li>Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.</li> </ol>
	<ol><li>Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.</li></ol>
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	• Do not lick the product or in any way allow it to enter the mouth.



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