

600V - 3A - MOS FET High Speed Power Switching

Rev.1.00

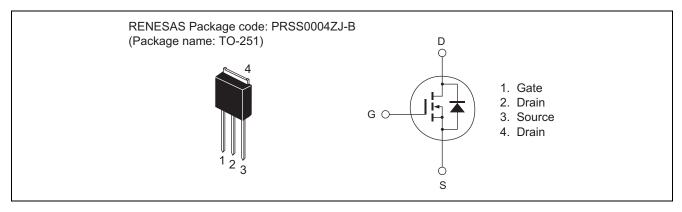
Jan 23, 2013

Datasheet

#### Features

- Low on-resistance
- $R_{DS(on)} = 3.3 \ \Omega$  typ. (at  $I_D = 1.0 \ A$ ,  $V_{GS} = 10 \ V$ ,  $Ta = 25^{\circ}C$ )
- Low drive current
- High density mounting

## Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$	
Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	600	V	
Gate to source voltage	V <sub>GSS</sub>	±30	V	
Drain current	I <sub>D</sub>	3	А	
Drain peak current	Note1 I <sub>D (pulse)</sub>	6	A	
Body-drain diode reverse drain current	I <sub>DR</sub>	3	А	
Body-drain diode reverse drain peak current	I <sub>DR (pulse)</sub> Note1	6	А	
Avalanche current	I <sub>AP</sub> <sup>Note2</sup>	3	А	
Avalanche energy	E <sub>AR</sub> <sup>Note2</sup>	0.49	mJ	
Channel dissipation	Pch Note3	40.3	W	
Channel to case thermal impedance	θch-c	3.1	°C/W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1.  $PW \leq 10~\mu s,\,duty~cycle \leq 1\%$ 

2. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C

3. Value at Tc =  $25^{\circ}C$ 



## **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	600	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	1	μΑ	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS}=\pm 30~V,~V_{DS}=0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	3.5	_	4.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS(on)</sub>	_	3.3	4.3	Ω	$I_D$ = 1.5 A, $V_{GS}$ = 10 V <sup>Note4</sup>
Input capacitance	Ciss		285		pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	31	_	pF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	3.5	_	pF	
Turn-on delay time	t <sub>d(on)</sub>	_	13	_	ns	$    I_D = 1.5 \text{ A} \\ V_{GS} = 10 \text{ V} \\ R_L = 200 \Omega \\ Rg = 10 \Omega $
Rise time	tr	_	13	_	ns	
Turn-off delay time	t <sub>d(off)</sub>	_	22	_	ns	
Fall time	t <sub>f</sub>	_	22	—	ns	
Total gate charge	Qg	—	9.0	—	nC	V <sub>DD</sub> = 480 V
Gate to source charge	Qgs	_	1.7	_	nC	V <sub>GS</sub> = 10 V I <sub>D</sub> = 3 A
Gate to drain charge	Qgd	_	4.9	_	nC	
Body-drain diode forward voltage	V <sub>DF</sub>	_	0.9	1.5	V	$I_F = 3 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	300	—	ns	$I_F = 3 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

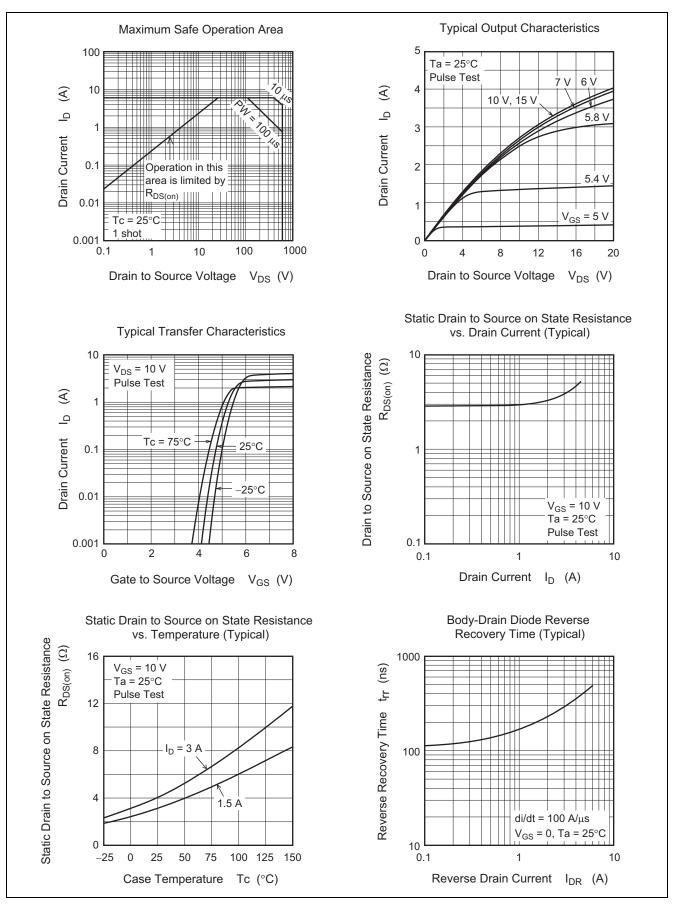
5. Since this device is equipped with high voltage FET chip ( $V_{DSS} \ge 600 \text{ V}$ ), high voltage may be supplied. Therefore, please be sure to confirm about Electric discharge between Drain terminal and other terminal.

This device is sensitive to electrostatic discharge.
It is recommended to adopt appropriate cautions when handling this product.

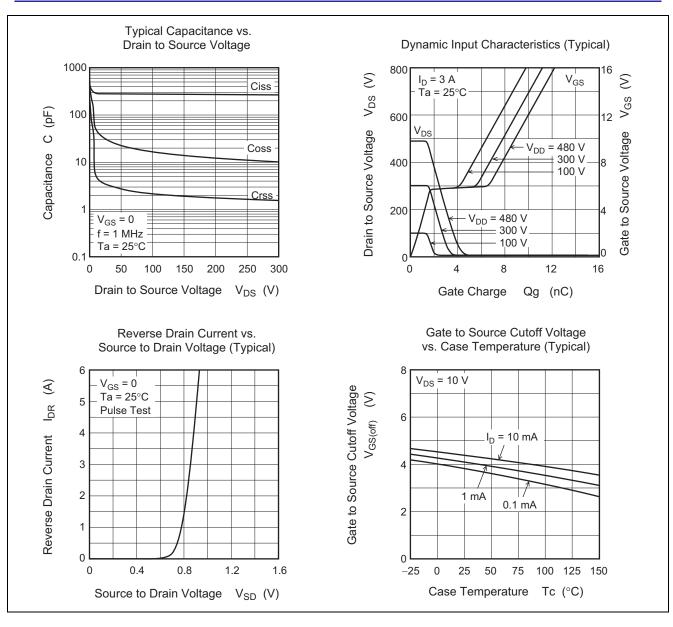
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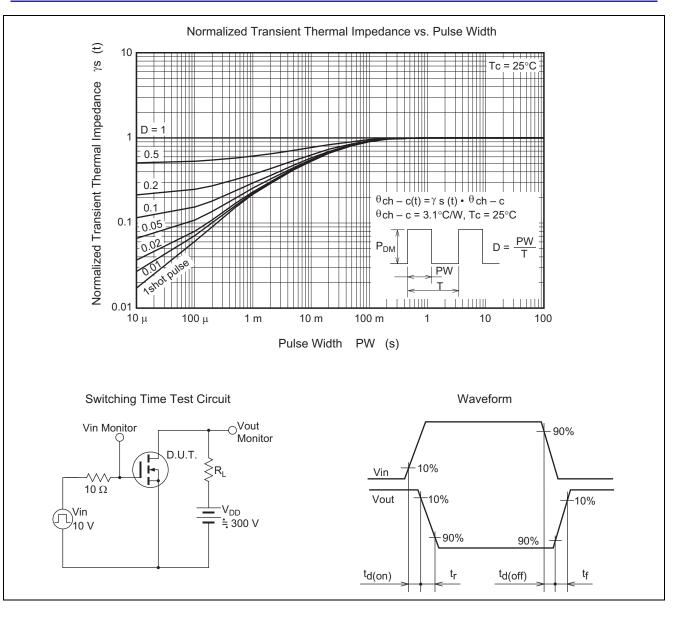
#### **Main Characteristics**





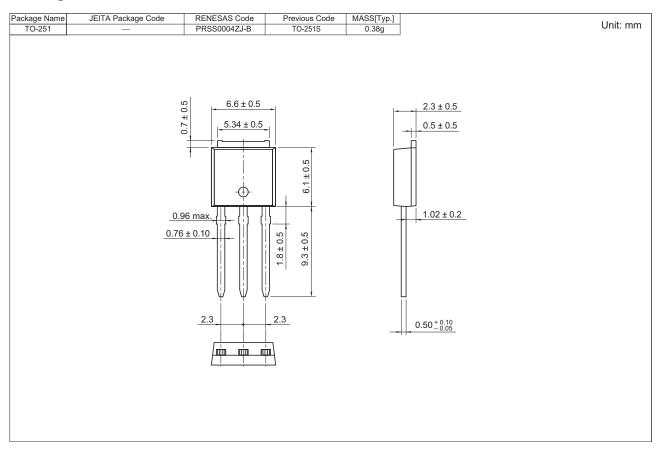








#### **Package Dimensions**



#### **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJK6032DPH-E0#T2	70 pcs	Tube



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