

# RJH60V2BDPE

600V - 12A - IGBT Application: Inverter R07DS0744EJ0100 Rev.1.00 Apr 25, 2012

#### **Features**

- Short circuit withstand time (6 µs typ.)
- Low collector to emitter saturation voltage  $V_{CE(sat)}=1.6~V$  typ. (at  $I_C=12~A,~V_{GE}=15~V,~Ta=25^{\circ}C$ )
- Built in fast recovery diode (25 ns typ.) in one package
- Trench gate and thin wafer technology
- High speed switching  $t_f=75$  ns typ. (at  $V_{CC}=300$  V,  $V_{GE}=15$  V,  $I_C=12$  A, Rg=5  $\Omega$ , Ta=25°C, inductive load)

#### **Outline**

RENESAS Package code: PRSS0004AE-B (Package name: LDPAK (S)-(1) )

1. Gate 2. Collector 3. Emitter 4. Collector

### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item		Symbol	Ratings	Unit
Collector to emitter voltage / diode reverse voltage		V <sub>CES</sub> / V <sub>R</sub>	600	V
Gate to emitter voltage		$V_{GES}$	±30	V
Collector current	Tc = 25°C	Ic	25	А
	Tc = 100°C	Ic	12	A
Collector peak current		ic(peak) Note1	50	А
Collector to emitter diode forward current		i <sub>DF</sub>	12	A
Collector to emitter diode forward peak current		i <sub>DF</sub> (peak) Note1	50	A
Collector dissipation		P <sub>C</sub> Note2	63	W
Junction to case thermal resistance (IGBT)		θj-c <sup>Note2</sup>	1.98	°C/W
Junction to case thermal resistance (Diode)		θj-cd <sup>Note2</sup>	1.75	°C/W
Junction temperature		Tj	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tc = 25°C

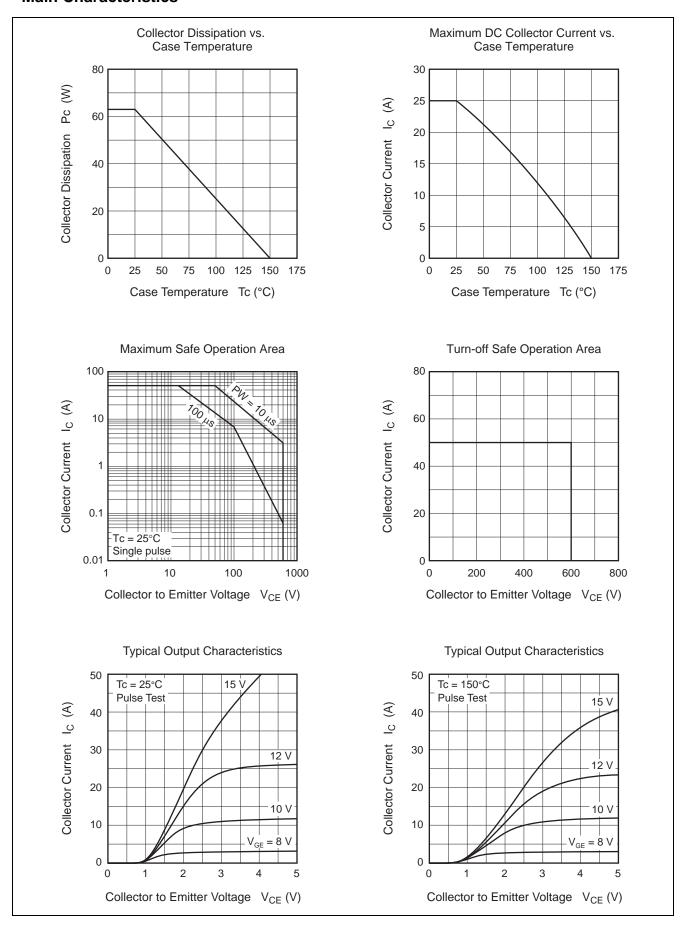
## **Electrical Characteristics**

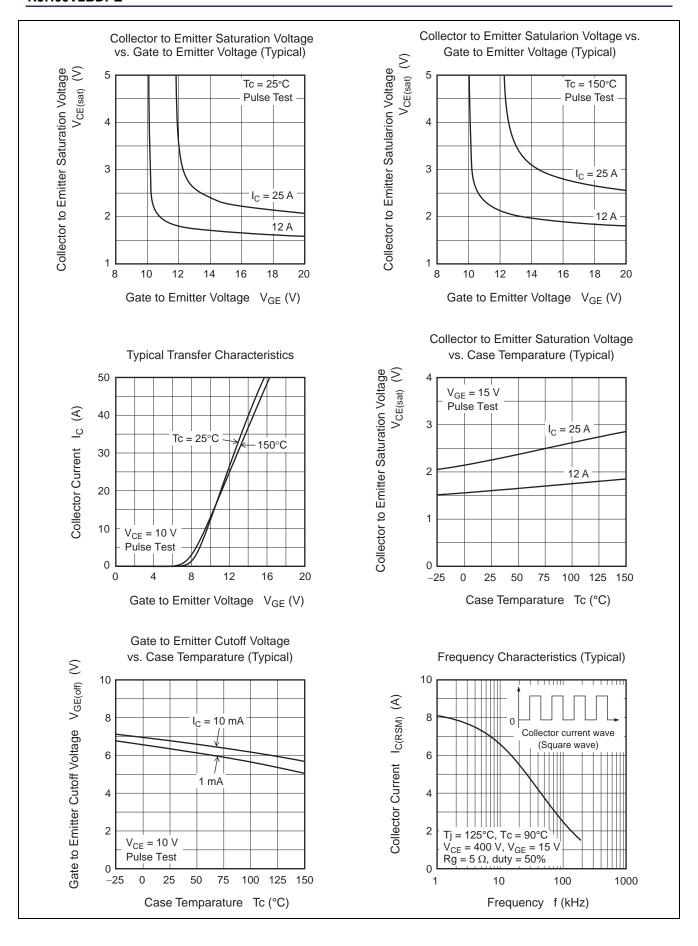
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions	
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	600	_	_	V	$I_C = 10 \mu A, V_{GE} = 0$	
Zero gate voltage collector current / Diode reverse current	I <sub>CES</sub> / I <sub>R</sub>	_	_	5	μА	$V_{CE} = 600 \text{ V}, V_{GE} = 0$	
Gate to emitter leak current	I <sub>GES</sub>	_	_	±1	μΑ	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$	
Gate to emitter cutoff voltage	$V_{GE(off)}$	5.5	_	7.5	V	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	_	1.6	2.2	V	$I_C = 12 \text{ A}, V_{GE} = 15 \text{ V}^{\text{Note3}}$	
	V <sub>CE(sat)</sub>	_	2.2	_	V	$I_C = 25 \text{ A}, V_{GE} = 15 \text{ V}^{\text{Note3}}$	
Input capacitance	Cies	_	450	_	pF	V <sub>CE</sub> = 25 V	
Output capacitance	Coes	_	37	_	pF	$V_{GE} = 0$	
Reverse transfer capacitance	Cres	_	18	_	pF	f = 1 MHz	
Total gate charge	Qg	_	32	_	nC	V <sub>GE</sub> = 15 V	
Gate to emitter charge	Qge	_	5	_	nC	V <sub>CE</sub> = 300 V	
Gate to collector charge	Qgc	_	17	_	nC	I <sub>C</sub> = 12 A	
Turn-on delay time	t <sub>d(on)</sub>	_	33	_	ns	V <sub>CC</sub> = 300 V	
Rise time	t <sub>r</sub>	_	15	_	ns	V <sub>GE</sub> = 15 V	
Turn-off delay time	t <sub>d(off)</sub>	_	65	_	ns	$I_{\rm C} = 12  {\rm A}$	
Fall time	t <sub>f</sub>	_	75	_	ns	$Rg = 5 \Omega$	
Turn-on energy	Eon	_	0.03	_	mJ	Inductive load	
Turn-off energy	E <sub>off</sub>	_	0.18	_	mJ		
Total switching energy	E <sub>total</sub>	_	0.21	_	mJ		
Short circuit withstand time	t <sub>sc</sub>	3	6	_	μ\$	$Tc = 100  ^{\circ}C$ $V_{CC} \leq 360  \text{V},  V_{GE} = 15  \text{V}$	
FRD forward voltage	V <sub>F</sub>	_	2.5		V	I <sub>F</sub> = 12 A <sup>Note3</sup>	
FRD reverse recovery time	t	l <u> </u>	25	l <u> </u>	ne	I_ = 12 A	

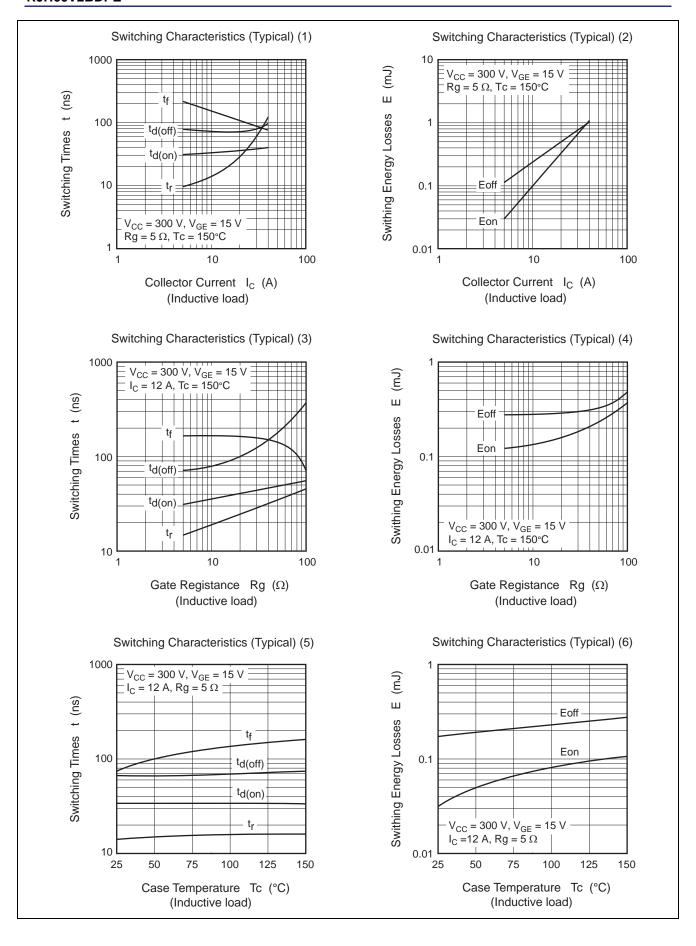
FRD forward voltage	V <sub>F</sub>	_	2.5		V	I <sub>F</sub> = 12 A Note3
FRD reverse recovery time	t <sub>rr</sub>	_	25	_	ns	I <sub>F</sub> = 12 A
FRD reverse recovery charge	Qrr	_	0.02	_	μС	di <sub>F</sub> /dt = 100 A/μs
FRD peak reverse recovery current	Im	_	1.2		Α	

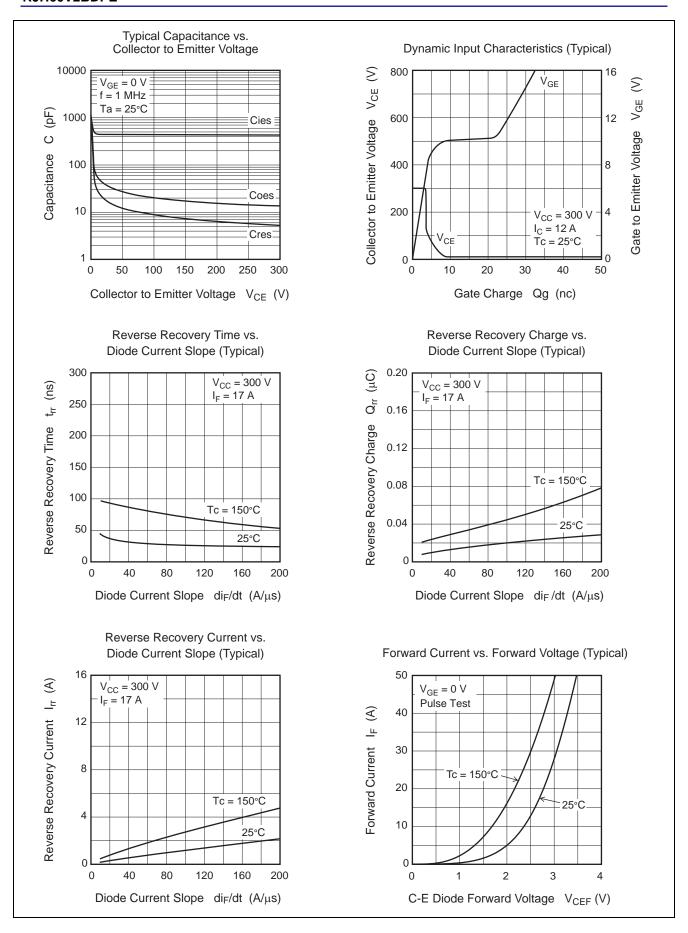
Notes: 3. Pulse test.

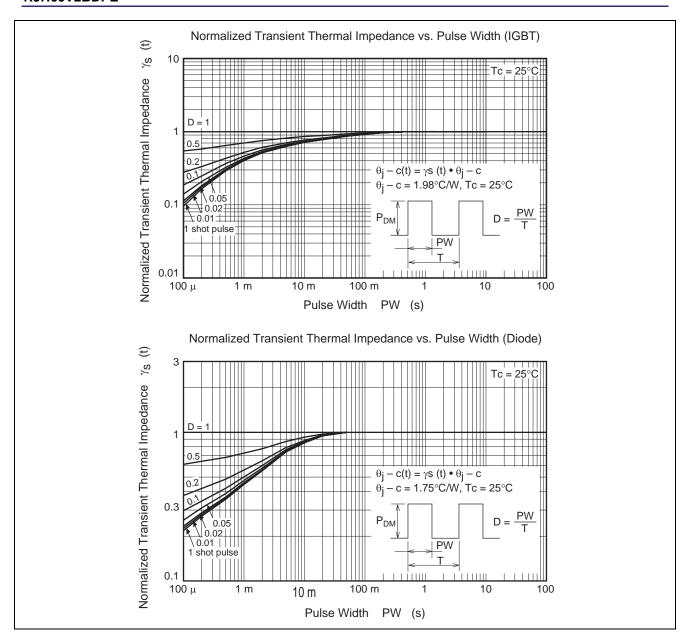
### **Main Characteristics**

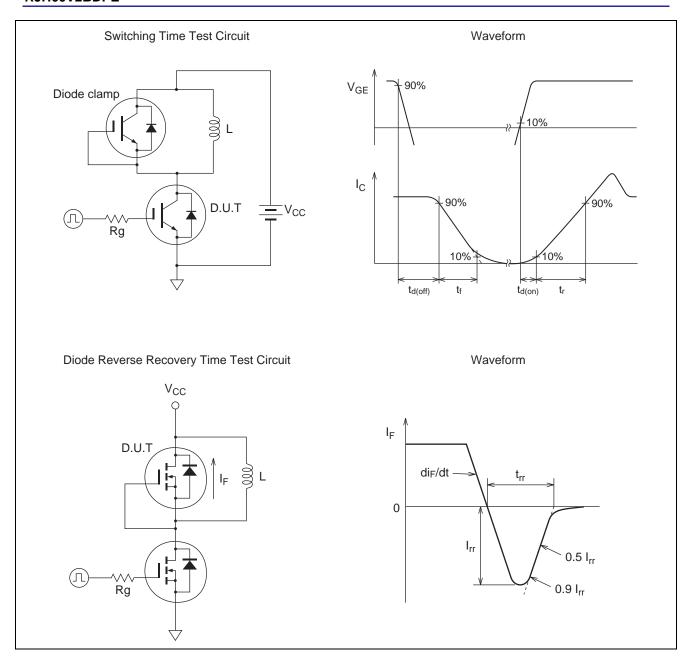




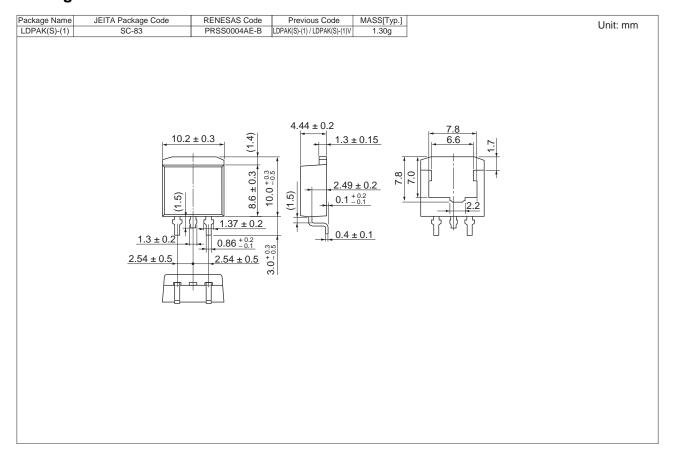








## **Package Dimension**



## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJH60V2BDPE-00#J3	1000 pcs	Taping

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