

RJK6024DPD

600V - 0.4A - MOS FET High Speed Power Switching R07DS0688EJ0200 (Previous: REJ03G1936-0100) Rev.2.00

Feb 27, 2012

Features

• Low on-resistance $R_{DS(on)} = 28~\Omega~typ.~(at~I_D=0.2~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$

- Low drive current
- High density mounting

Outline

RENESAS Package code: PRSS0004ZG-A
(Package name : MP-3A)

1. Gate
2. Drain
3. Source
4. Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	0.4	Α
Drain peak current	I _{D(pulse)} Note1	0.6	Α
Body-drain diode reverse drain current	I _{DR}	0.4	Α
Body-drain diode reverse drain peak current	I _{DR(pulse)} Note1	0.6	А
Channel dissipation	Pch Note2	27.2	W
Channel to case thermal impedance	θch-c	4.58	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

Electrical Characteristics

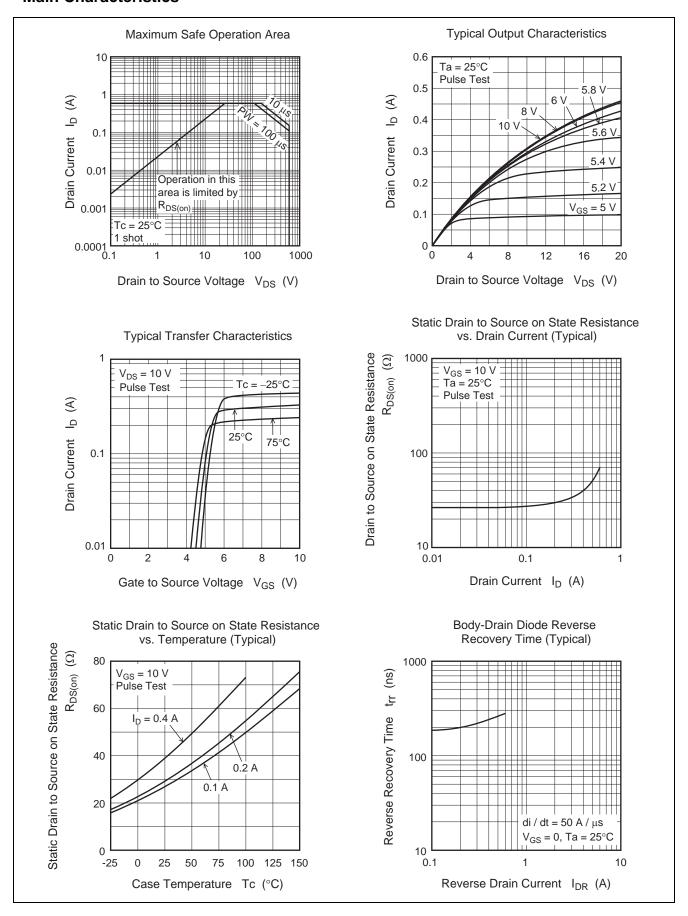
 $(Ta = 25^{\circ}C)$

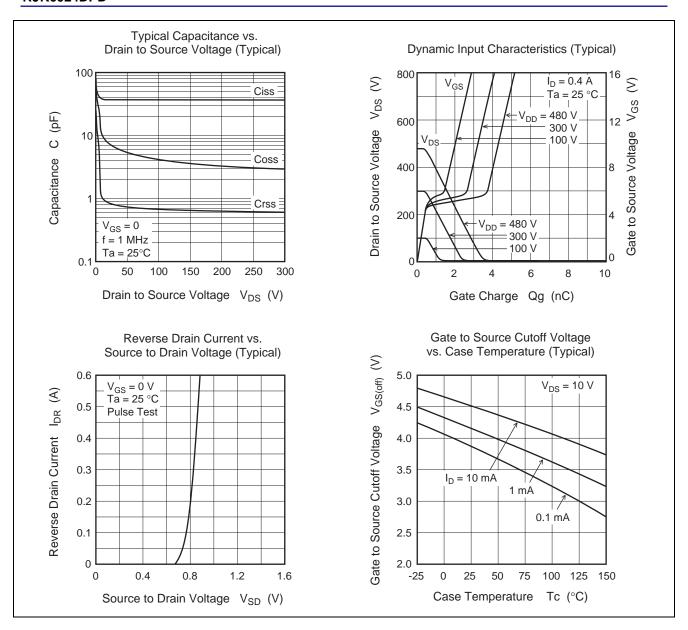
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 600 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3	_	5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	_	28	42	Ω	$I_D = 0.2 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	37.5	_	pF	V _{DS} = 25 V V _{GS} = 0 f = 1 MHz
Output capacitance	Coss	_	7.5	_	pF	
Reverse transfer capacitance	Crss	_	0.9	_	pF	
Turn-on delay time	t _{d(on)}	_	30	_	ns	$I_D = 0.2 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 1500 \Omega$ $Rg = 10 \Omega$
Rise time	t _r	_	14.5	_	ns	
Turn-off delay time	$t_{d(off)}$	_	48	_	ns	
Fall time	t _f	_	77	_	ns	
Total gate charge	Qg	_	4.3	_	nC	$V_{DD} = 480 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 0.15 \text{ A}$
Gate to source charge	Qgs	_	0.5	_	nC	
Gate to drain charge	Qgd	_	3.2	_	nC	
Body-drain diode forward voltage	V_{DF}	_	0.85	1.45	V	$I_F = 0.4 \text{ A}, V_{GS} = 0^{\text{Note3}}$
Body-drain diode reverse recovery time	t _{rr}	_	230	_	ns	$I_F = 0.4 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

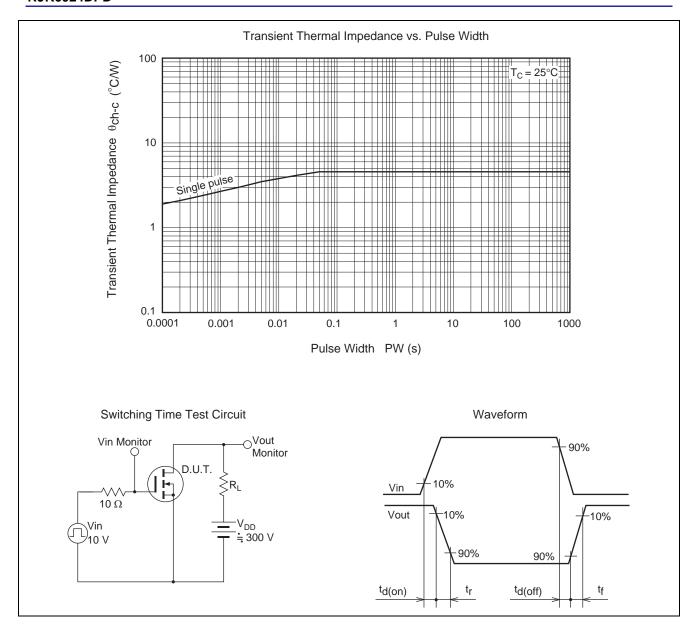
Notes: 3. Pulse test

- 4. Since this device is equipped with high voltage FET chip (V_{DSS} ≥ 600 V), high voltage may be supplied. Therefore, please be sure to confirm about Electric discharge between Drain terminal and other terminal.
- This device is sensitive to electrostatic discharge.
 It is recommended to adopt appropriate cautions when handling this product.

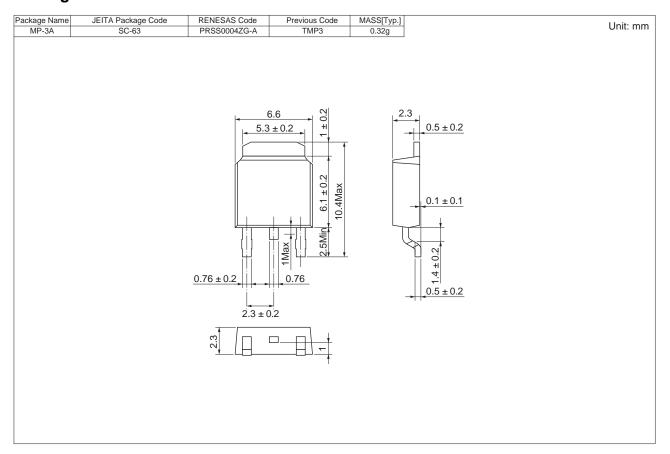
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK6024DPD-00#J2	3000 pcs	Taping

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