

# RJK03M3DPA

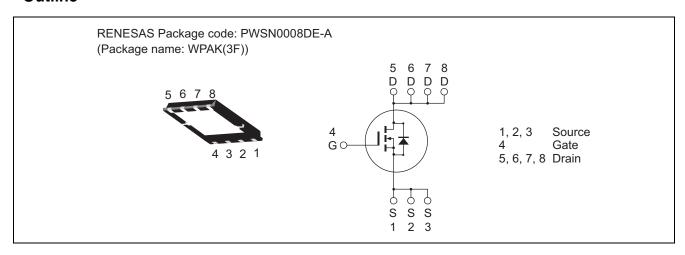
30V, 40A, 3.9mΩmax. N Channel Power MOS FET High Speed Power Switching

R07DS0767EJ0200 Rev.2.00 Feb 12, 2013

#### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- Pb-free
- Halogen-free

### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	40	Α
Drain peak current	I <sub>D(pulse)</sub> Note1	160	А
Body-drain diode reverse drain current	I <sub>DR</sub>	40	А
Avalanche current	I <sub>AP</sub> Note 2	15	А
Avalanche energy	E <sub>AS</sub> Note 2	22.5	mJ
Channel dissipation	Pch Note3	35	W
Channel to case thermal impedance	θch-c Note3	3.57	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. Tc = 25°C

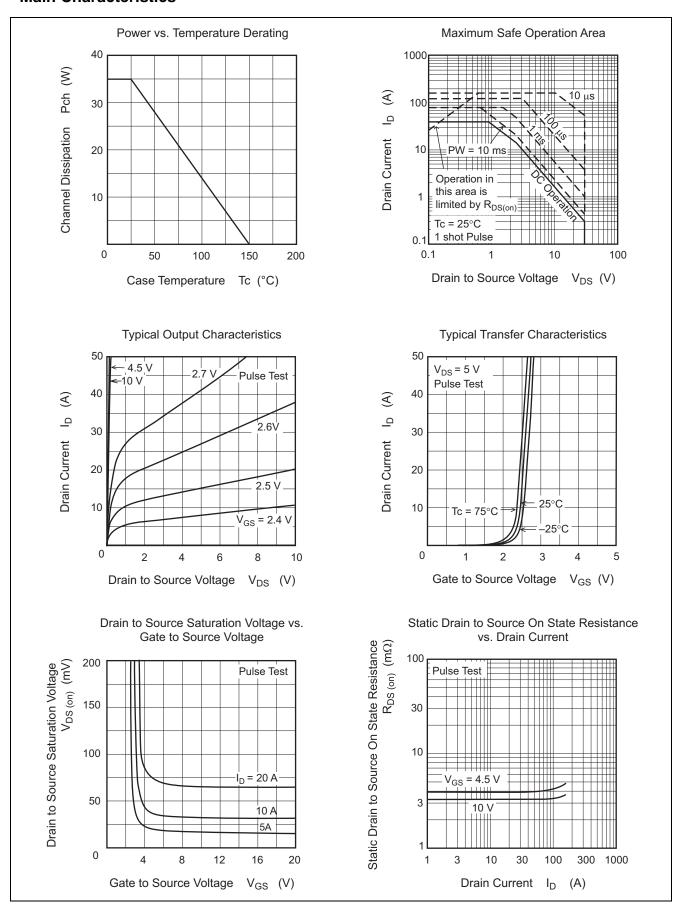
# **Electrical Characteristics**

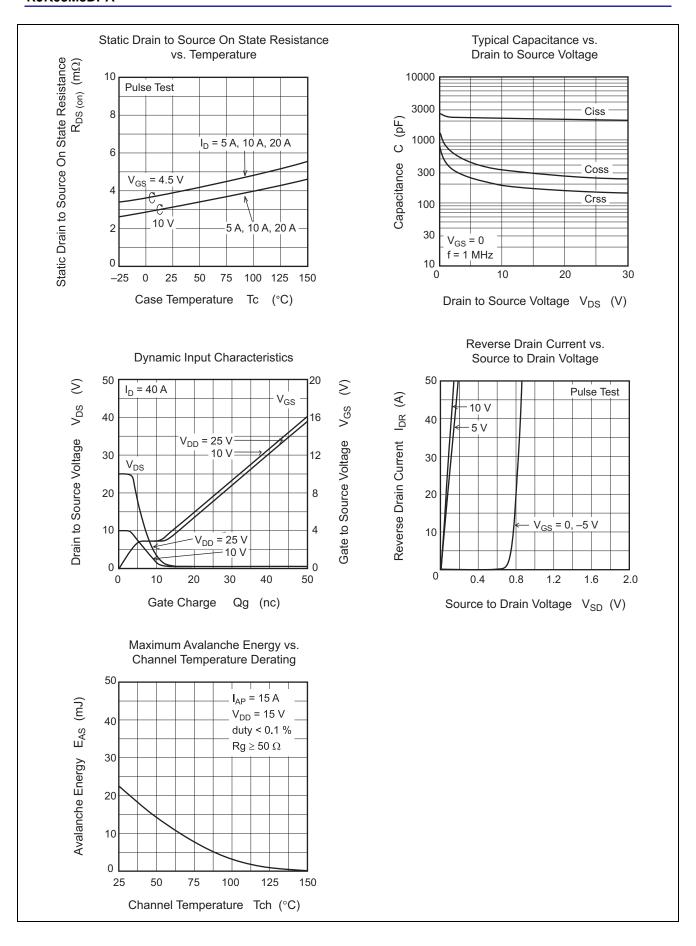
 $(Ta = 25^{\circ}C)$ 

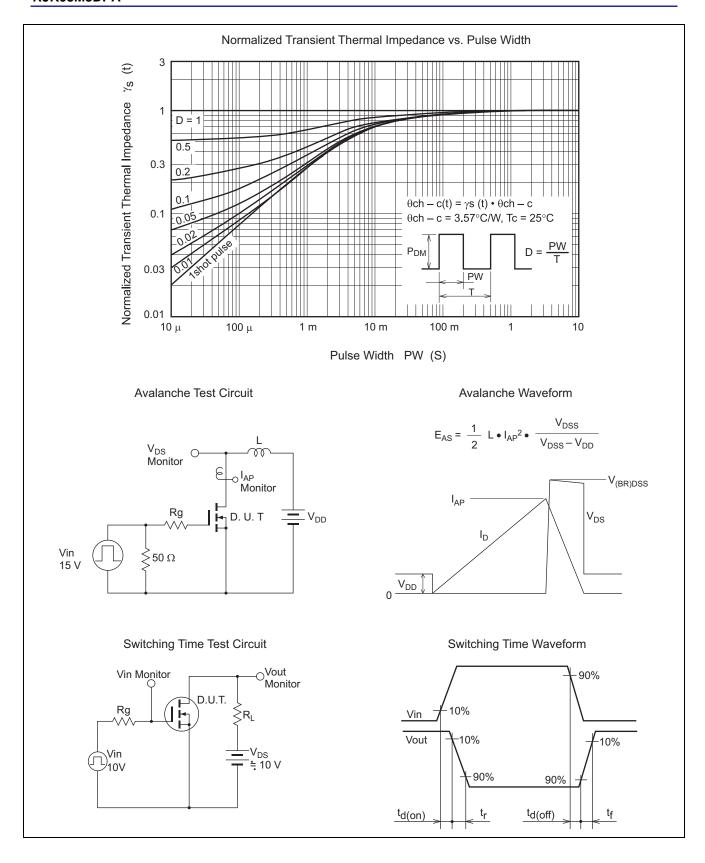
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	_	_	± 0.5	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	3.2	3.9	mΩ	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	3.9	5.1	mΩ	$I_D = 20 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	_	90	_	S	I <sub>D</sub> = 20 A, V <sub>DS</sub> = 5 V <sup>Note4</sup>
Input capacitance	Ciss	_	2150	3010	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	335	_	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	Crss	_	190	_	pF	f = 1 MHz
Gate Resistance	Rg		1.85	3.7	Ω	
Total gate charge	Qg		15.7	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs		6.6	_	nC	V <sub>GS</sub> = 4.5 V I <sub>D</sub> = 40 A
Gate to drain charge	Qgd	_	4.5	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	4.1	_	ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A
Rise time	t <sub>r</sub>		3.0	_	ns	$V_{DD} \cong 10 \text{ V}$ $R_L = 0.5 \Omega$ $Rg = 4.7 \Omega$
Turn-off delay time	$t_{d(off)}$		39.3	_	ns	
Fall time	t <sub>f</sub>		12.0	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.84	1.09	V	I <sub>F</sub> = 40 A, V <sub>GS</sub> = 0 <sup>Note4</sup>
Body–drain diode reverse recovery time	t <sub>rr</sub>	_	8.0	_	ns	$I_F = 40 \text{ A}, V_{GS} = 0$ $di_F / dt = 500 \text{ A} / \mu \text{s}$

Notes: 4. Pulse test

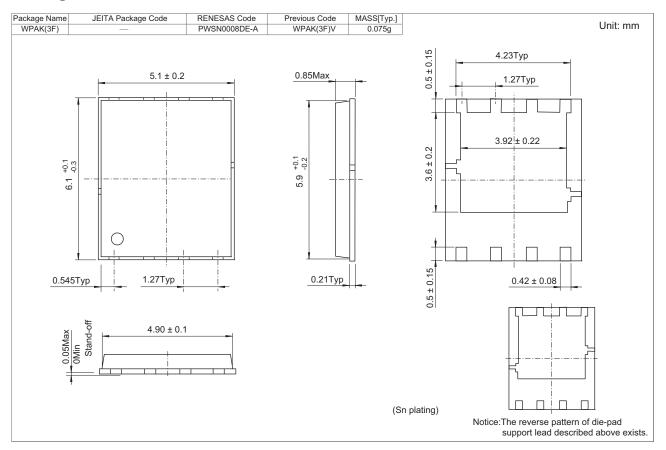
### **Main Characteristics**







## **Package Dimensions**



# **Ordering Information**

Orderable Part Number	Quantity	Shipping Container			
RJK03M3DPA-00-J5A	3000 pcs	Taping			

Note: The symbol of 2nd "-" is occasionally presented as "#".

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