

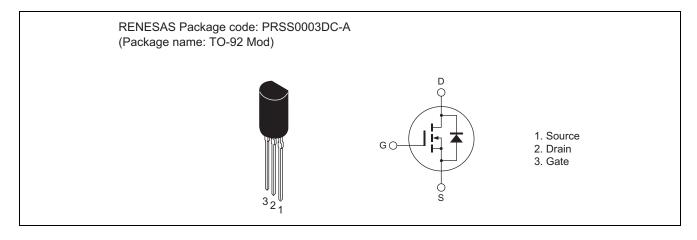
# RJK4002DJE

400V - 3A - MOS FET High Speed Power Switching R07DS0842EJ0200 Rev.2.00 Aug 03, 2012

### **Features**

- Low on-state resistance  $R_{DS(on)}=2.4~\Omega~typ.~(at~I_D=1.5~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$
- High speed switching

### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	400	V
Gate to source voltage	$V_{GSS}$	±30	V
Drain current	I <sub>D</sub> Note1	3	А
Drain peak current	I <sub>D(pulse)</sub> Note4	6	А
Body-drain diode reverse drain current	I <sub>DR</sub> Note1	3	А
Body-drain diode reverse drain peak current	I <sub>DR(pulse)</sub> Note4	6	А
Avalanche current	I <sub>AP</sub> Note3	2.5	А
Avalanche energy	E <sub>AR</sub> Note3	0.357	mJ
Channel dissipation	Pch Note 2	2.54	W
Channel to ambient thermal Impedance	θch-a	49.2	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Limited by Tch max.

- 2. Value at Tc = 25°C
- 3. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C
- 4. Pulse width limited by safe operating area.

## **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

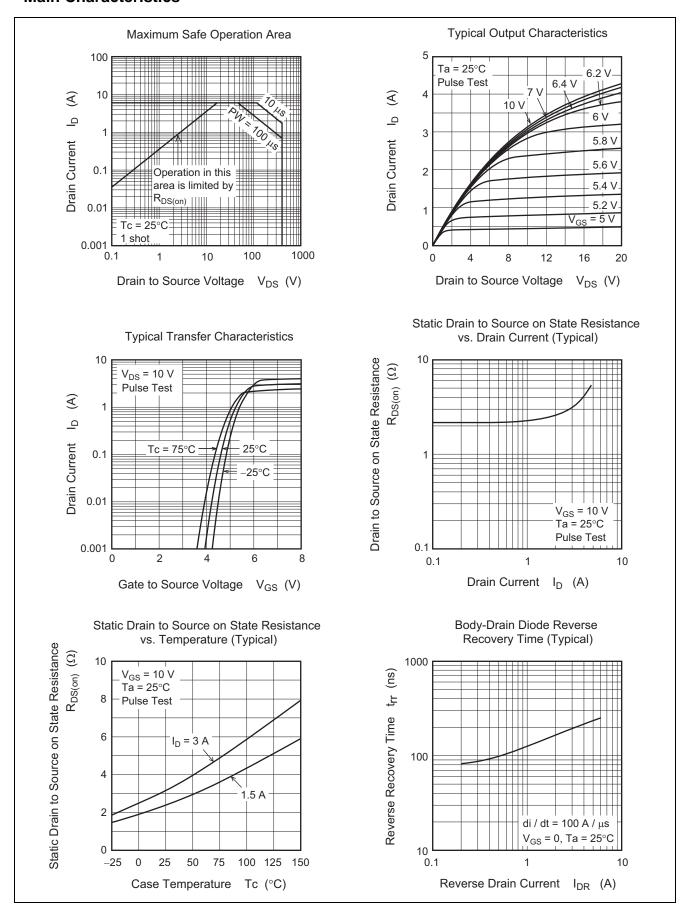
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	400	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	3.5	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS(on)</sub>	_	2.4	2.9	Ω	$I_D = 1.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 5}}$
Input capacitance	Ciss	_	165	_	рF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	25	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	2.6	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d(on)</sub>	_	11	_	ns	I <sub>D</sub> = 1.5 A
Rise time	t <sub>r</sub>	_	12	_	ns	$V_{GS} = 10 \text{ V}$ $R_L = 133 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	23	_	ns	
Fall time	t <sub>f</sub>	_	20	_	ns	
Total gate charge	Qg	_	6.0	_	nC	V <sub>DD</sub> = 320 V
Gate to source charge	Qgs	_	1.2	_	nC	$V_{DS} = 100 \text{ V}$ $I_{D} = 3 \text{ A}$
Gate to drain charge	Qgd	_	3.4	_	nC	
Body-drain diode forward voltage	$V_{DF}$	_	0.9	1.5	V	$I_F = 3 \text{ A}, V_{GS} = 0^{\text{Note 5}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	200	_	ns	$I_F = 3 A, V_{GS} = 0$
						di <sub>F</sub> /dt = 100 A/μs

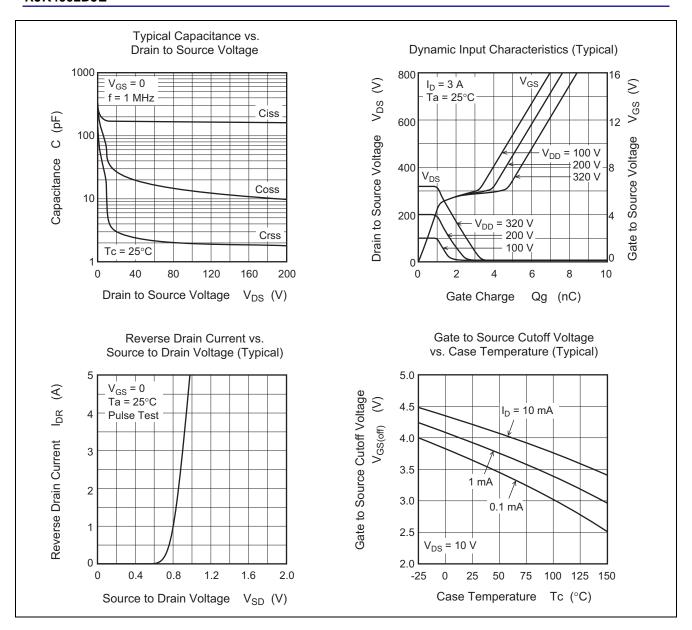
Note: 5. Pulse test

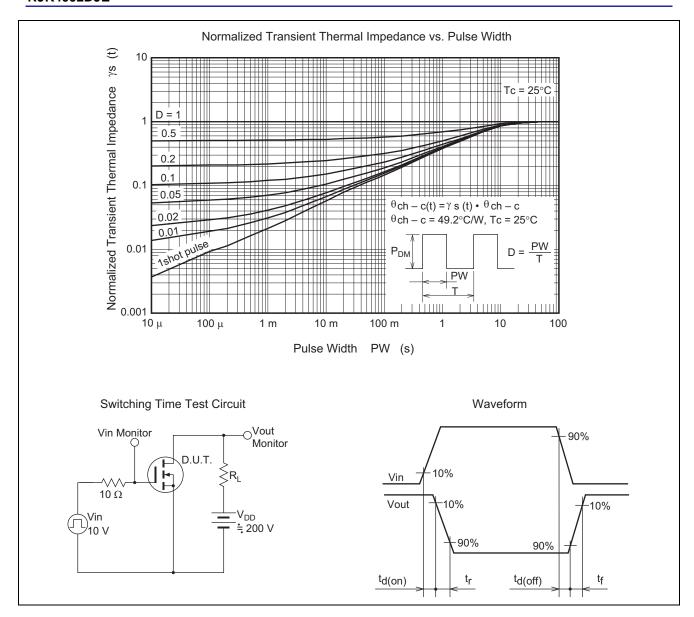
- 6. Since this device is equipped with high voltage FET chip (V<sub>DSS</sub> ≥ 400 V), high voltage may be supplied. Therefore, please be sure to confirm about electric discharge between drain terminal and other terminal.
- 7. This device is sensitive to electrostatic discharge.

  It is recommended to adopt appropriate cautions when handling this product.

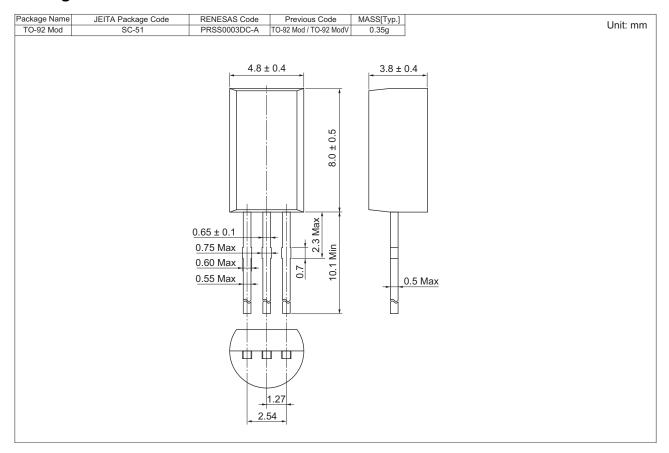
### **Main Characteristics**







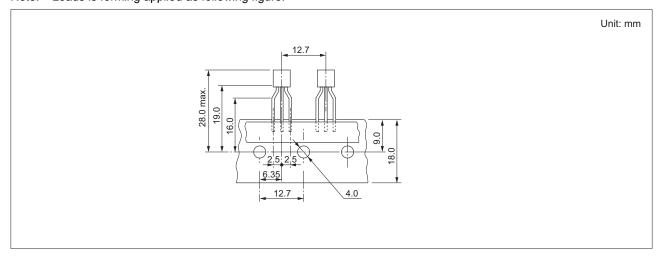
## **Package Dimensions**



# **Ordering Information**

Orderable Part No.	Quantity	Shipping Container
RJK4002DJE-00#Z0	2500 pcs	Hold Box, Radial Taping

Note: Leads is forming applied as following figure.



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Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Milliboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd. 7th Floor, Quantum Plaza, No.27 ZhiChunLu Ha Tel: +86-10-8235-1155, Fax: +86-10-8235-7679 i. nunLu Haidian District. Beiiing 100083. P.R.China

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852 2869-9022/9044

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