

RJK6006DPD

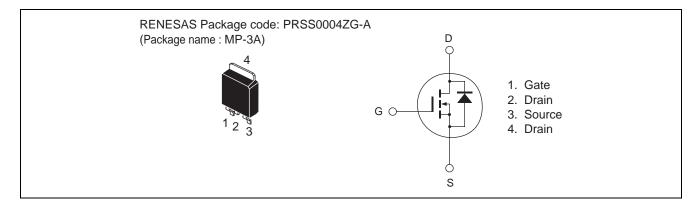
Silicon N Channel MOS FET High Speed Power Switching

REJ03G1935-0100 Rev.1.00 Jun 01, 2010

Features

- Low on-state resistance $R_{DS(on)}=1.4~\Omega~typ.~(at~I_D=2.5~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$
- High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D Note4	5	Α
Drain peak current	I _{D (pulse)} Note1	15	А
Body-drain diode reverse drain current	I _{DR}	5	А
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note1	15	Α
Avalanche current	I _{AP} Note3	5	Α
Avalanche energy	E _{AR} Note3	2	mJ
Channel dissipation	Pch Note 2	77.6	W
Channel to case thermal Impedance	θch-c	1.61	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C
- 4. Limited by maximum safe operation area

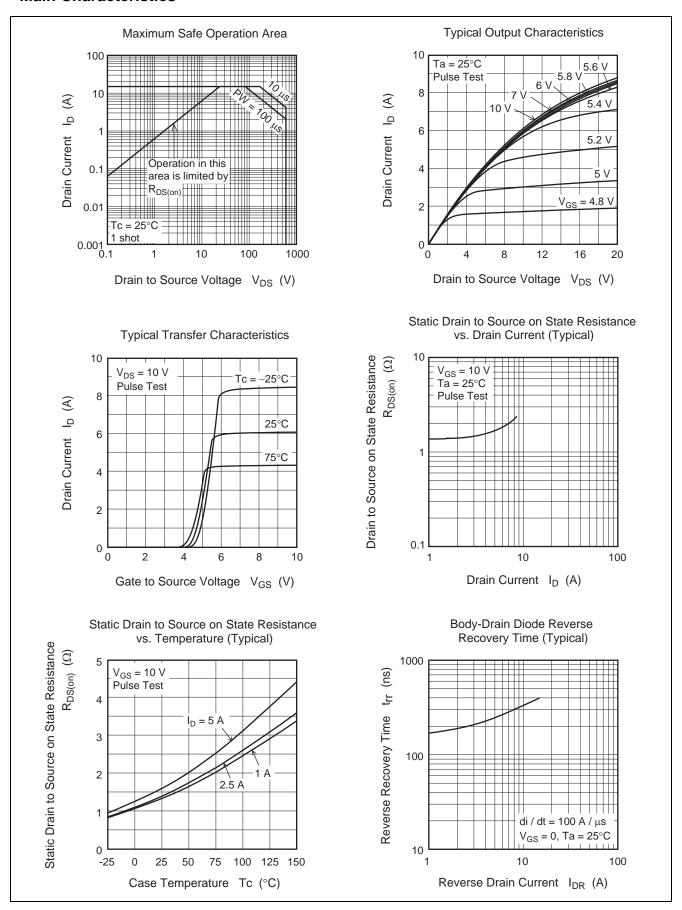
Electrical Characteristics

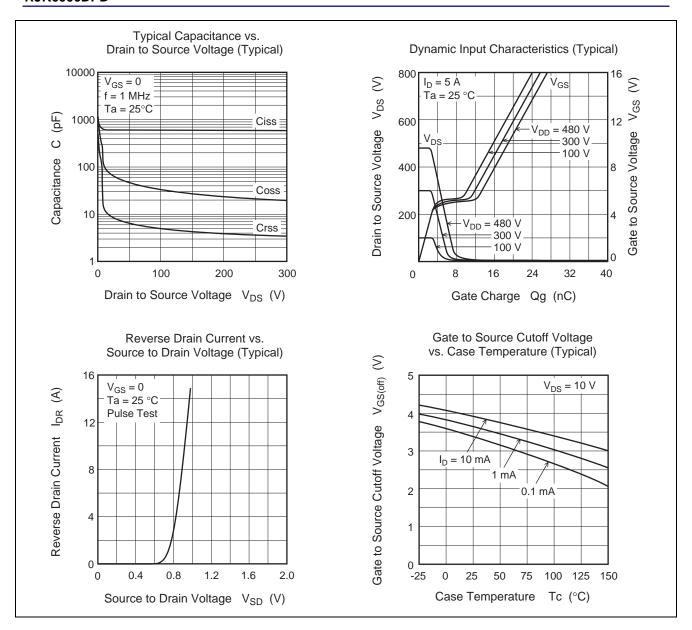
 $(Ta = 25^{\circ}C)$

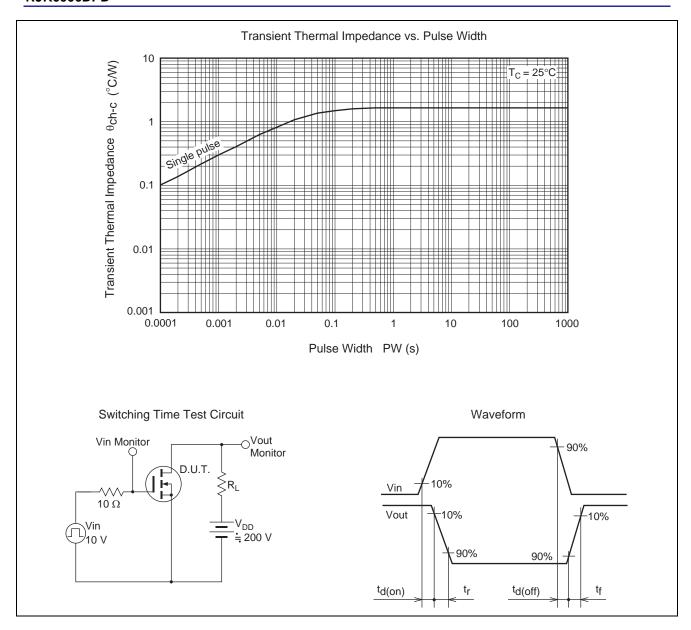
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 600 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS (on)}	_	1.4	1.6	Ω	$I_D = 2.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 5}}$
Input capacitance	Ciss	_	600	_	рF	V _{DS} = 25 V
Output capacitance	Coss	_	70	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	10	_	рF	f = 1 MHz
Turn-on delay time	t _{d (on)}	_	25	_	ns	I _D = 2.5 A
Rise time	t _r	_	17	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d (off)}	_	60	_	ns	$R_L = 80 \Omega$
Fall time	t _f	_	10	_	ns	$Rg = 10 \Omega$
Total gate charge	Qg	_	19	_	nC	V _{DD} = 480 V
Gate to source charge	Qgs	_	3.4	_	nC	V _{GS} = 10 V
Gate to drain charge	Qgd	_	9.2	_	nC	I _D = 5 A
Body-drain diode forward voltage	V_{DF}	_	0.9	1.5	V	$I_F = 5 \text{ A}, V_{GS} = 0^{\text{Note } 5}$
Body-drain diode reverse recovery time	t _{rr}	_	250	_	ns	I _F = 5 A, V _{GS} = 0
						di _F /dt = 100 A/μs

Note: 5. Pulse test

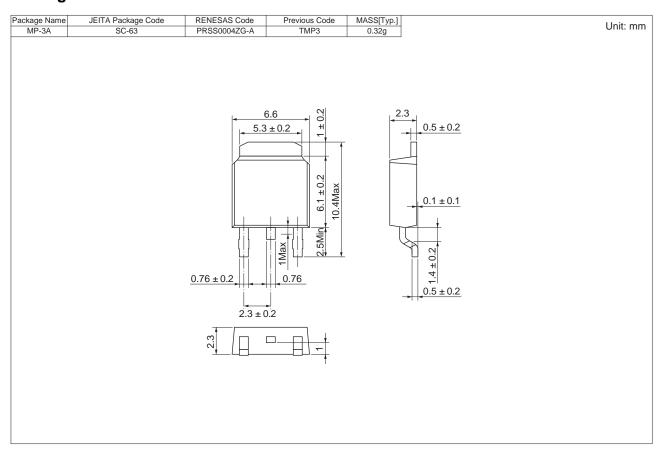
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK6006DPD-00-J2	3000 pcs	Taping

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