

# PS2705A-1

R08DS0073EJ0502

Rev.5.02

HIGH ISOLATION VOLTAGE SOP PHOTOCOUPLER

May 14, 2020

## DESCRIPTION

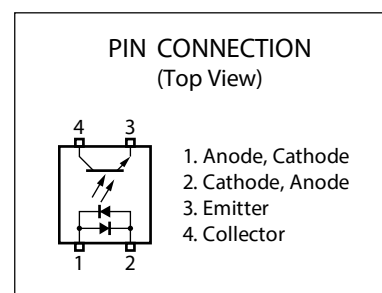
The PS2705A-1 is an optically coupled isolator containing a GaAs light emitting diode and an NPN silicon phototransistor to realize an excellent cost performance.

This package is SOP (Small Outline Package) type and has shield effect to cut off ambient light.

It is designed for high density mounting applications.

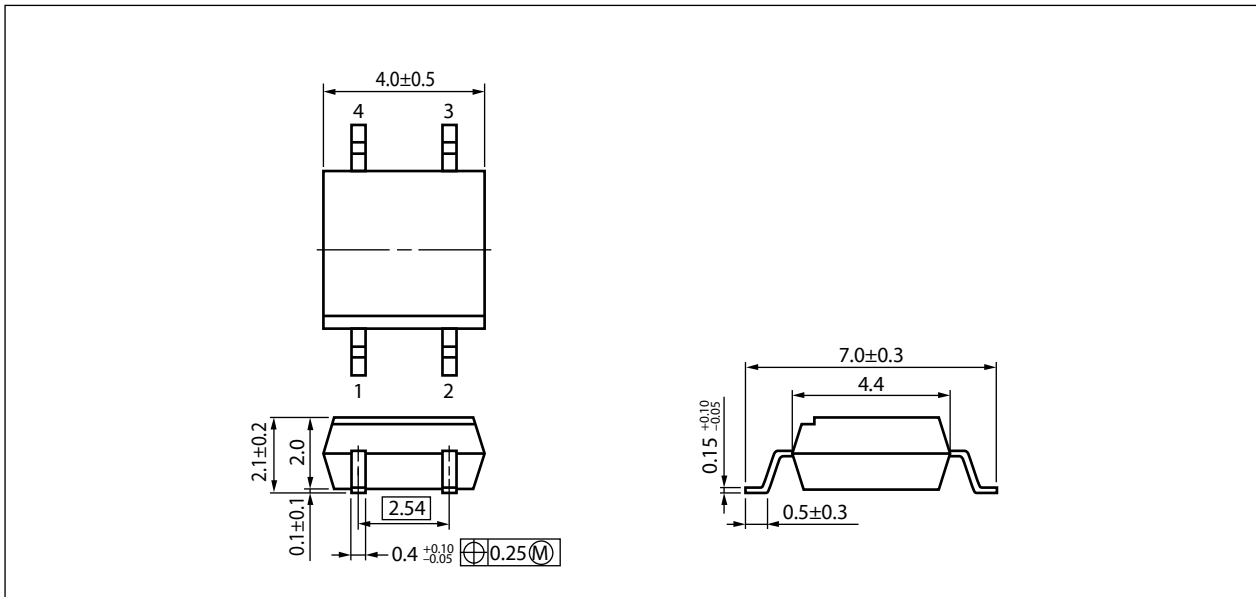
## FEATURES

- AC input response
- High isolation voltage (BV = 3 750 Vr.m.s.)
- SOP (Small Outline Package) type
- Ordering number of taping product: PS2705A-1-F3: 3 500 pcs/reel
- Pb-Free product
- Safety standards
  - UL approved: UL1577, Single protection
  - CSA approved: CAN/CSA-C22.2 No. 62368-1, Basic/Supplementary insulation
  - SEMKO approved: EN 62368-1, IEC 62368-1, Basic/Supplementary insulation
  - NEMKO approved: EN 62368-1, Basic/Supplementary insulation
  - FIMKO approved: EN 62368-1, Basic/Supplementary insulation
  - DEMKO approved: EN 62368-1, Basic/Supplementary insulation
  - VDE approved: DIN EN 60747-5-5 (Option)



## APPLICATIONS

- Hybrid IC
- Telephone/FAX.
- FA/OA equipment
- Programmable logic controllers
- Power supply

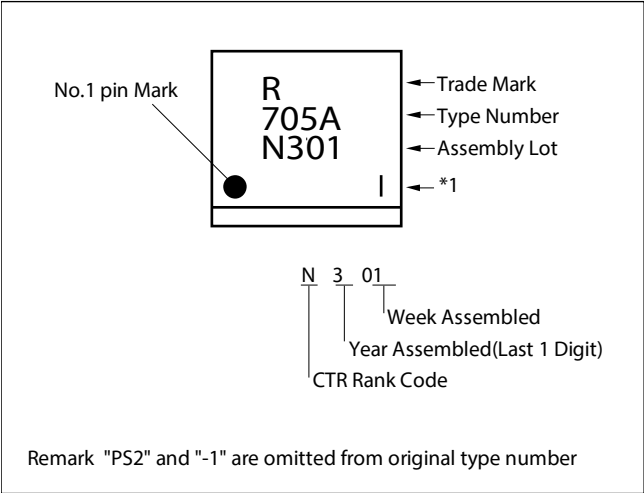
**PACKAGE DIMENSIONS (UNIT: mm)**

Weight : 0.08 g (typ.)

**PHOTOCOUPLER CONSTRUCTION**

Parameter	Unit (MIN.)
Air Distance	5 mm
Creepage Distance	5 mm
Isolation Distance	0.3 mm

MARKING EXAMPLE



Note: Bar indication contents of \*1.

<b>Made in Taiwan</b> ( *1: No indication )	<p>The diagram shows a square marking area with a black dot in the bottom-left corner. The text inside the square is: R, 705A, N301. There is no vertical bar on the right.</p>
<b>Made in Japan</b> ( *1: "   " (Vertical bar) )	<p>The diagram shows a square marking area with a black dot in the bottom-left corner. The text inside the square is: R, 705A, N301, and a vertical bar ( ) on the right. An arrow points from the label '"   " (Vertical bar) :Made in Japan' to the vertical bar.</p>

## ORDERING INFORMATION

Part Number	Order Number <sup>*1</sup>	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number <sup>*2</sup>
PS2705A-1	PS2705A-1-A	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products (UL, CSA, SEMKO, NEMKO, FIMKO, DEMKO approved)	PS2705A-1
PS2705A-1-F3	PS2705A-1-F3-A		Embossed Tape 3 500 pcs/reel		
PS2705A-1-V	PS2705A-1-V-A		20 pcs (Tape 20 pcs cut)	UL, CSA, SEMKO, NEMKO, FIMKO,	
PS2705A-1-V-F3	PS2705A-1-V-F3-A		Embossed Tape 3 500 pcs/reel	DIN EN 60747-5-5 approved	

Notes: \*1. When specifying CTR rank, please add "/CTR rank" after Order Number.

ex. L rank : PS2705A-1-A/L

Notes: \*2. For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ , unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current (DC)	$I_F$	$\pm 50$	mA
	Power Dissipation Derating	$\Delta P_D/^\circ\text{C}$	0.8	mW/ $^\circ\text{C}$
	Power Dissipation	$P_D$	80	mW
	Peak Forward Current <sup>*1</sup>	$I_{FP}$	$\pm 0.5$	A
Transistor	Collector to Emitter Voltage	$V_{CEO}$	70	V
	Emitter to Collector Voltage	$V_{ECO}$	5	V
	Collector Current	$I_C$	30	mA
	Power Dissipation Derating	$\Delta P_C/^\circ\text{C}$	1.5	mW/ $^\circ\text{C}$
	Power Dissipation	$P_C$	150	mW
Isolation Voltage <sup>*2</sup>		BV	3 750	Vr.m.s.
Operating Ambient Temperature		$T_A$	-55 to +100	$^\circ\text{C}$
Storage Temperature		$T_{stg}$	-55 to +150	$^\circ\text{C}$

Note: \*1. PW = 100  $\mu\text{s}$ , Duty Cycle = 1%

\*2. AC voltage for 1 minute at  $T_A = 25^\circ\text{C}$ , RH = 60% between input and output.

Pins 1-2 shorted together, 3-4 shorted together.

**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)**

	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = ±5 mA		1.2	1.4	V
	Terminal Capacitance	C <sub>t</sub>	V = 0 V, f = 1.0 MHz		20		pF
Transistor	Collector to Emitter Dark Current	I <sub>CEO</sub>	I <sub>F</sub> = 0 mA, V <sub>CE</sub> = 70 V			100	nA
Coupled	Current Transfer Ratio (I <sub>C</sub> /I <sub>F</sub> ) <sup>*1</sup>	CTR	I <sub>F</sub> = ±5 mA, V <sub>CE</sub> = 5 V	50		300	%
	Collector Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>F</sub> = ±10 mA, I <sub>C</sub> = 2 mA		0.13	0.3	V
	Isolation Resistance	R <sub>I-O</sub>	V <sub>I-O</sub> = 1.0 kV <sub>DC</sub>	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1.0 MHz		0.4		pF
	Rise Time <sup>*2</sup>	t <sub>r</sub>	V <sub>CC</sub> = 5 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 Ω		5		μs
	Fall Time <sup>*2</sup>	t <sub>f</sub>			7		
	Turn-on Time <sup>*2</sup>	t <sub>on</sub>			8		
	Turn-off Time <sup>*2</sup>	t <sub>off</sub>			10		

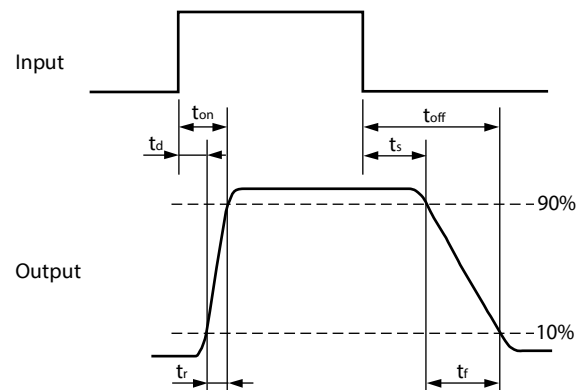
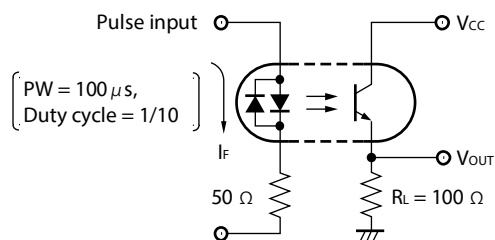
Note: \*1. CTR rank

N: 50 to 300 (%)

L: 100 to 300 (%)

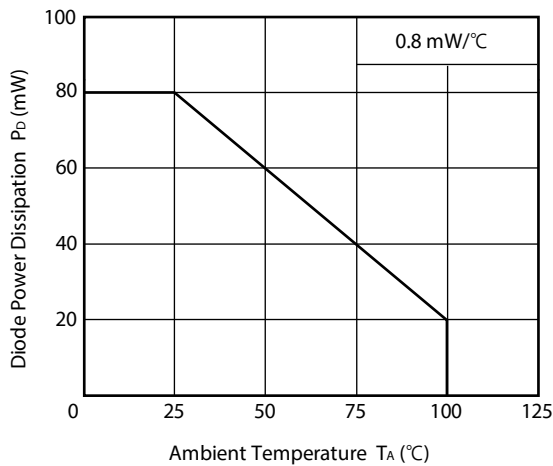
M: 50 to 150 (%)

\*2. Test Circuit for Switching Time

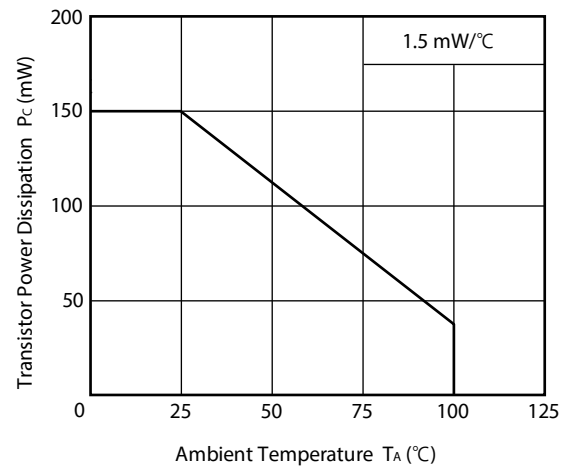


# TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ , unless otherwise specified)

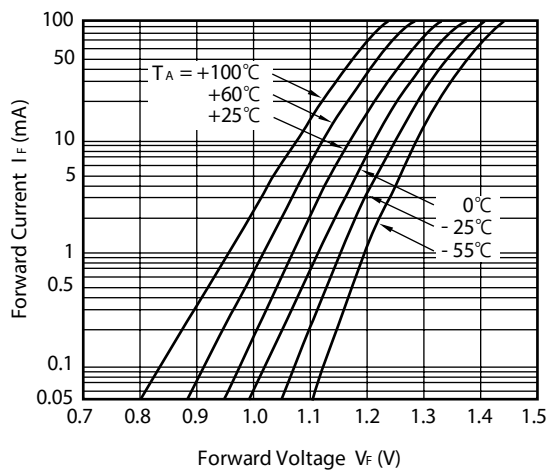
DIODE POWER DISSIPATION vs.  
AMBIENT TEMPERATURE



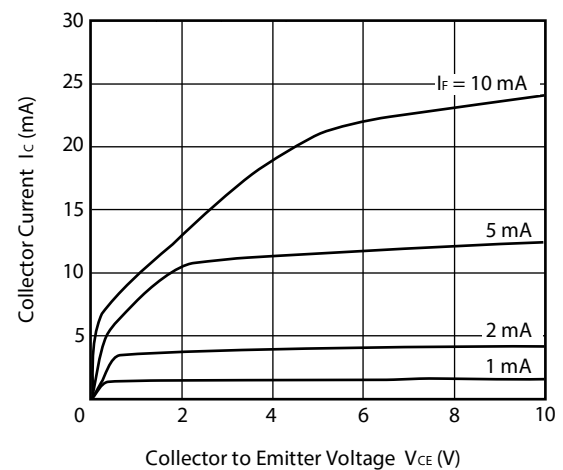
TRANSISTOR POWER DISSIPATION vs.  
AMBIENT TEMPERATURE



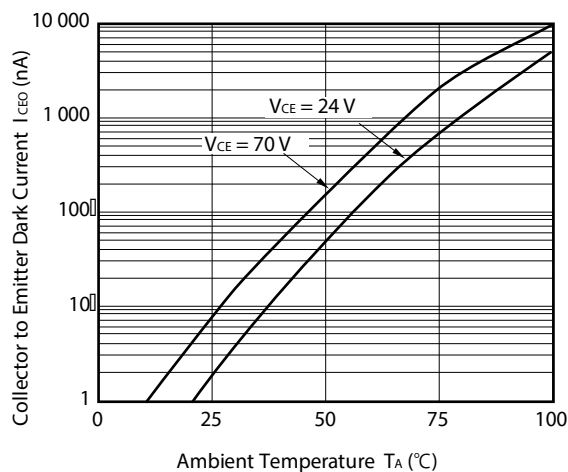
FORWARD CURRENT vs.  
FORWARD VOLTAGE



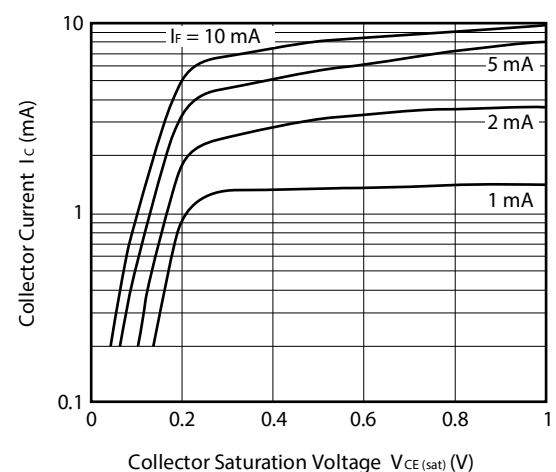
COLLECTOR CURRENT vs.  
COLLECTOR TO EMITTER VOLTAGE



COLLECTOR TO EMITTER DARK  
CURRENT vs. AMBIENT TEMPERATURE

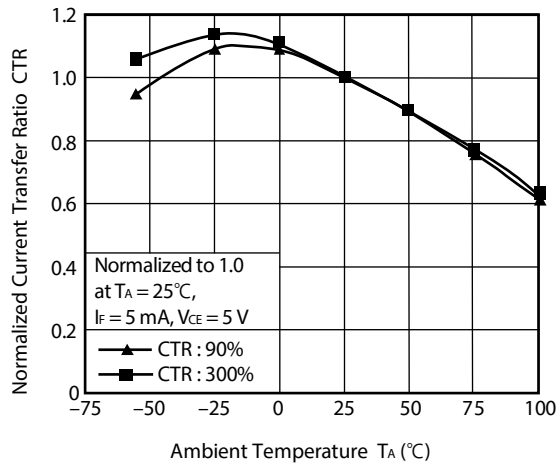


COLLECTOR CURRENT vs.  
COLLECTOR SATURATION VOLTAGE

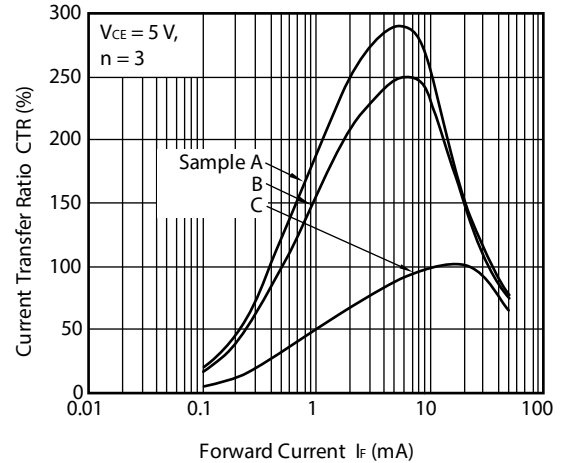


**Remark** The graphs indicate nominal characteristics.

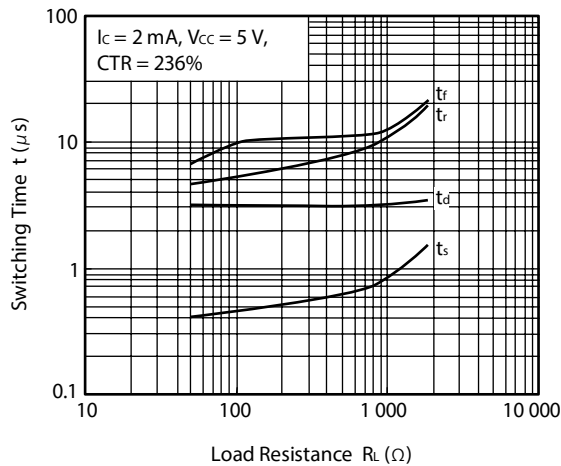
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



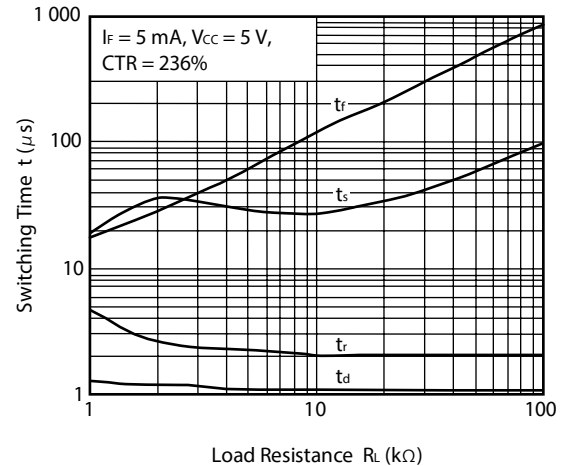
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



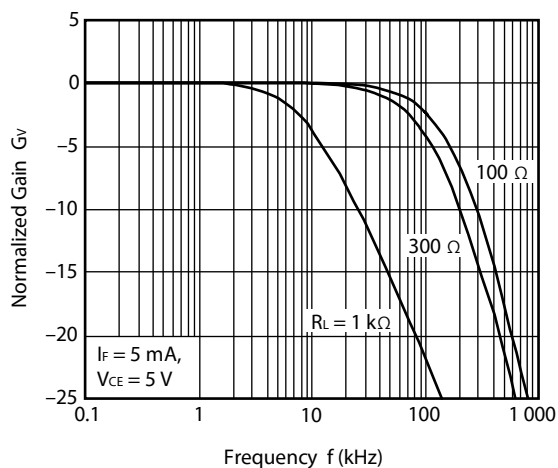
SWITCHING TIME vs. LOAD RESISTANCE



SWITCHING TIME vs. LOAD RESISTANCE



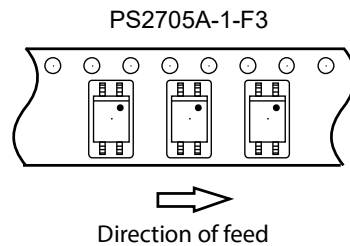
FREQUENCY RESPONSE



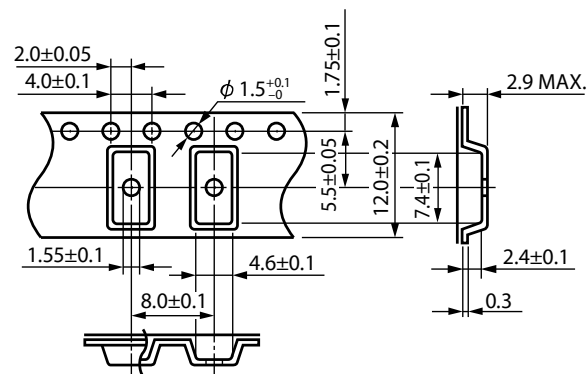
**Remark** The graphs indicate nominal characteristics.

## TAPING SPECIFICATIONS (UNIT: mm)

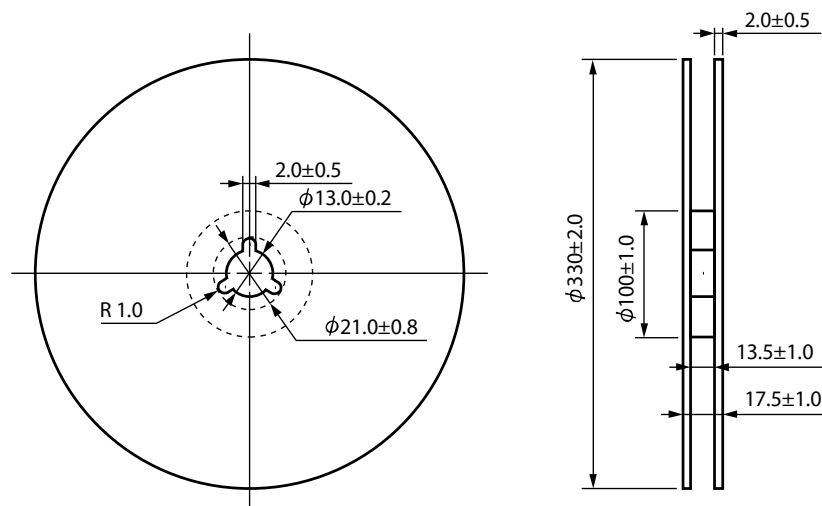
Tape Direction



Outline and Dimensions (Tape)

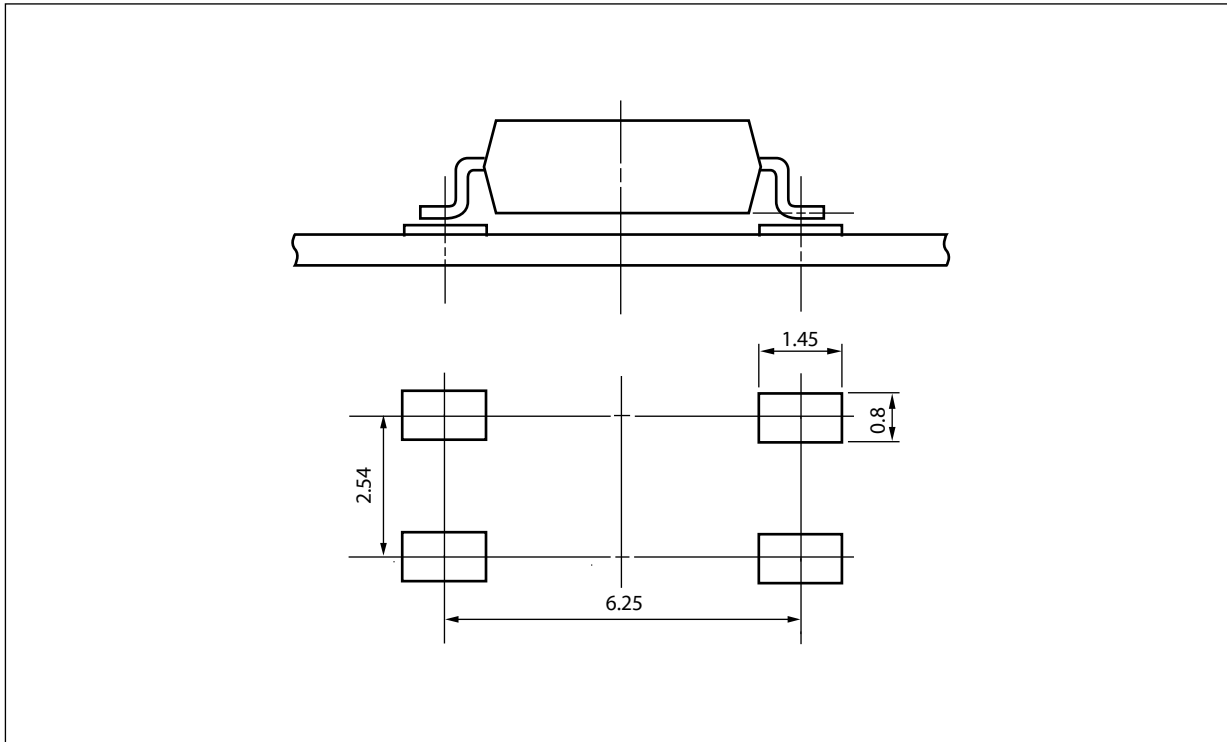


Outline and Dimensions (Reel)



Packing: 3 500 pcs/reel



**RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)**

**Remark** All dimensions in this figure must be evaluated before use.

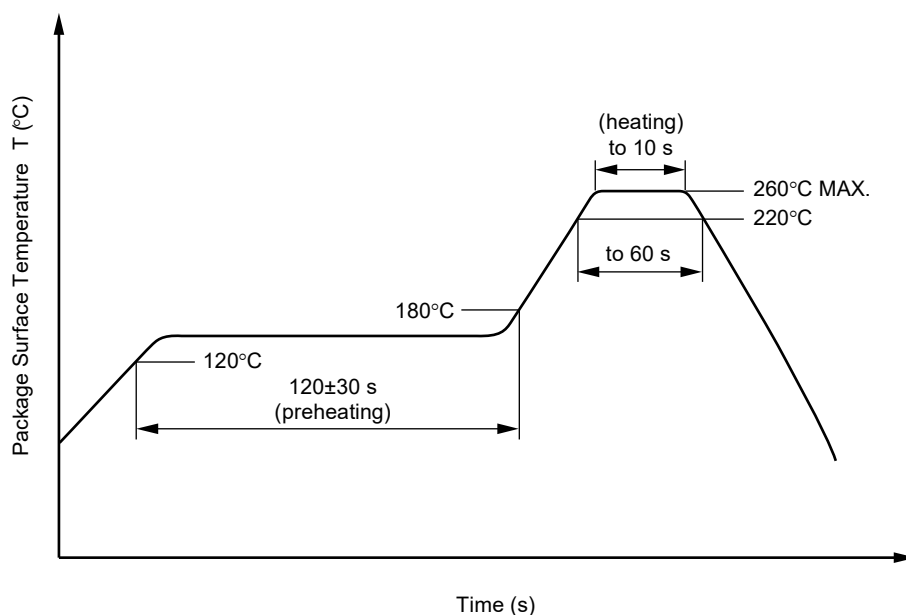
## NOTES ON HANDLING

### 1. Recommended soldering conditions

#### (1) Infrared reflow soldering

- |   |   |
|---|---|
| • Peak reflow temperature                       | 260°C or below (package surface temperature)  |
| • Time of peak reflow temperature               | 10 seconds or less  |
| • Time of temperature higher than 220°C         | 60 seconds or less  |
| • Time to preheat temperature from 120 to 180°C | 120±30 s  |
| • Number of reflows                             | Three   |
| • Flux  | Rosin flux containing small amount of chlorine<br>(The flux with a maximum chlorine content of 0.2 Wt% is recommended.) |

Recommended Temperature Profile of Infrared Reflow



#### (2) Wave soldering

- |                         |  |
|-------------------------|--|
| • Temperature           | 260°C or below (molten solder temperature)   |
| • Time                  | 10 seconds or less   |
| • Preheating conditions | 120°C or below (package surface temperature)   |
| • Number of times       | One (Allowed to be dipped in solder including plastic mold portion.)   |
| • Flux                  | Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.) |

#### (3) Soldering by Soldering Iron

- |  |   |
|--|---|
| • Peak Temperature (lead part temperature) | 350°C or below  |
| • Time (each pins)                         | 3 seconds or less   |
| • Flux                                     | Rosin flux containing small amount of chlorine<br>(The flux with a maximum chlorine content of 0.2 Wt% is recommended.) |

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C

#### (4) Cautions

- Flux Cleaning  
Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.
- Do not use fixing agents or coatings containing halogen-based substances.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. This tendency may sometimes be obvious, especially below  $I_F = 1 \text{ mA}$ .

Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

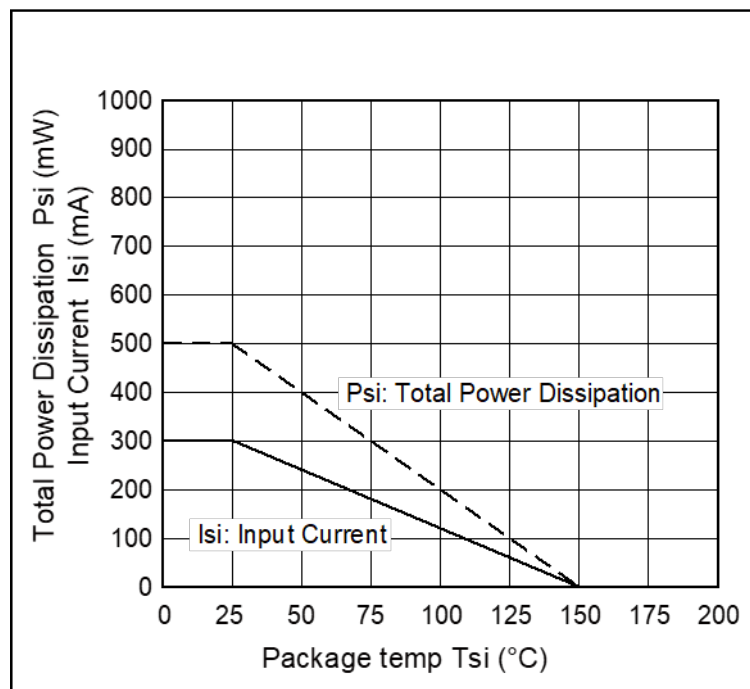
## USAGE CAUTIONS

1. Protect against static electricity when handling.
2. Avoid storage at a high temperature and high humidity.
3. Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.
4. Do not use fixing agents or coatings containing halogen-based substances.

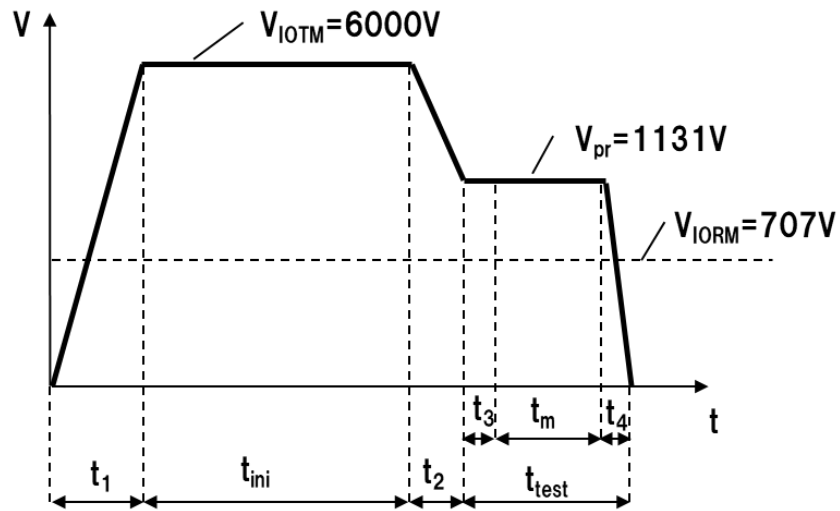
## SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Rating	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength			
maximum operating isolation voltage	$U_{IORM}$	707	$V_{peak}$
Test voltage (partial discharge test, procedure a for type test and random test)	$U_{pr}$	1 131	$V_{peak}$
$U_{pr} = 1.6 \times U_{IORM}$ , $P_d < 5 \text{ pC}$			
Test voltage (partial discharge test, procedure b for all devices)	$U_{pr}$	1 325	$V_{peak}$
$U_{pr} = 1.875 \times U_{IORM}$ , $P_d < 5 \text{ pC}$			
Highest permissible overvoltage	$U_{IOTM}$	6 000	$V_{peak}$
Degree of pollution (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303-11))	CTI	175	
Material group (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		III a	
Storage temperature range	$T_{stg}$	-55 to +150	°C
Operating temperature range	$T_A$	-55 to +100	°C
Isolation resistance, minimum value			
$V_{IO} = 500 \text{ V dc}$ at $T_A = 25^\circ\text{C}$	Ris MIN.	$10^{12}$	$\Omega$
$V_{IO} = 500 \text{ V dc}$ at $T_A \text{ MAX.}$ at least $100^\circ\text{C}$	Ris MIN.	$10^{11}$	$\Omega$
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve)			
Package temperature	$T_{si}$	150	°C
Current (input current $I_F$ , $\Psi_i = 0$ )	$I_{si}$	300	mA
Power (output or total power dissipation)	$\Psi_i$	500	mW
Isolation resistance			
$V_{IO} = 500 \text{ V dc}$ at $T_A = T_{si}$	Ris MIN.	$10^9$	$\Omega$

## Dependence of maximum safety ratings with package temperature



### Method a) Destructive Test, Type and Sample Test



$$t_1, t_2 = 1 \text{ to } 10 \text{ sec}$$

$$t_3, t_4 = 1 \text{ sec}$$

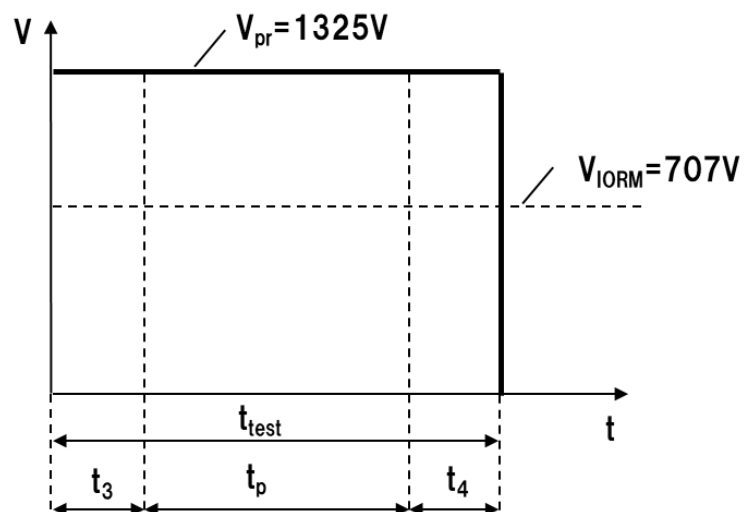
$$t_m \text{ (PARTIAL DISCHARGE)} = 10 \text{ sec}$$

$$t_{test} = 12 \text{ sec}$$

$$t_{ini} = 60 \text{ sec}$$

### Method b) Non-destructive Test, 100% Production Test

&lt;R&gt;



$$t_3, t_4 = 0.1 \text{ sec}$$

$$t_p \text{ (PARTIAL DISCHARGE)} = 1.0 \text{ sec}$$

$$t_{test} = 1.2 \text{ sec}$$

**Caution**

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
  2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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(Rev.4.0-1 November 2017)



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