

RBN25H125S1FPQ-A0

1250V - 25V - IGBT Power Switching

R07DS1378EJ0140 Rev.1.40 Aug.03.2020

Features

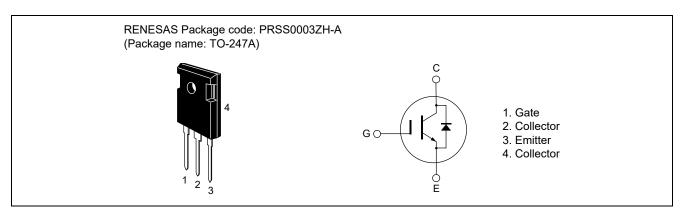
- Trench gate and thin wafer technology (G8H series)
- Built in fast recovery diode in one package
- Low collector to emitter saturation voltage
 V_{CE(sat)} = 1.8 V typ. (at I_C = 25 A, V_{GE} = 15 V, Ta = 25°C)
- Quality grade: Standard

- · High speed switching
- Short circuit withstands time (10 µs min.)
- Applications: UPS, Welding, photovoltaic inverters, Power converter system

Key Performance

Туре	V _{CES}	lc	V _{CE(sat)} , T _C =25°C	l _F	tsc	Tj
RBN25H125S1FPQ-A0	1250 V	25 A	1.8 V	15 A	10 μs	175 °C

Outline



Absolute Maximum Ratings

 $(Tc = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Collector to emitter voltage		Vces	1250	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	Tc = 25 °C	Ic	50	Α
	Tc = 100 °C	Ic	25	Α
Collector peak current		I _{C(peak)} Notes1	100	Α
Diode forward current	Tc = 25 °C	lF	30	Α
	Tc = 100 °C	lF	15	Α
Diode forward peak current		IF(peak) Notes1	100	Α
Collector power dissipation		Pc Notes2	223	W
Junction temperature		T _j Notes2	175	°C
Storage temperature		T _{stg}	-55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Please use this device in the thermal conditions which the junction temperature does not exceed 175 °C. Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175 °C.

Thermal Resistance Characteristics

 $(Tc = 25^{\circ}C)$

Item	Symbol	Max. Value Notes3	Unit
Junction to case thermal resistance (IGBT)	R _{th(j-c)}	0.67	°C/W
Junction to case thermal resistance (Diode)	R _{th(j-c)}	2.10	°C/W

Notes: 3. Designed target value on Renesas measurement condition. (Not tested)

Electrical Characteristics

 $(Tc = 25^{\circ}C)$

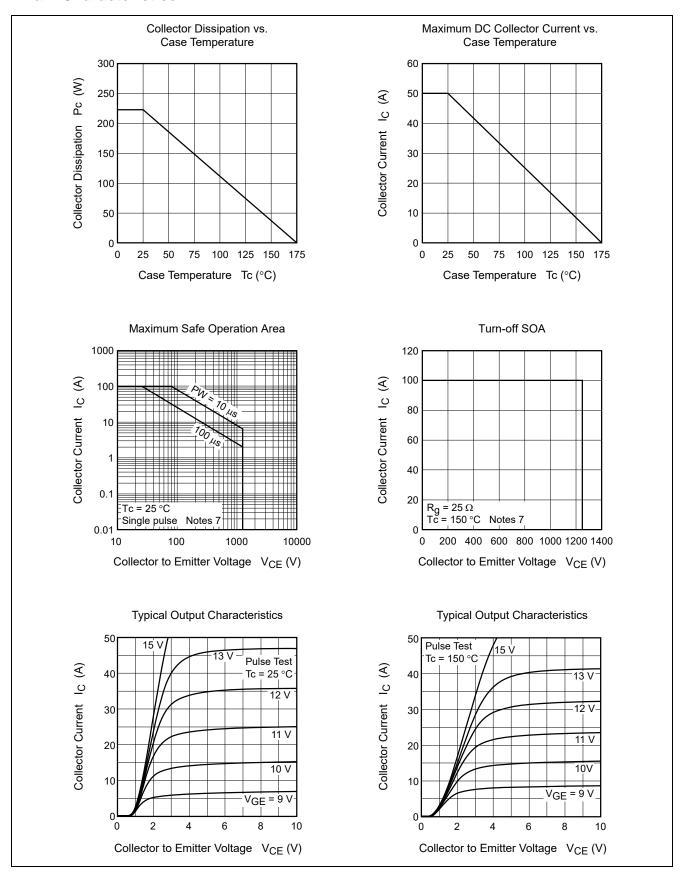
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Collector to emitter leakage current	Ices	_	_	200	μΑ	V _{CE} = 1250 V, V _{GE} = 0 V	
Gate to emitter leakage current	I _{GES}	_	_	±1	μΑ	V _{GE} = ±30 V, V _{CE} = 0 V	
Gate to emitter threshold voltage	V _{GE(th)}	5.3	_	7.1	V	V _{CE} = 10 V, I _C = 0.83 mA	
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.8	2.34	V	Ic = 25 A, V _{GE} = 15 V Notes4	
Input capacitance	Cies	_	1540	_	pF	V _{CE} = 25 V	
Output capacitance	Coes	_	78	_	pF	V _{GE} = 0 V	
Reverse transfer capacitance	Cres	_	12	_	pF	f = 1 MHz	
Total gate charge	Qg	_	56	_	nC	V _{GE} = 15 V	
Gate to emitter charge	Qge	_	16	_	nC	V _{CE} = 600 V	
Gate to collector charge	Qgc	_	29	_	nC	Ic = 25 A	
Turn-on delay time	t _{d(on)}	_	19	_	Ns	V _{CC} = 600 V	
Rise time	t _r	_	8	_	ns	V _{GE} = 15 V/–15 V	
Turn-off delay time	$t_{d(off)}$	_	109	_	ns	Ic = 25 A	
Fall time	t _f	_	58	_	ns	$R_g = 10 \Omega$	
Turn-on loss energy	Eon	_	1.1	_	mJ	Tc = 25 °C	
Turn-off loss energy	E _{off}	_	0.8	_	mJ	Inductive load Notes5	
Total switching energy	E _{total}	_	1.9	_	mJ		
Turn-on delay time	t _{d(on)}	_	20	_	ns	V _{CC} = 600 V	
Rise time	t _r	_	9	_	ns	V _{GE} = 15 V/–15 V	
Turn-off delay time	$t_{d(off)}$	_	132	_	ns	Ic = 25 A	
Fall time	t _f	_	70	_	ns	$R_g = 10 \Omega$	
Turn-on loss energy	Eon	_	1.8	_	mJ	Tc = 150 °C	
Turn-off loss energy	E _{off}	_	1.4	_	mJ	Inductive load Notes5	
Total switching energy	E _{total}	_	3.2	_	mJ		
Short circuit withstand time Notes6	Tsc	10	_	_	μs	$V_{CC} \le 720 \text{ V}, V_{GE} = 15 \text{ V}$ $Tc \le 150 \text{ °C}$	
	<u> </u>			<u> </u>		10=100 0	
Diode forward voltage	VF	_	2.9	3.77	V	I _F = 15 A ^{Notes4}	
Diode reverse recovery time	t _{rr}	_	102	_	ns	$I_F = 15 \text{ A}, d_{iF}/d_t = 300 \text{ A}/\mu\text{s}$	
Diada assessed as a second	_		0.50		_	1	

Diode forward voltage	VF	_	2.9	3.77	V	I _F = 15 A ^{Notes4}
Diode reverse recovery time	t _{rr}	_	102	_	ns	$I_F = 15 \text{ A}, d_{iF}/d_t = 300 \text{ A}/\mu\text{s}$
Diode reverse recovery charge	Qrr	_	0.53	_	μC	
Diode peak reverse recovery current	I _{rr}	_	9	_	Α	

Notes: 4. Pulse test

- 5. Switching time test circuit and waveform are shown below.
- 6. Designed target value on Renesas measurement condition. (Not tested)

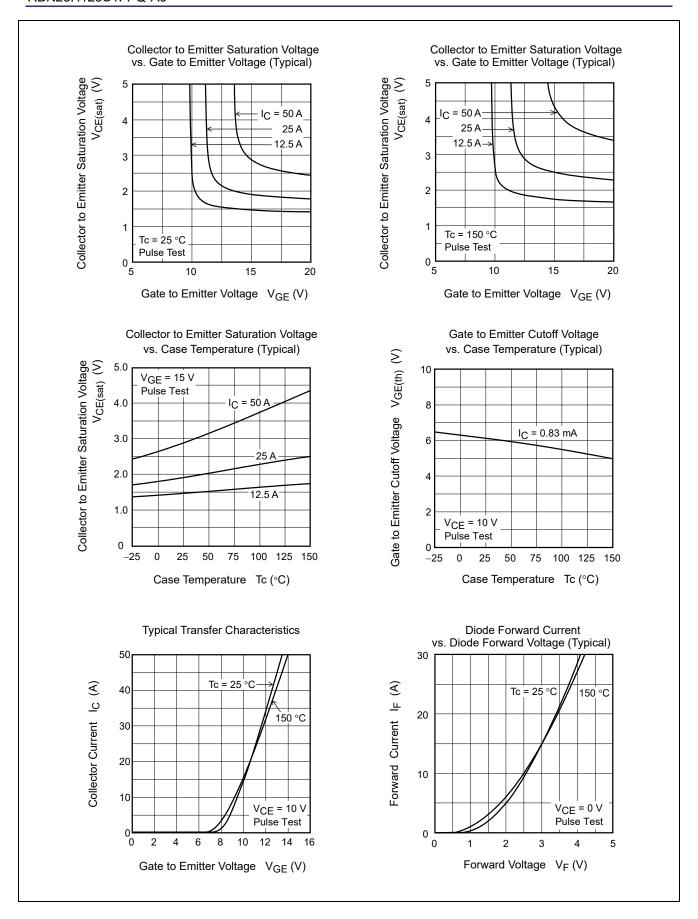
Main Characteristics

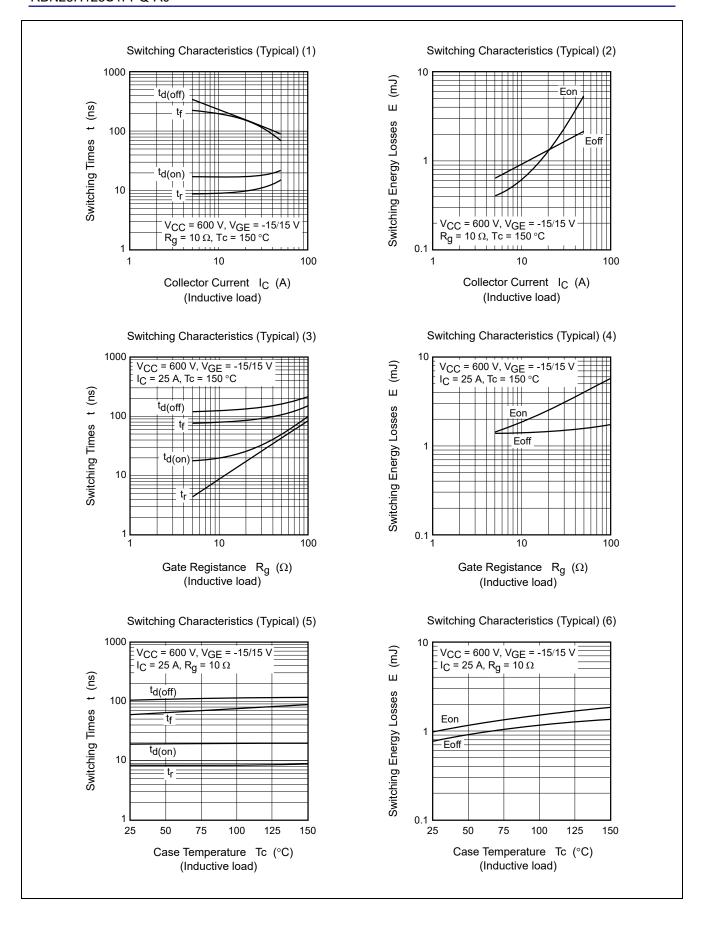


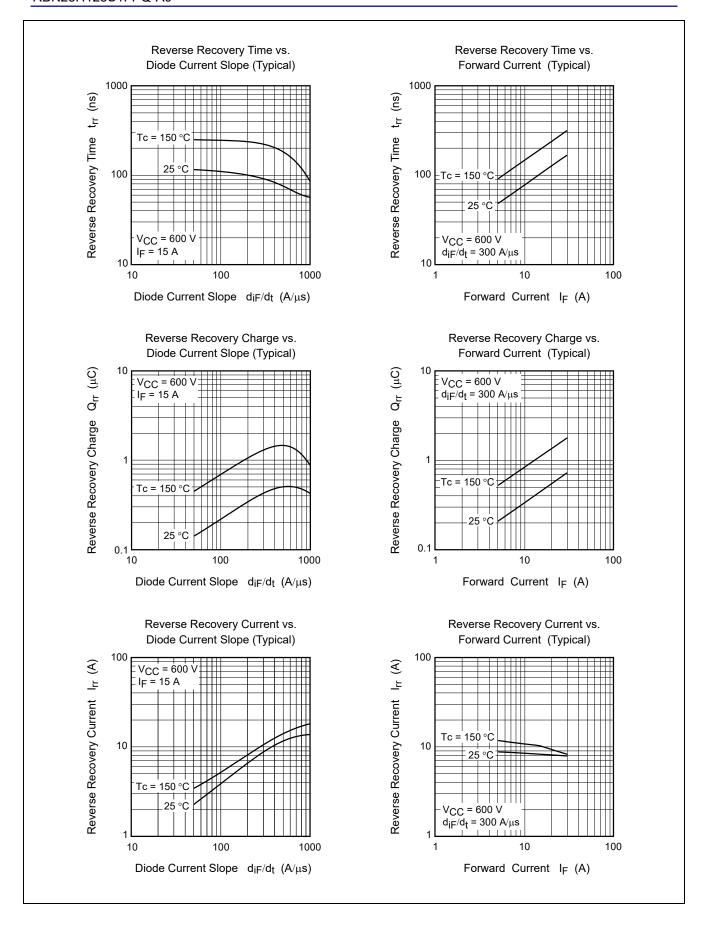
Notes: 7. Designed target value on Renesas measurement condition. (Not tested)

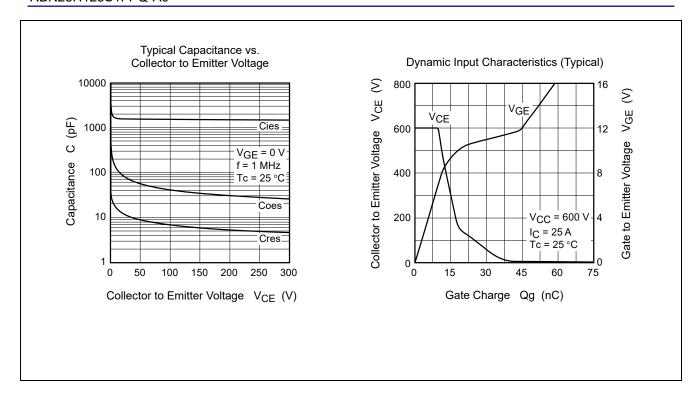
Renesas recommends that operating conditions are designed according to a document "Power MOS FET •

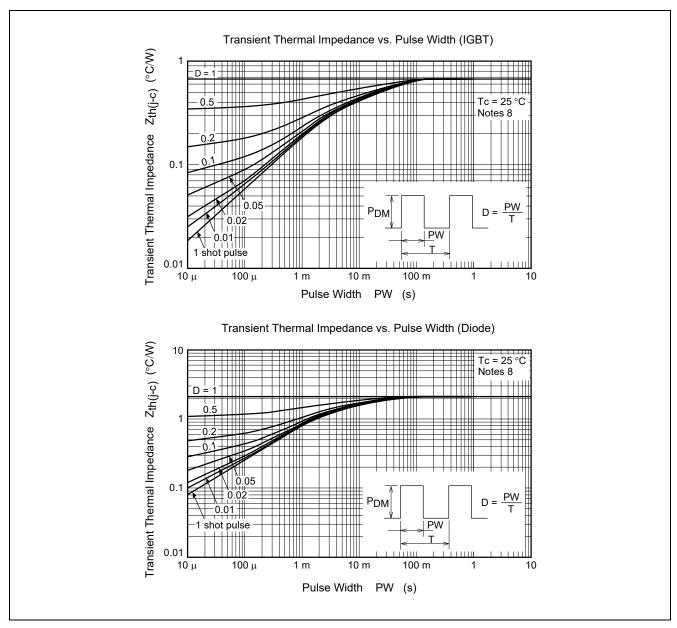
IGBT Attention of Handling Semiconductor Devices".



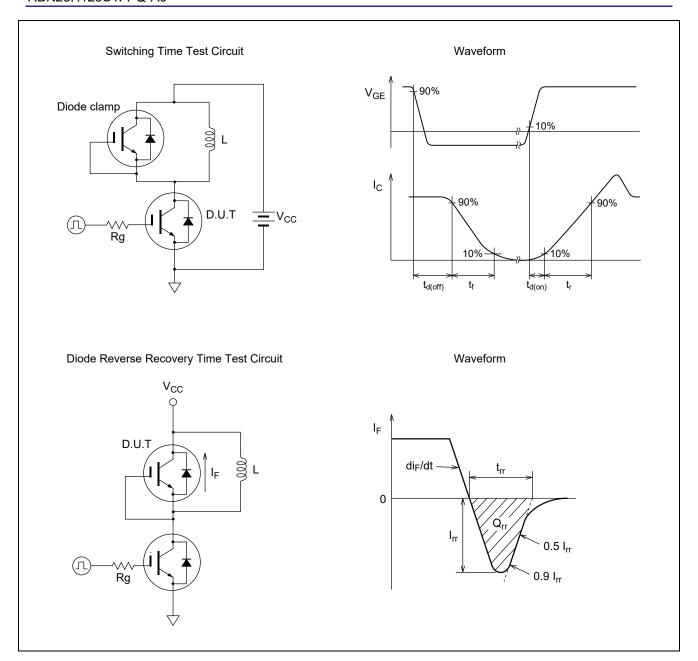




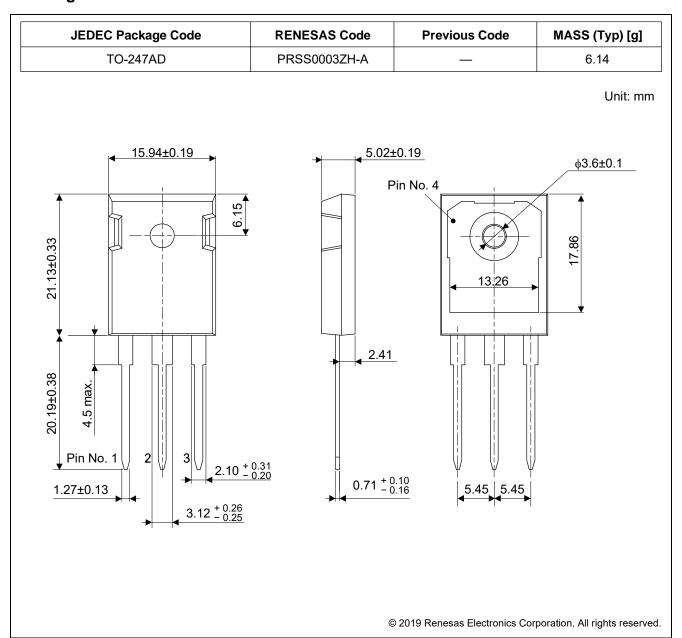




Notes: 8. Designed target value on Renesas measurement condition. (Not tested)



Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container		
RBN25H125S1FPQ-A0#CB0	240 pcs	Box (Tube)		

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