

RJP65T43DPQ-A0

650V - 30A - IGBT

Application: Power Factor Correction circuit

R07DS1376EJ0101

Rev. 1.01

Jul 11, 2017

Features

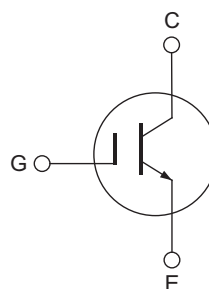
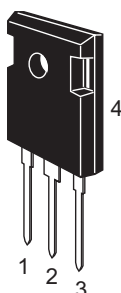
- Low collector to emitter saturation voltage
 $V_{CE(sat)} = 1.8 \text{ V typ. (at } I_C = 20 \text{ A, } V_{GE} = 15 \text{ V, } T_a = 25^\circ\text{C)}$
- Trench gate and thin wafer technology (G7H series)
- High speed switching
 $t_f = 45 \text{ ns typ. (at } V_{CC} = 400\text{V, } V_{GE} = 15\text{V, } I_C = 20\text{A, } R_g = 10\Omega, T_a = 25^\circ\text{C, Inductive load)}$
- Operation frequency ($20\text{kHz} \leq f < 100\text{kHz}$)
Rating of collector current $I_C = 30\text{A}$ (at $T_c = 100^\circ\text{C}$)
- Not guarantee short circuit withstand time

Key Nominal Performance

Type	V_{CES}	I_C	$V_{CE(sat)}, T_a = 25^\circ\text{C}$	T_j	Marking	Package
RJP65T43DPQ-A0	650V	20A	1.8V	175°C	RJP65T43	TO-247A

Outline

RENESAS Package code: PRSS0003ZH-A
(Package name: TO-247A)



1. Gate
2. Collector
3. Emitter
4. Collector

Absolute Maximum Ratings

($T_c = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Collector to emitter voltage	V_{CES}	650	V
Gate to emitter voltage	V_{GES}	± 30	V
Collector current	$T_c = 25^\circ\text{C}$	I_C	60
	$T_c = 100^\circ\text{C}$	I_C	30
Collector peak current	$i_{c(peak)}$ ^{Note1}	150	A
Collector dissipation	P_C	150	W
Junction to case thermal resistance	θ_{j-c}	1.0	$^\circ\text{C/W}$
Junction temperature	T_j ^{Note2}	175	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it are within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Electrical Characteristics

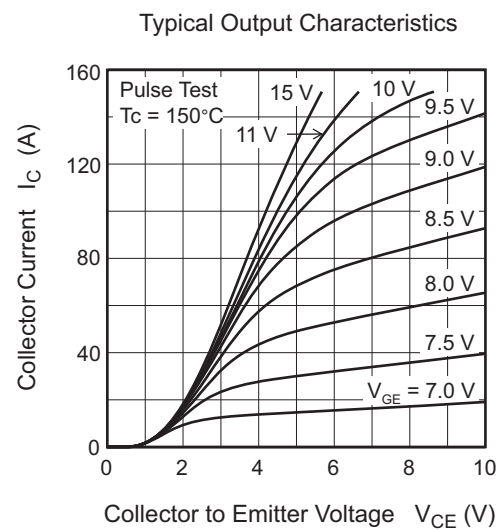
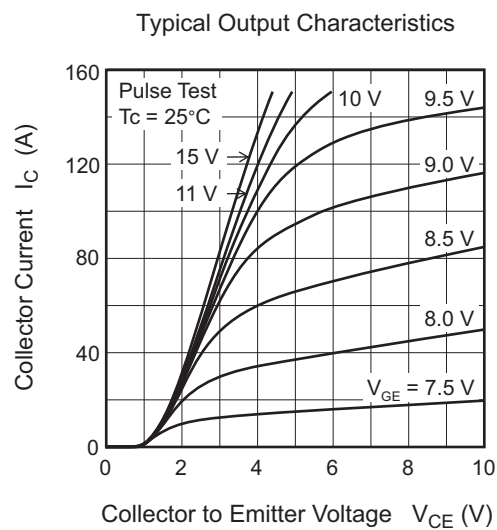
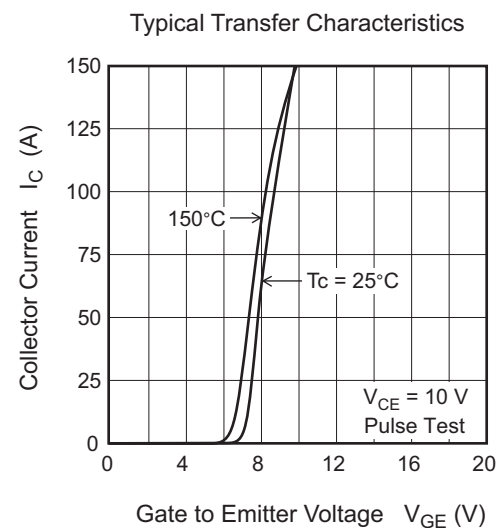
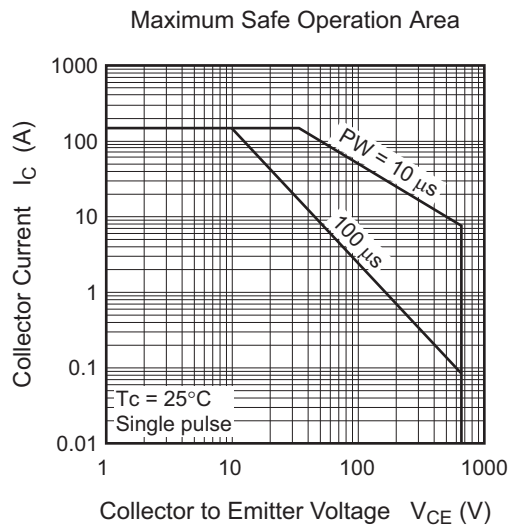
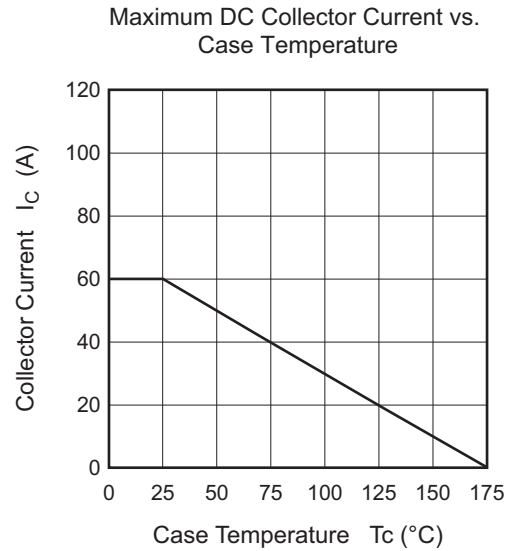
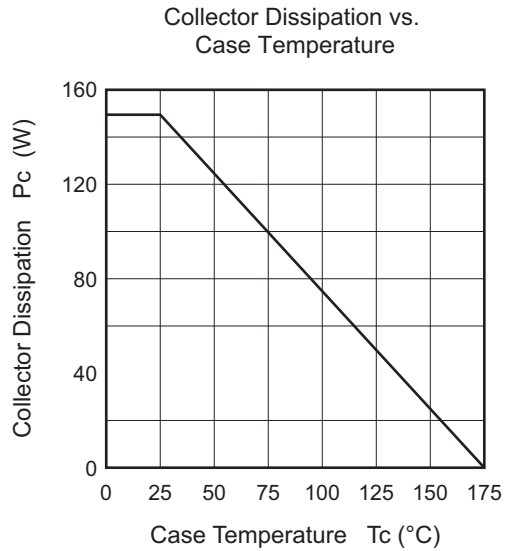
(Ta = 25°C)

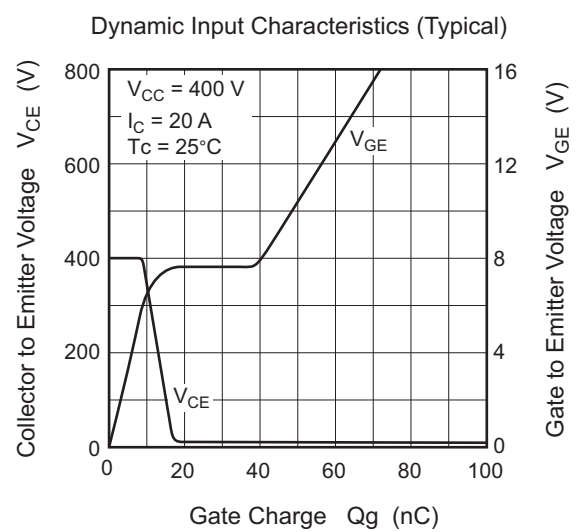
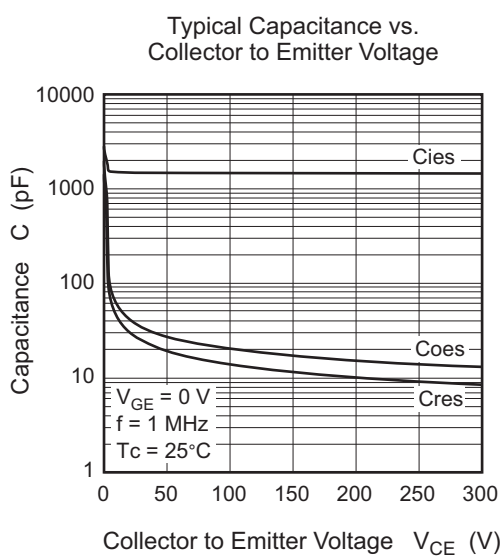
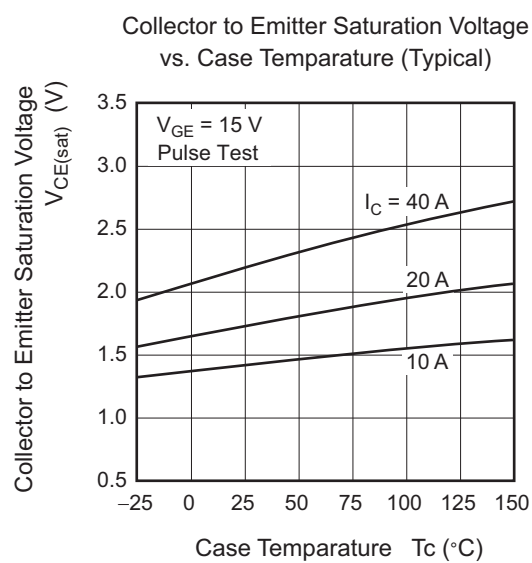
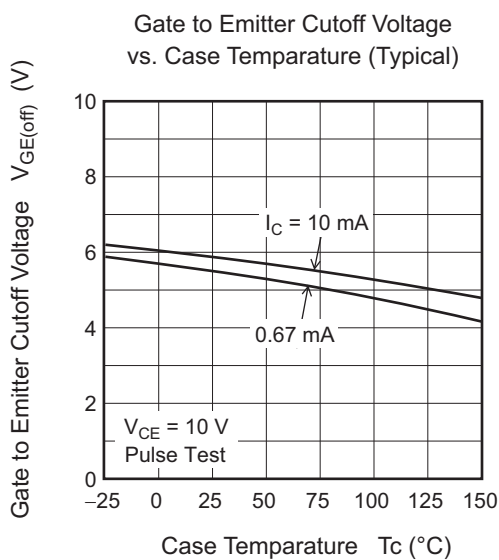
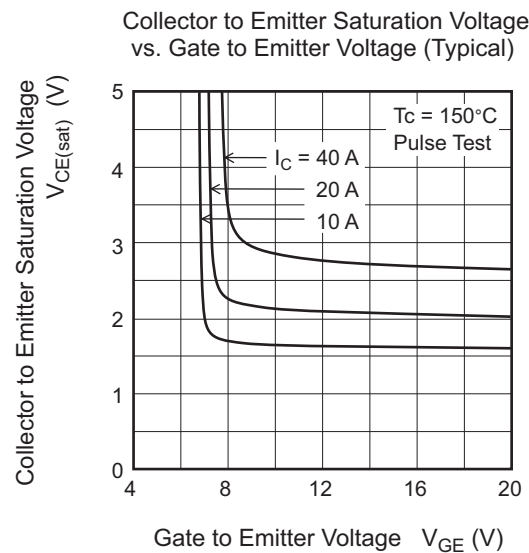
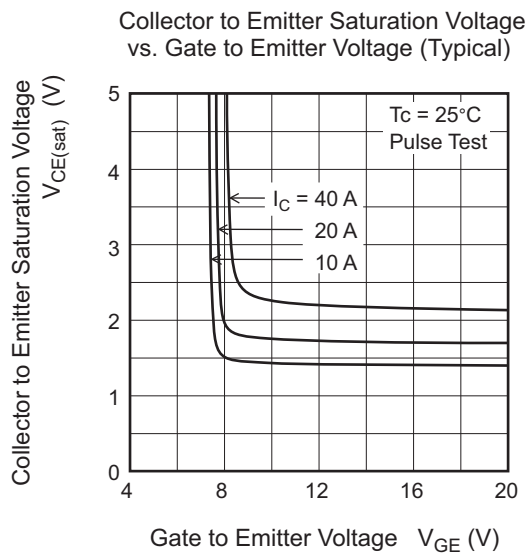
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero gate voltage collector current	I_{CES}	—	—	1	μA	$V_{CE} = 650 V, V_{GE} = 0$
Gate to emitter leak current	I_{GES}	—	—	± 1	μA	$V_{GE} = \pm 30 V, V_{CE} = 0$
Gate to emitter cutoff voltage	$V_{GE(off)}$	4.0	—	7.0	V	$V_{CE} = 10V, I_C = 0.67 mA$
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	1.8	2.4	V	$I_C = 20 A, V_{GE} = 15V$ ^{Note3}
Total gate charge	Q_g	—	69	—	nC	$V_{CE} = 400 V$ $V_{GE} = 15V$ $I_C = 20A$
Gate to emitter charge	Q_{ge}	—	10	—	nC	
Gate to collector charge	Q_{gc}	—	30	—	nC	
Input capacitance	C_{ies}	—	1550	—	pF	$V_{CE} = 25 V$ $V_{GE} = 0$ $f = 1 MHz$
Output capacitance	C_{oes}	—	37	—	pF	
Reveres transfer capacitance	C_{res}	—	26	—	pF	
Turn-on delay time	$t_{d(on)}$	—	35	—	ns	$V_{CC} = 400 V$ $V_{GE} = 15 V, I_C = 20 A$ $R_g = 10 \Omega, T_C = 25 ^\circ C$ Inductive load ^{Note4}
Rise time	t_r	—	20	—	ns	
Turn-off delay time	$t_{d(off)}$	—	105	—	ns	
Fall time	t_f	—	45	—	ns	
Turn-on loss energy	E_{on}	—	0.17	—	mJ	
Turn-off loss energy	E_{off}	—	0.13	—	mJ	$V_{CC} = 400 V$ $V_{GE} = 15 V, I_C = 20 A$ $R_g = 10 \Omega, T_C = 150 ^\circ C$ Inductive load ^{Note4}
Turn-on delay time	$t_{d(on)}$	—	32	—	ns	
Rise time	t_r	—	20	—	ns	
Turn-off delay time	$t_{d(off)}$	—	115	—	ns	
Fall time	t_f	—	45	—	ns	
Turn-on loss energy	E_{on}	—	0.28	—	mJ	
Turn-off loss energy	E_{off}	—	0.53	—	mJ	

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$

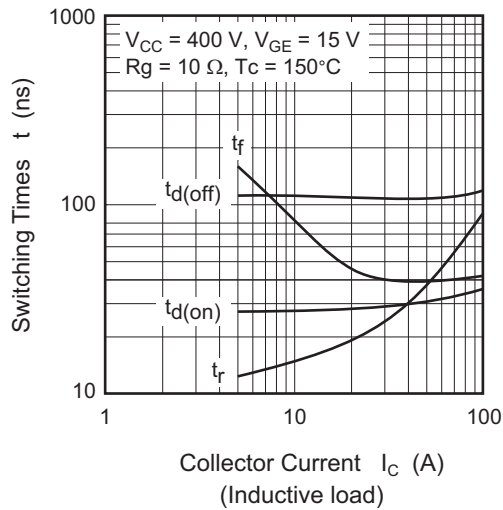
2. Please use this device in the thermal conditions which the junction temperature does not exceed 175°C.
Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175°C.
3. Pulse test
4. Switching time test circuit and waveform are shown below.

Main Characteristics

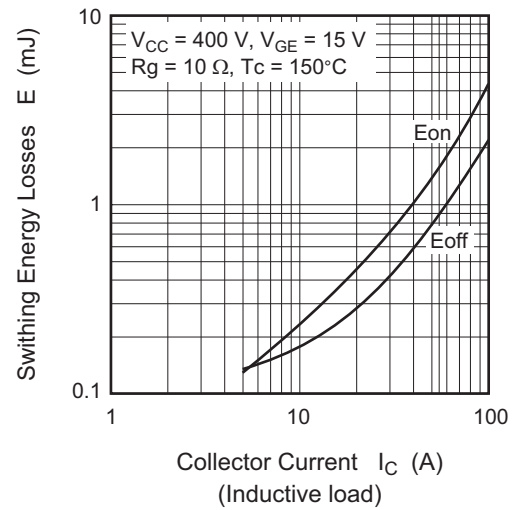




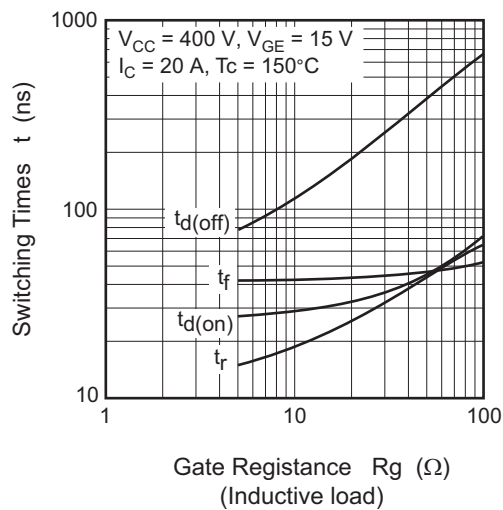
Switching Characteristics (Typical) (1)



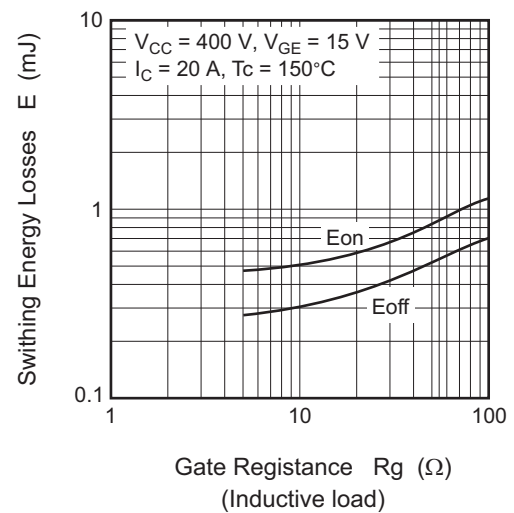
Switching Characteristics (Typical) (2)



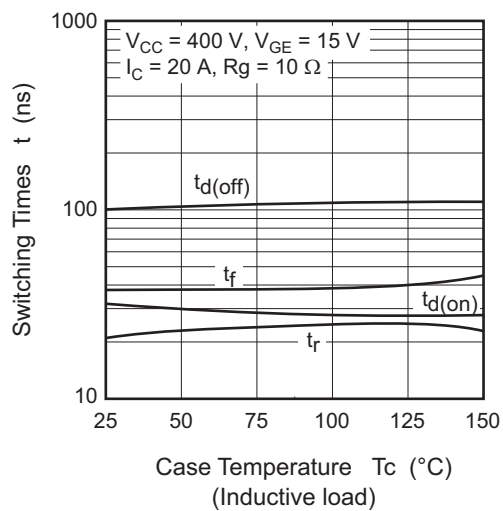
Switching Characteristics (Typical) (3)



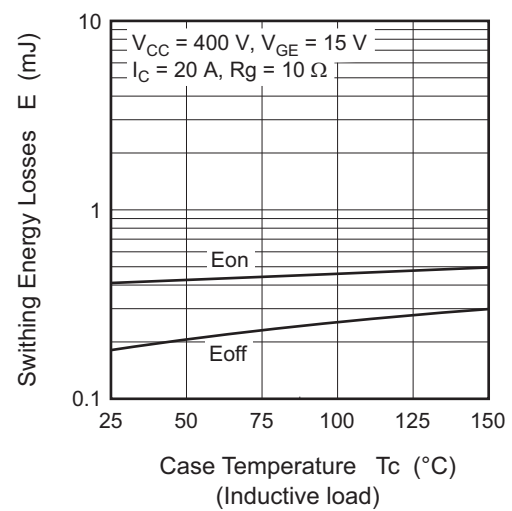
Switching Characteristics (Typical) (4)

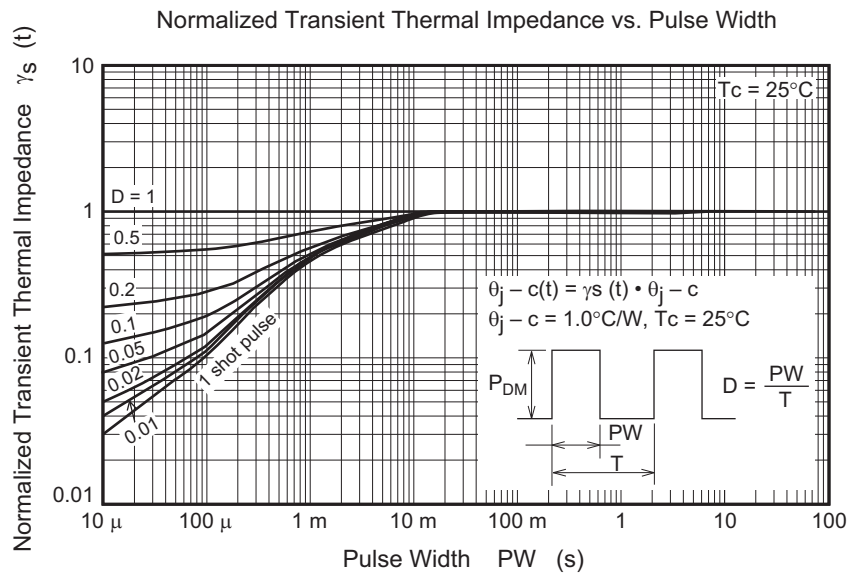


Switching Characteristics (Typical) (5)

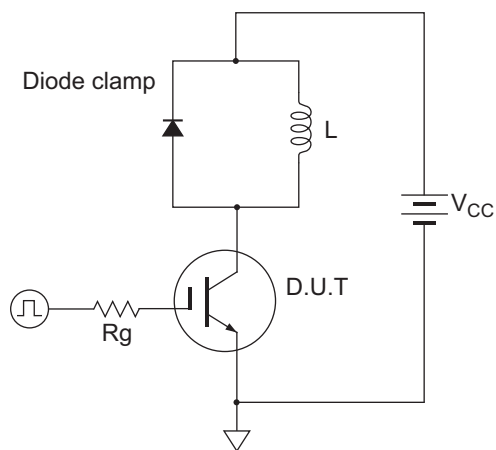


Switching Characteristics (Typical) (6)

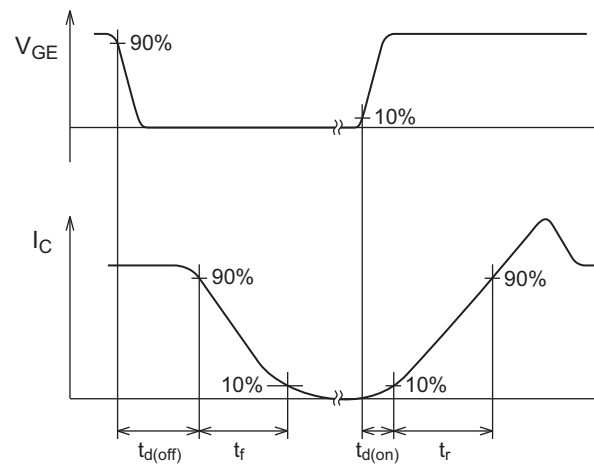




Switching Time Test Circuit



Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]	Unit: mm
TO-247A	-	PRSS0003ZH-A	-	6.14g	

Technical drawing showing the package dimensions for the RJP65T43DPQ-A0 package (TO-247A).

Dimensions (mm):

- Top View: 15.94 ± 0.19, 21.13 ± 0.33, 6.15, 2.10^{+0.1}/_{-0.2}, 1.27 ± 0.13, 5.45, 5.45, 20.19 ± 0.38, 4.5 max.
- Side View: 5.02 ± 0.19, 0.71 ± 0.1, 2.41.
- Front View: 13.26, 17.63, φ3.60 ± 0.1.

Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJP65T43DPQ-A0#T2	240 pcs	Box (Tube)

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