

RJH60M7DPQ-E0

600V - 50A - IGBT

Application: Inverter

R07DS1089EJ0200

Rev.2.00

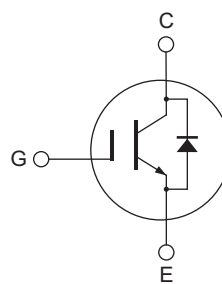
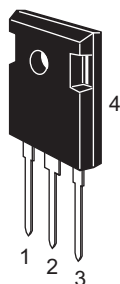
Jun 25, 2013

Features

- Short circuit withstand time (8 μ s typ.)
- Low collector to emitter saturation voltage
 $V_{CE(sat)} = 1.6$ V typ. (at $I_C = 50$ A, $V_{GE} = 15$ V, $T_a = 25^\circ\text{C}$)
- Built in fast recovery diode (100 ns typ.) in one package
- Trench gate and thin wafer technology
- High speed switching
 $t_f = 45$ ns typ. (at $V_{CC} = 300$ V, $V_{GE} = 15$ V, $I_C = 50$ A, $R_g = 5$ Ω , $T_a = 25^\circ\text{C}$, inductive load)

Outline

RENESAS Package code: PRSS0003ZE-A
(Package name: TO-247)



1. Gate
2. Collector
3. Emitter
4. Collector

Absolute Maximum Ratings

($T_a = 25^\circ\text{C}$)

Item		Symbol	Ratings	Unit
Collector to emitter voltage / diode reverse voltage		V_{CES} / V_R	600	V
Gate to emitter voltage		V_{GES}	± 30	V
Collector current	$T_c = 25^\circ\text{C}$	I_C	90	A
	$T_c = 100^\circ\text{C}$	I_C	50	A
Collector peak current		$I_{C(peak)}$ ^{Note1}	150	A
Collector to emitter diode forward current		I_{DF}	50	A
Collector to emitter diode forward peak current		$I_{DF(peak)}$ ^{Note1}	200	A
Collector dissipation		P_C ^{Note2}	367	W
Junction to case thermal resistance (IGBT)		θ_{j-c} ^{Note2}	0.34	$^\circ\text{C/W}$
Junction to case thermal resistance (Diode)		θ_{j-cd} ^{Note2}	1.07	$^\circ\text{C/W}$
Junction temperature		T_j	150	$^\circ\text{C}$
Storage temperature		T_{stg}	-55 to +150	$^\circ\text{C}$

Notes: 1. $PW \leq 10$ μ s, duty cycle $\leq 1\%$

2. Value at $T_c = 25^\circ\text{C}$

Electrical Characteristics

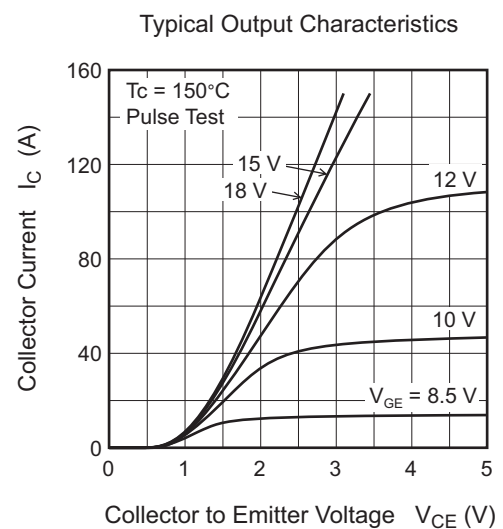
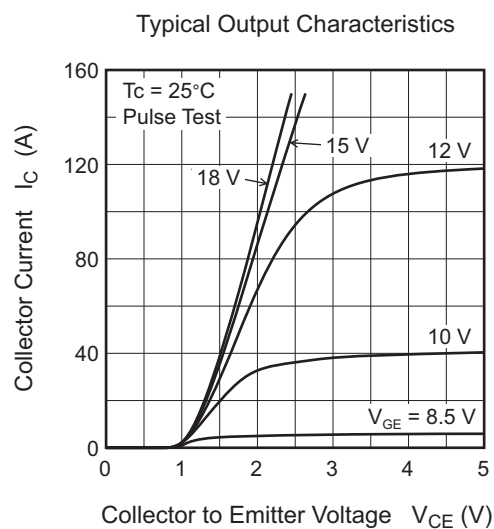
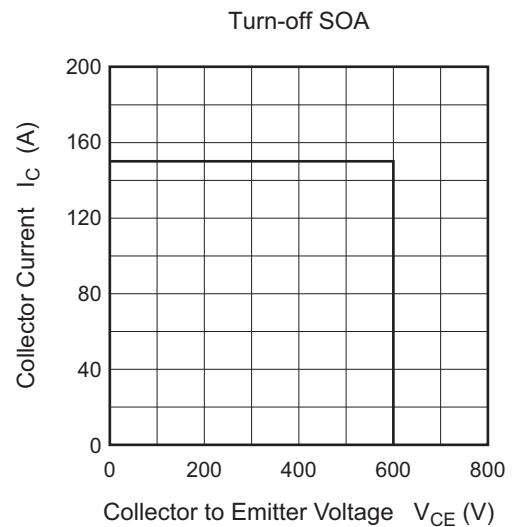
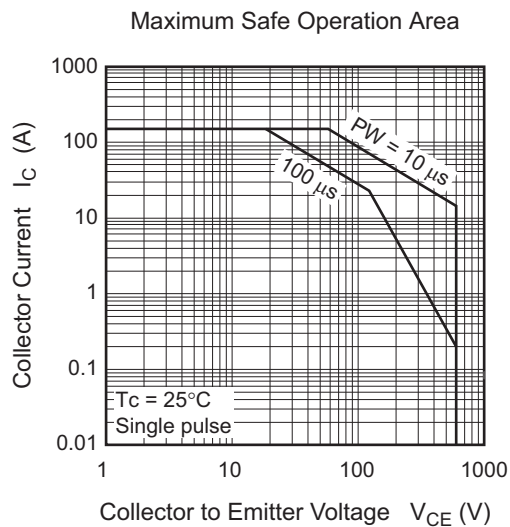
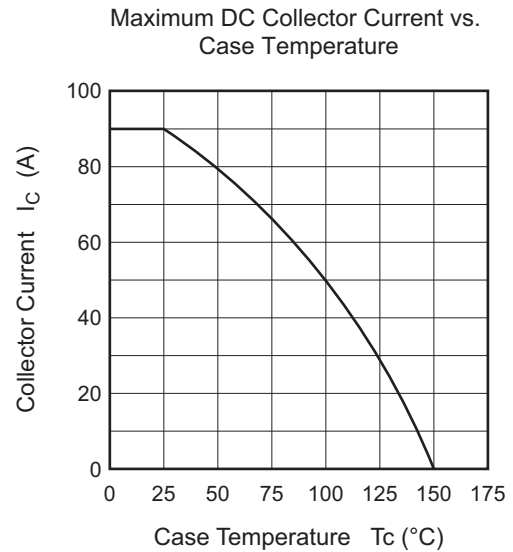
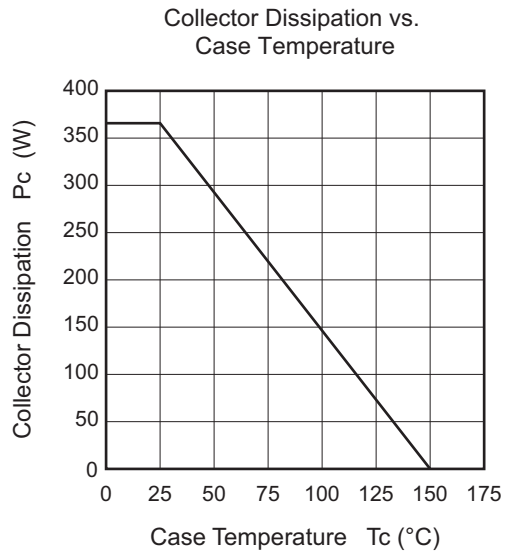
(Ta = 25°C)

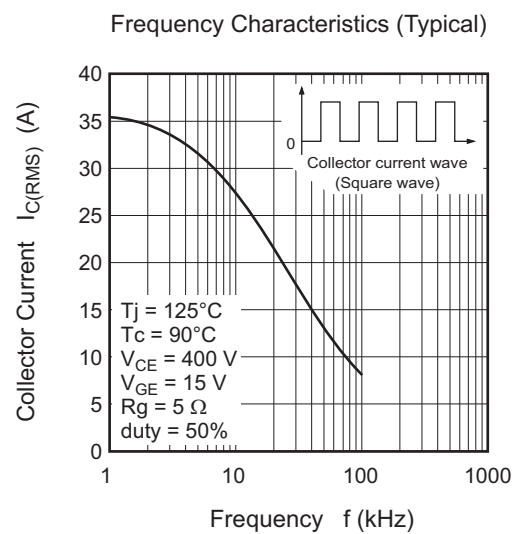
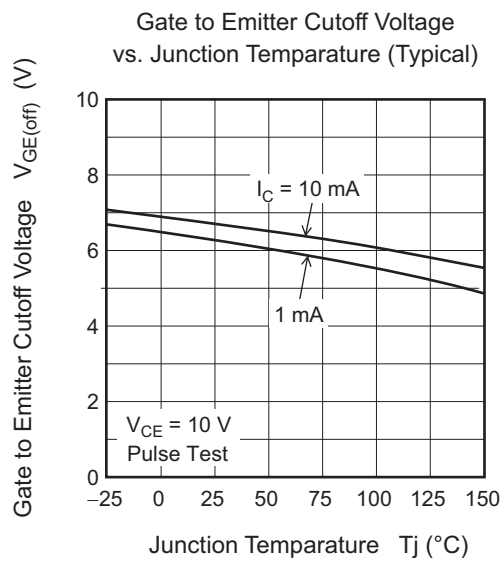
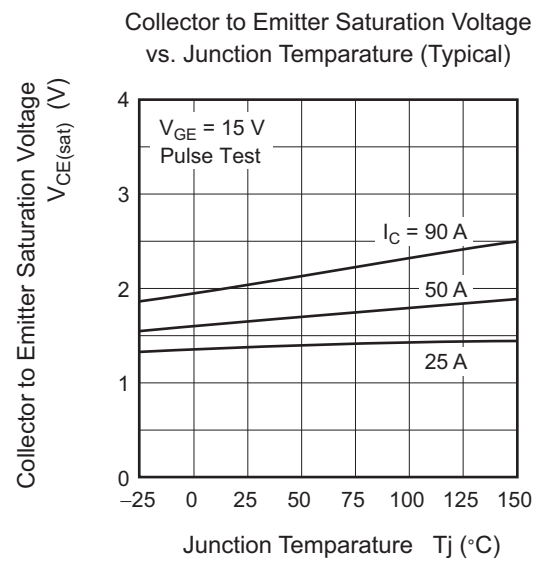
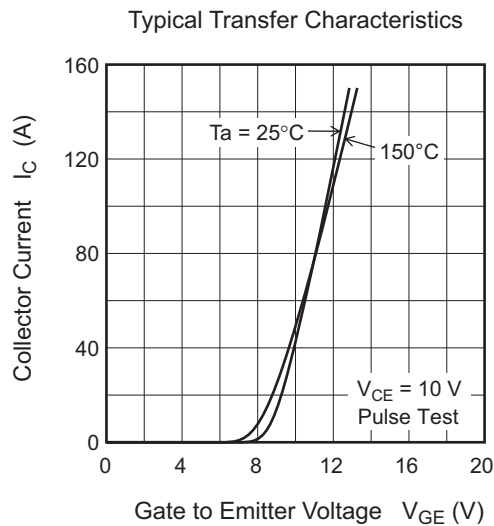
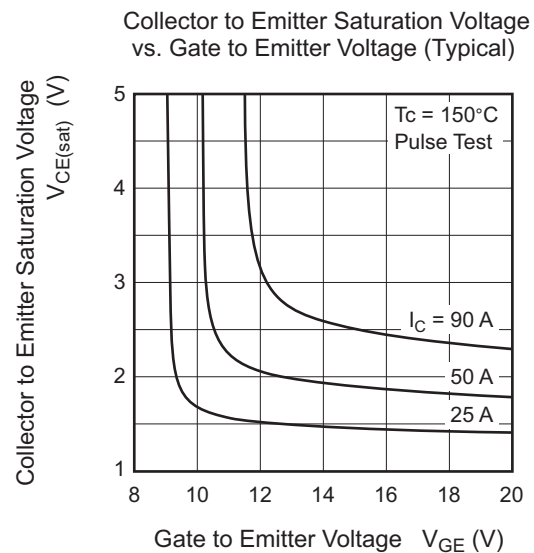
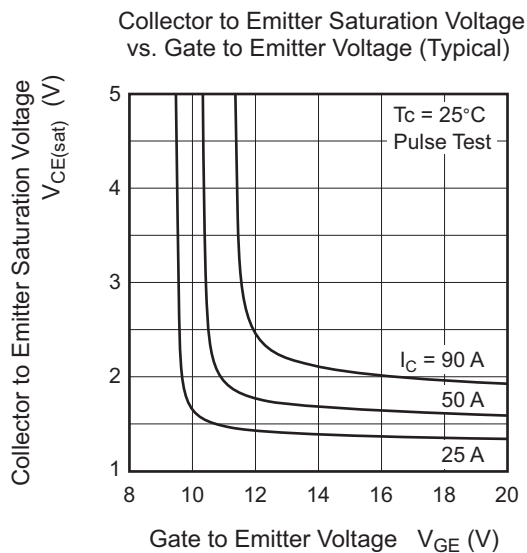
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero gate voltage collector current / Diode reverse current	I_{CES} / I_R	—	—	5	μA	$V_{CE} = 600 V, V_{GE} = 0$
Gate to emitter leak current	I_{GES}	—	—	± 1	μA	$V_{GE} = \pm 30 V, V_{CE} = 0$
Gate to emitter cutoff voltage	$V_{GE(off)}$	5	—	7	V	$V_{CE} = 10 V, I_C = 1 mA$
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	1.6	2.1	V	$I_C = 50 A, V_{GE} = 15 V$ ^{Note3}
	$V_{CE(sat)}$	—	2.0	—	V	$I_C = 90 A, V_{GE} = 15 V$ ^{Note3}
Input capacitance	C_{ies}	—	3150	—	pF	$V_{CE} = 25 V$
Output capacitance	C_{oes}	—	200	—	pF	$V_{GE} = 0$
Reverse transfer capacitance	C_{res}	—	120	—	pF	$f = 1 MHz$
Total gate charge	Q_g	—	170	—	nC	$V_{GE} = 15 V$
Gate to emitter charge	Q_{ge}	—	30	—	nC	$V_{CE} = 300 V$
Gate to collector charge	Q_{gc}	—	90	—	nC	$I_C = 50 A$
Turn-on delay time	$t_{d(on)}$	—	60	—	ns	$V_{CC} = 300 V$ $V_{GE} = 15 V$ $I_C = 50 A$ $R_g = 5 \Omega$ (Inductive load)
Rise time	t_r	—	70	—	ns	
Turn-off delay time	$t_{d(off)}$	—	200	—	ns	
Fall time	t_f	—	45	—	ns	
Turn-on energy	E_{on}	—	1.7	—	mJ	
Turn-off energy	E_{off}	—	1.2	—	mJ	
Total switching energy	E_{total}	—	2.9	—	mJ	
Short circuit withstand time	t_{sc}	6	8	—	μs	$T_C = 100 ^\circ C$ $V_{CC} \leq 360 V, V_{GE} = 15 V$

FRD forward voltage	V_F	—	1.4	2.0	V	$I_F = 50 A$ ^{Note3}
FRD reverse recovery time	t_{rr}	—	100	—	ns	$I_F = 50 A$ $diF/dt = 100 A/\mu s$
FRD reverse recovery charge	Q_{rr}	—	0.27	—	μC	
FRD peak reverse recovery current	I_{rr}	—	5	—	A	

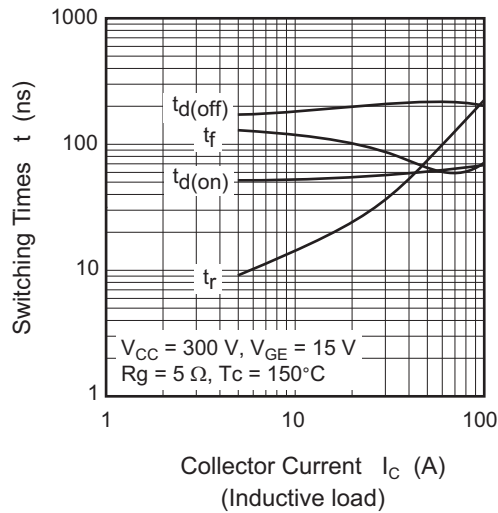
Notes: 3. Pulse test

Main Characteristics

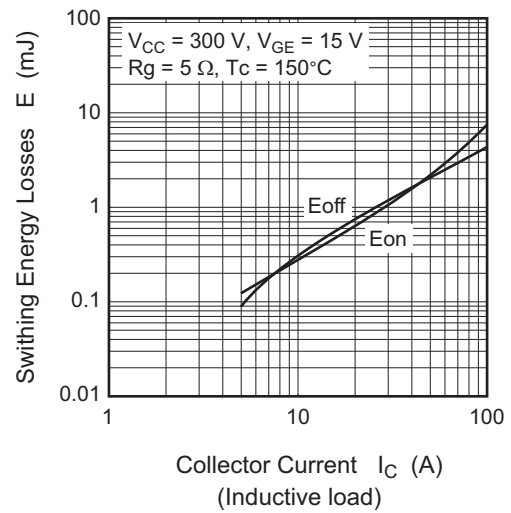




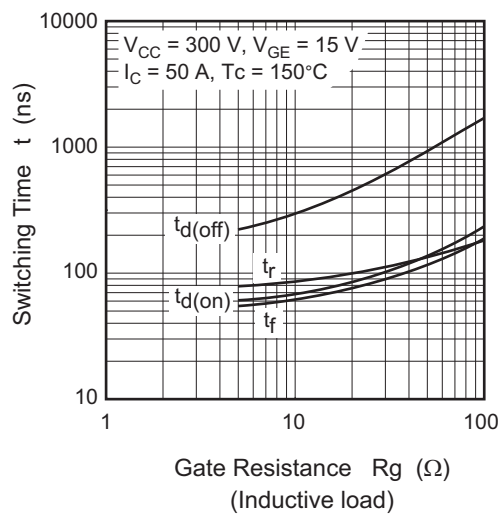
Switching Characteristics (Typical) (1)



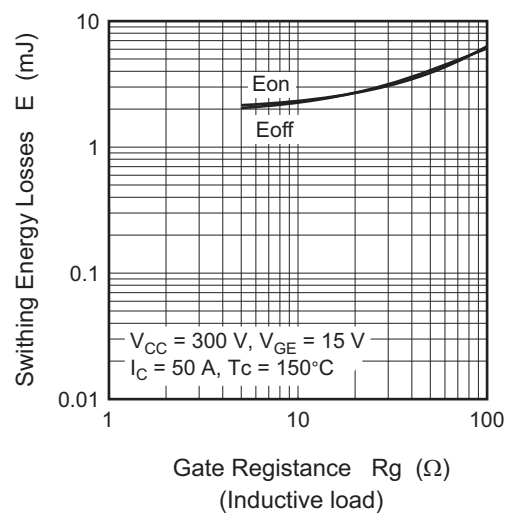
Switching Characteristics (Typical) (2)



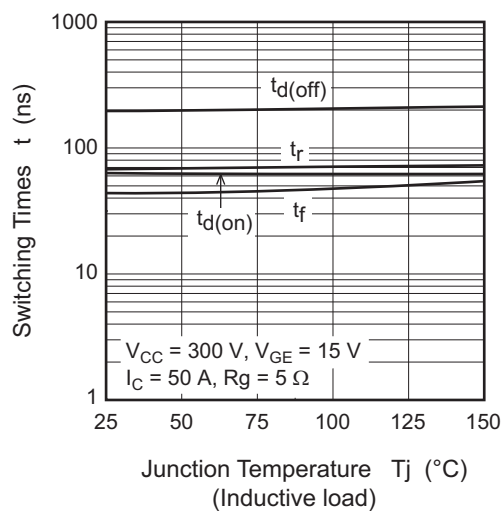
Switching Characteristics (Typical) (3)



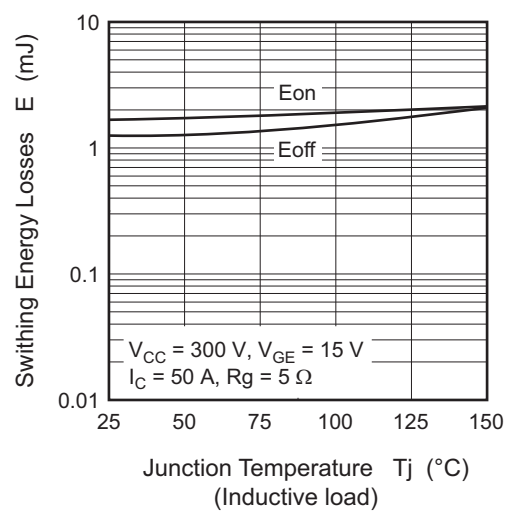
Switching Characteristics (Typical) (4)



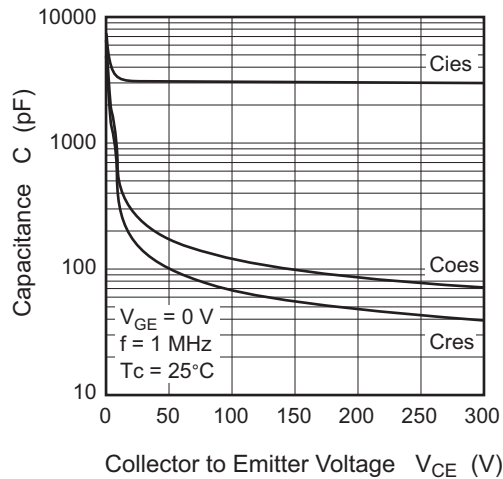
Switching Characteristics (Typical) (5)



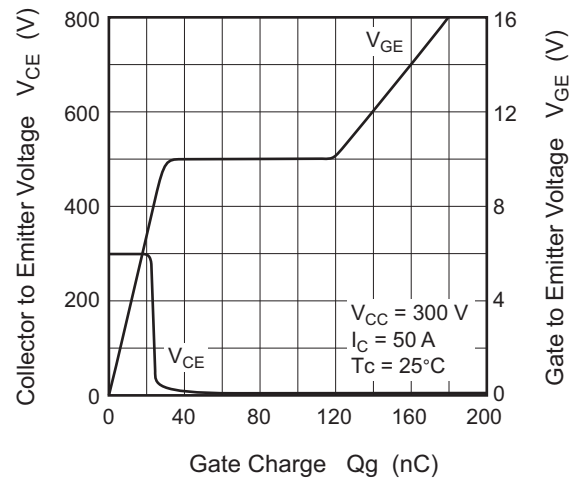
Switching Characteristics (Typical) (6)



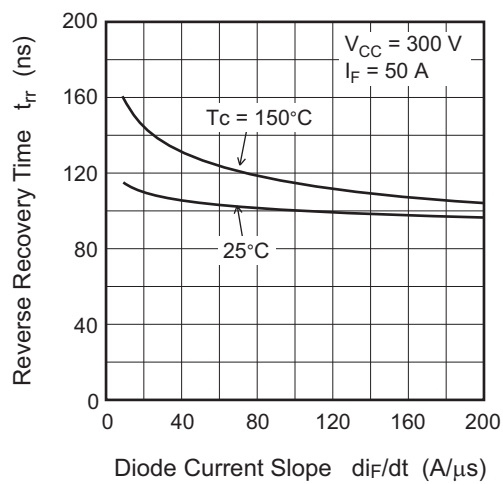
Typical Capacitance vs.
Collector to Emitter Voltage



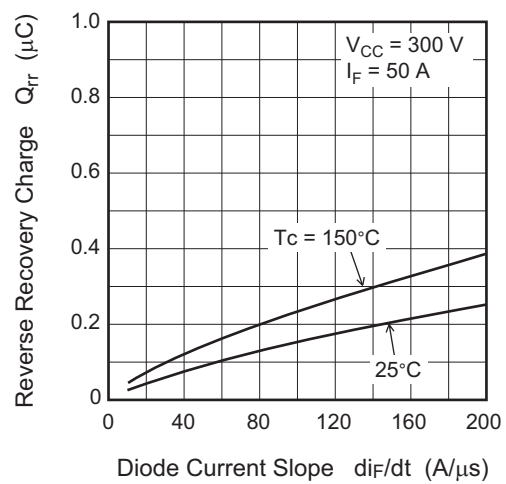
Dynamic Input Characteristics (Typical)



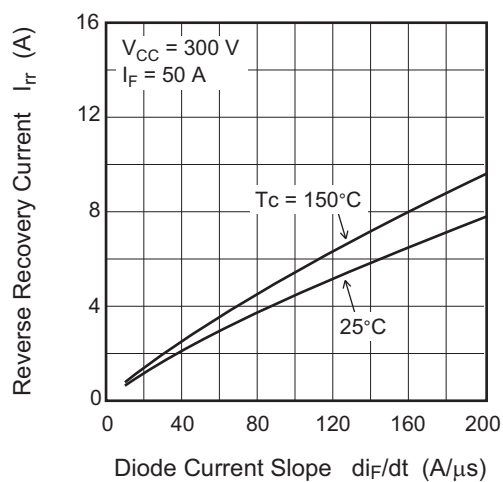
Reverse Recovery Time vs.
Diode Current Slope (Typical)



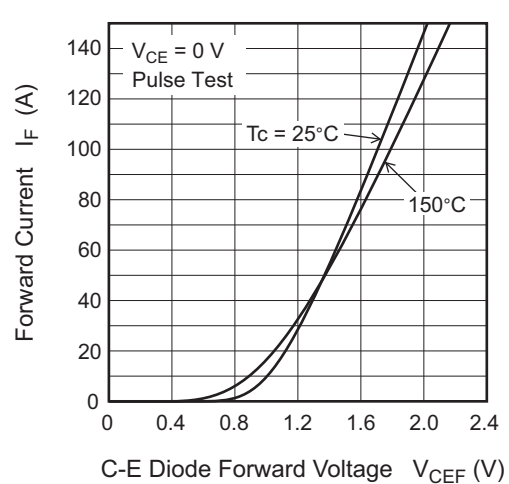
Reverse Recovery Charge vs.
Diode Current Slope (Typical)

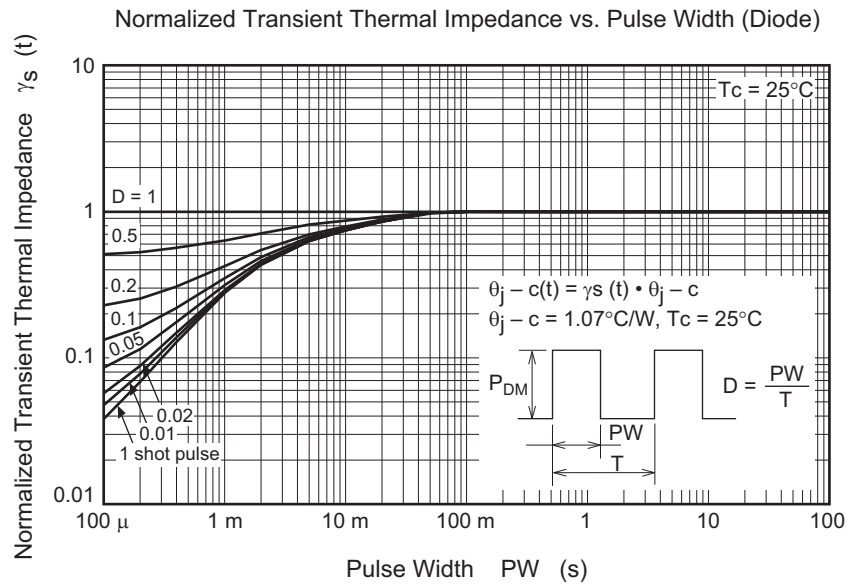
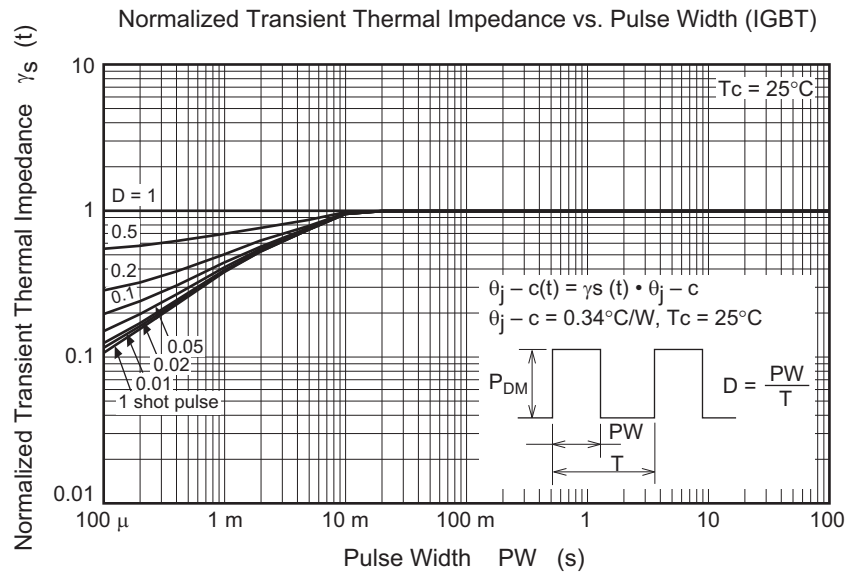


Reverse Recovery Current vs.
Diode Current Slope (Typical)

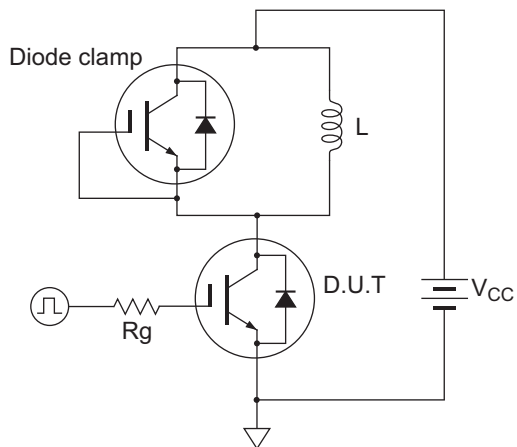


Forward Current vs. Forward Voltage (Typical)

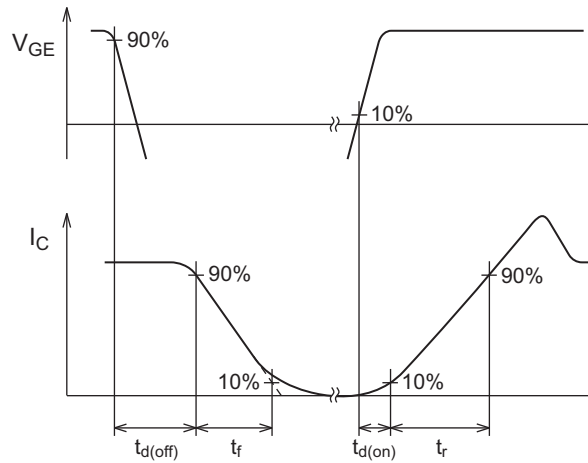




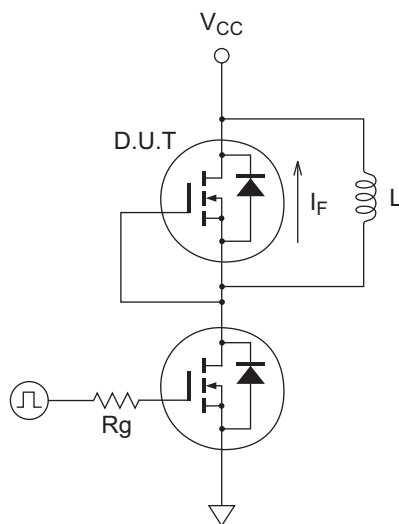
Switching Time Test Circuit



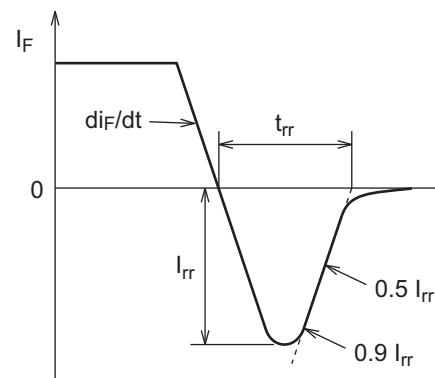
Waveform



Diode Reverse Recovery Time Test Circuit



Waveform



Package Dimension

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]	Unit: mm
TO-247	—	PRSS0003ZE-A	—	6.0g	

Technical drawing of the RJH60M7DPQ-E0 package showing three views: top, side, and front. The top view shows a square package with a central circular feature and four pins. Dimensions include 15.94 ± 0.19 mm for the width, 21.13 ± 0.33 mm for the height, and 6.15 mm for the pin height. The side view shows a height of 5.02 ± 0.19 mm. The front view shows a width of 13.26 mm and a height of 17.63 mm. Other dimensions include 20.19 ± 0.38 mm, 4.5 max, 2.10 +0.1/-0.2, 1.27 ± 0.13, 5.45, 0.71 ± 0.1, and 2.41.

Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJH60M7DPQ-E0#T2	450 pcs	Tube

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