

BCR5FM-12RB

600V - 5A - Triac

Medium Power Use

R07DS0956EJ0101

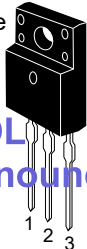
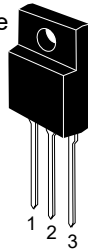
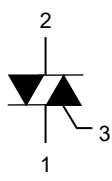
Rev.1.01

Feb. 19, 2019

Features

- $I_T (RMS)$: 5 A
- V_{DRM} : 600 V
- T_j : 150°C
- $I_{FGTI}, I_{RGTI}, I_{RGTIII}$: 15 mA (10 mA)^{Note5}
- Insulated Type
- Planar Passivation Type
- Viso: 2000 V

Outline

RENESAS Package code: PRSS0003AG-A (Package name: TO-220FP) Ordering code #BB0	RENESAS Package code: PRSS0003AP-A (Package name: TO-220FPA) Ordering code #BG0				1. T ₁ Terminal 2. T ₂ Terminal 3. Gate Terminal
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EOL announced

Application

Electric rice cooker, electric pot, and other resistive load.

Maximum Ratings

Parameter	Symbol	Voltage class		Unit
		12		
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	600		V
Non-repetitive peak off-state voltage ^{Note1}	V_{DSM}	720		V

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	$I_T (RMS)$	5	A	Commercial frequency, sine full wave 360°conduction, $T_c = 122^\circ\text{C}$
Surge on-state current	I_{TSM}	50	A	60 Hz sinewave 1 full cycle, peak value, non-repetitive
I^2t for fusion	I^2t	10.4	A ² s	Value corresponding to 1 cycle of half wave 60 Hz, surge on-state current
Peak gate power dissipation	P_{GM}	3	W	
Average gate power dissipation	$P_{G(AV)}$	0.3	W	
Peak gate voltage	V_{GM}	10	V	
Peak gate current	I_{GM}	2	A	
Junction Temperature	T_j	-40 to +150	°C	
Storage temperature	T_{stg}	-40 to +150	°C	
Isolation voltage ^{Note5}	V_{iso}	2000	V	$T_a=25^\circ\text{C}$, AC 1 minute, $T_1 \cdot T_2 \cdot G$ terminal to case

Notes: 1. Gate open.

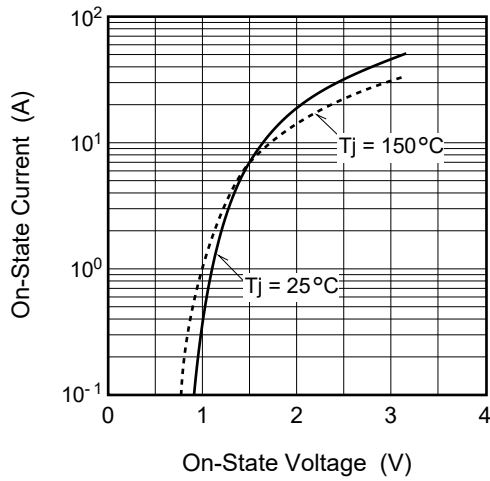
Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Repetitive peak off-state current	I_{DRM}	—	—	2.0	mA	$T_j = 150^\circ\text{C}$, V_{DRM} applied
On-state voltage	V_{TM}	—	—	1.5	V	$T_c = 25^\circ\text{C}$, $I_{TM} = 7\text{ A}$, instantaneous measurement
Gate trigger voltage ^{Note2}	I	V_{FGTI}	—	—	1.5	$T_j = 25^\circ\text{C}$, $V_D = 6\text{ V}$, $R_L = 6\ \Omega$, $R_G = 330\ \Omega$
	II	V_{RGTI}	—	—	1.5	
	III	V_{RGTIII}	—	—	1.5	
Gate trigger current ^{Note2}	I	I_{FGTI}	—	—	15 ^{Note4}	$T_j = 25^\circ\text{C}$, $V_D = 6\text{ V}$, $R_L = 6\ \Omega$, $R_G = 330\ \Omega$
	II	I_{RGTI}	—	—	15 ^{Note4}	
	III	I_{RGTIII}	—	—	15 ^{Note4}	
Gate non-trigger voltage	V_{GD}	0.2	—	—	V	$T_j = 125^\circ\text{C}$, $V_D = 1/2 V_{DRM}$
		0.1	—	—	V	$T_j = 150^\circ\text{C}$, $V_D = 1/2 V_{DRM}$
Thermal resistance	$R_{th(j-c)}$	—	—	4.9	$^\circ\text{C/W}$	Junction to case ^{Note3}

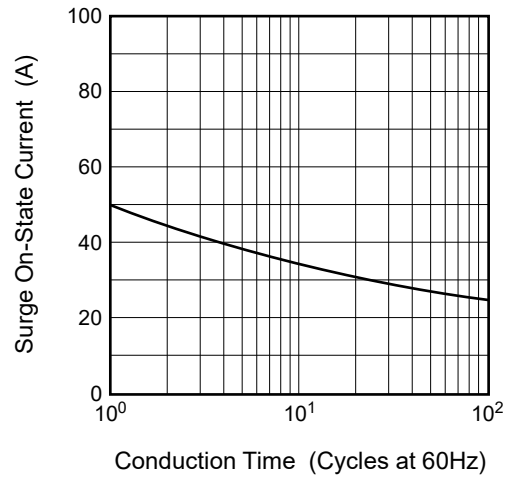
- Notes: 2. Measurement using the gate trigger characteristics measurement circuit.
 3. The contact thermal resistance $R_{th(c-f)}$ in case of greasing is 0.5°C/W .
 4. High sensitivity ($I_{GT} \leq 10\text{ mA}$) is also available. (I_{GT} item:1)
 5. Make sure that your finished product containing this device meets your safe isolation requirements.
 For safety, it's advisable that heatsink is electrically floating.

Performance Curves

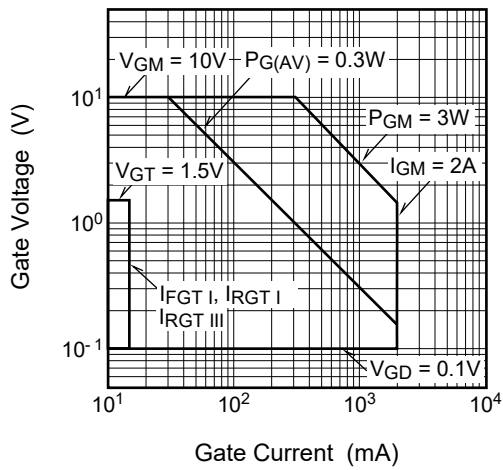
Maximum On-State Characteristics



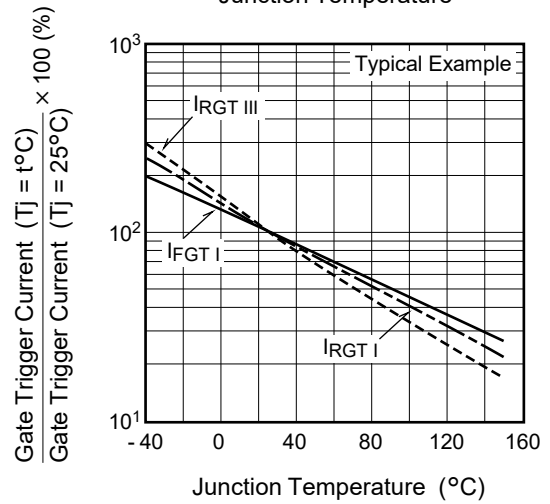
Rated Surge On-State Current



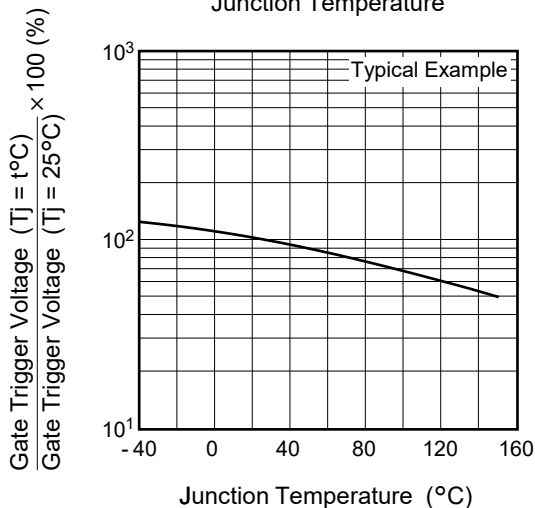
Gate Characteristics (I, II and III)



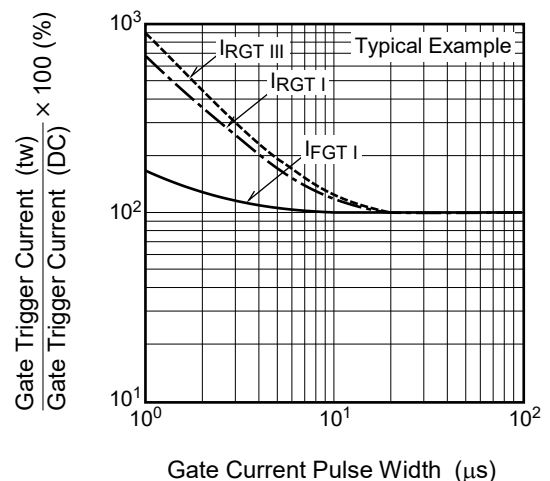
Gate Trigger Current vs. Junction Temperature

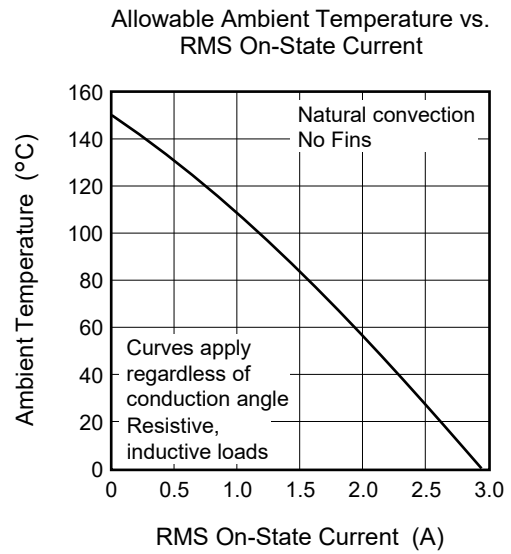
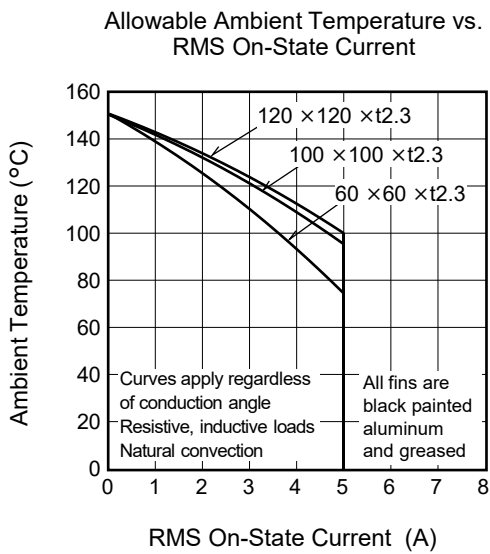
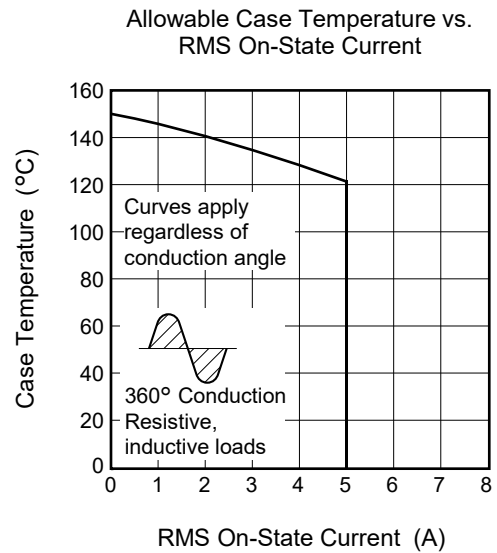
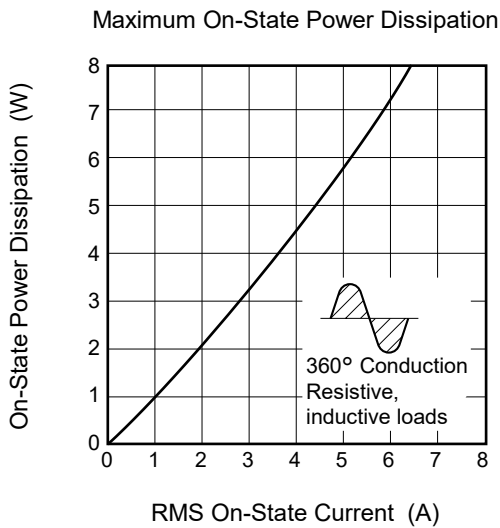
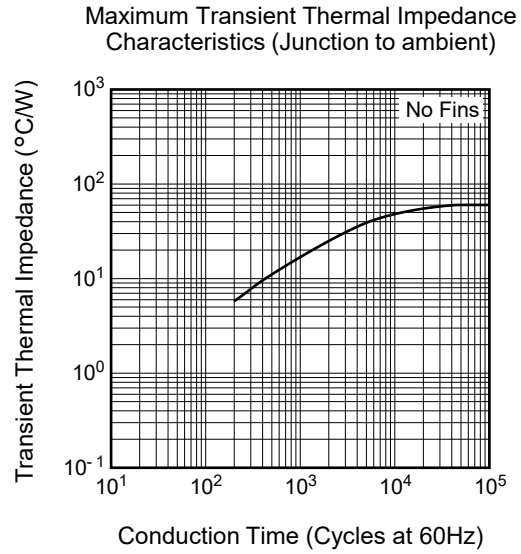
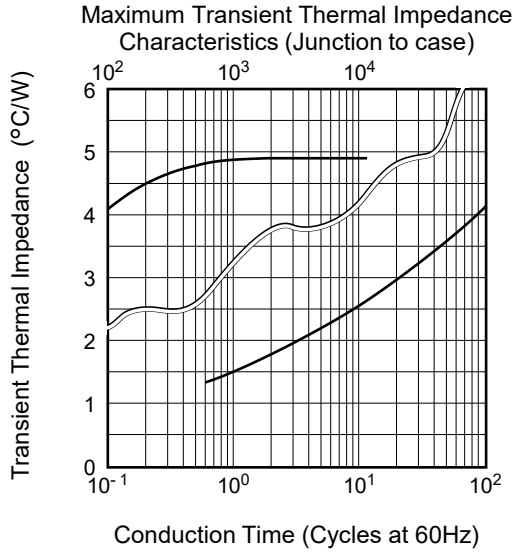


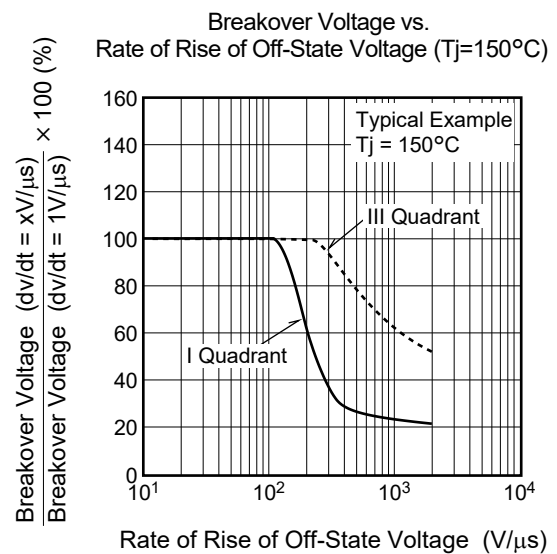
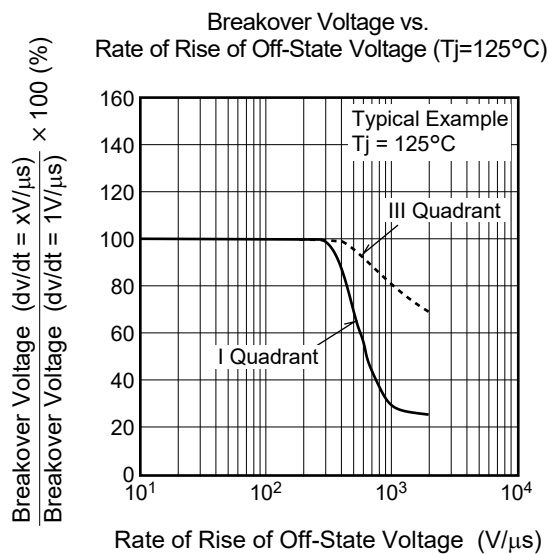
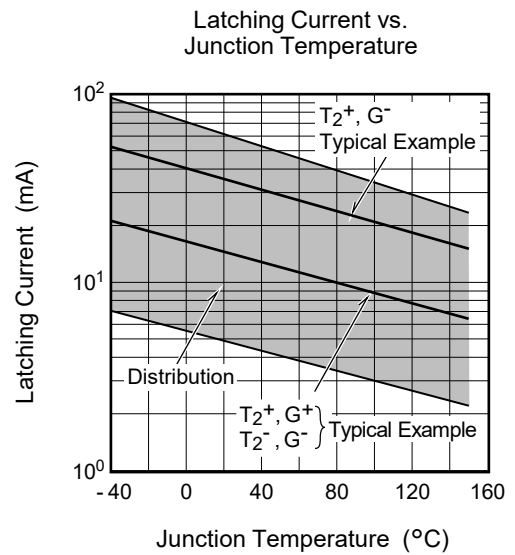
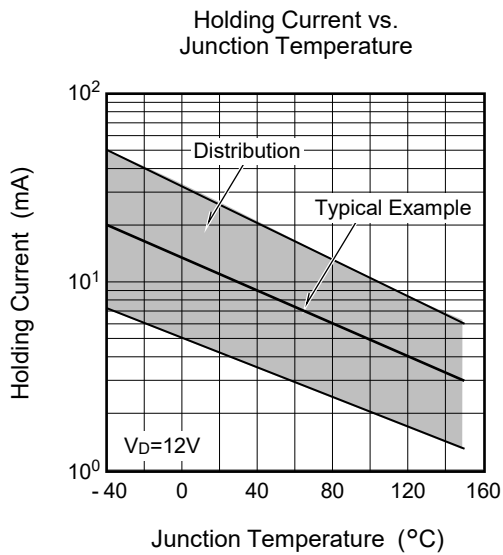
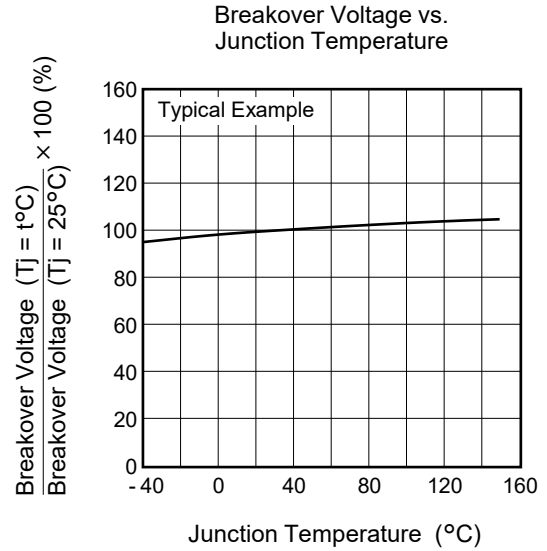
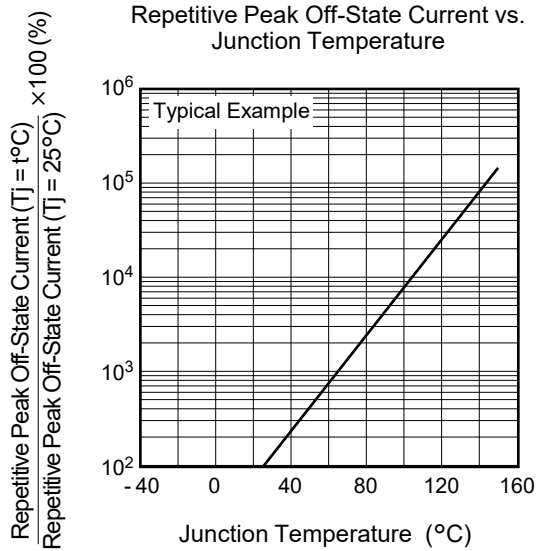
Gate Trigger Voltage vs. Junction Temperature



Gate Trigger Current vs. Gate Current Pulse Width

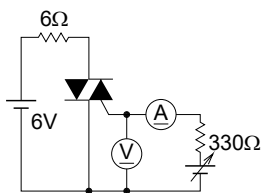




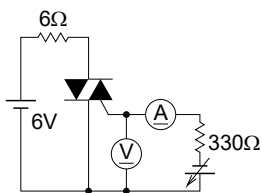


Gate Trigger Characteristics Test Circuits

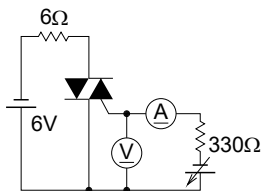
Recommended peripheral components for Triac



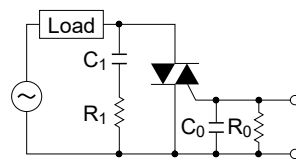
Test Procedure I



Test Procedure II



Test Procedure III



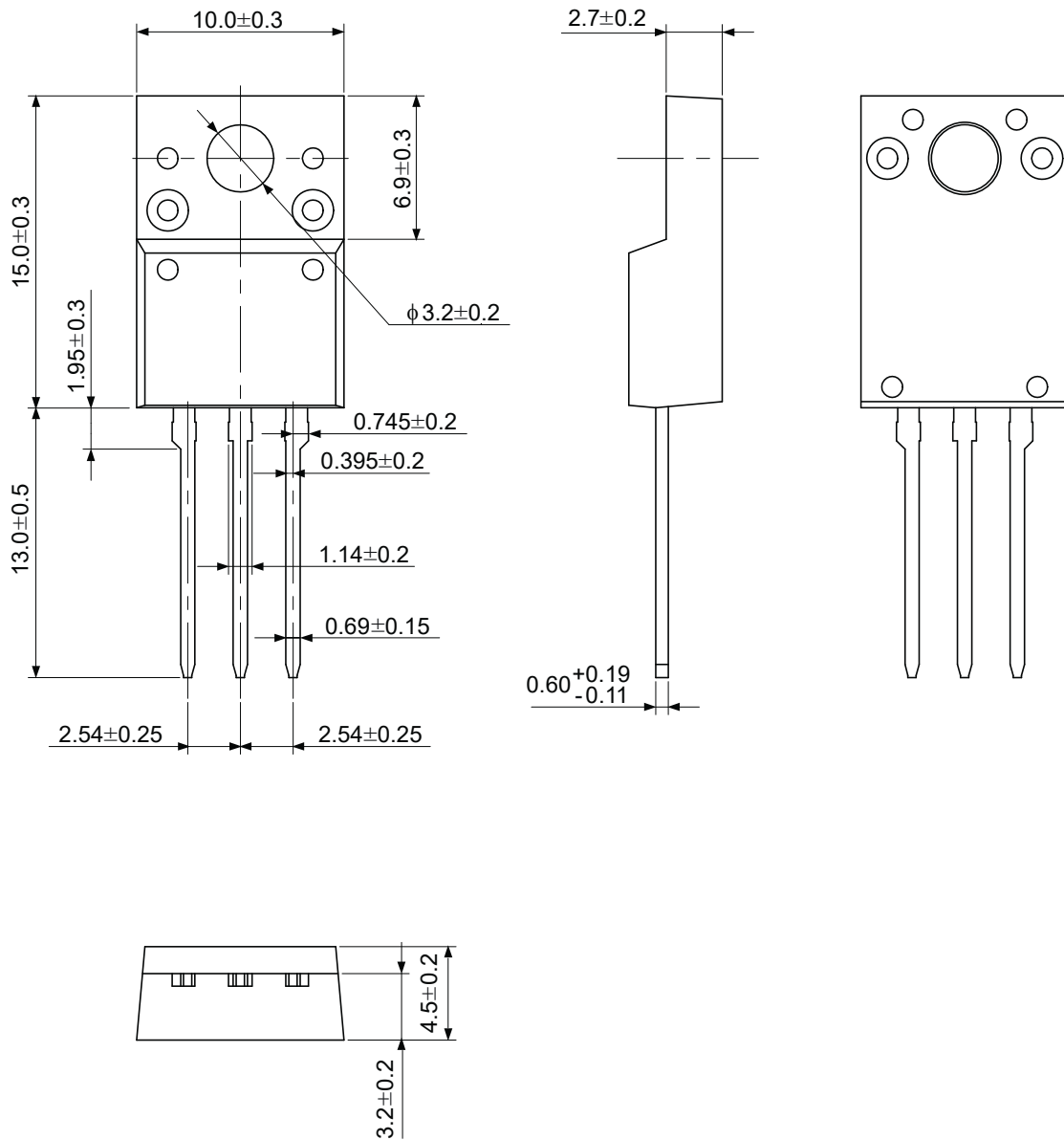
$C_1 = 0.1 \text{ to } 0.47 \mu\text{F}$ $C_0 = 0.1 \mu\text{F}$
 $R_1 = 47 \text{ to } 100\Omega$ $R_0 = 100\Omega$

Package Dimensions

Ordering code: #BG0

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
-	PRSS0003AP-A	TO-220FPA	1.65

Unit: mm



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