

BCR08DS-14A

700V-0.8A-Triac

Low Power Use

R07DS0258EJ0300 Rev.3.00 Dec 01, 2014

Features

I_{T (RMS)}: 0.8 A
 V_{DRM}:700 V

 $\bullet \quad I_{FGTI},\,I_{RGTI},\,I_{RGTIII}:5\,\,mA$

Planar Passivation Type

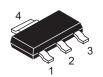
• Surface Mounted Type

Completed Pb Free

Outline

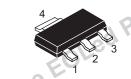
RENESAS Package code: PRSP0004ZB-A

(Package name: SOT-223)



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Applications

Washing machine, electric fan, air cleaner, other general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class 14	Unit
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	700	V
Non- repetitive peak off-state voltage ^{Note1}	V_{DSM}	840	V

Notes: 1. Gate open.

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.8	A	Commercial frequency, sine full wave 360° conduction, Tc= 96°C ^{Note3}
Surge on-state current	I _{TSM}	8	А	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.26	A ² s	Value corresponding to 1 cycle of half
				wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	1	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate voltage	V_{GM}	6	V	
Peak gate current	I _{GM}	0.5	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.12	g	Typical value

Electrical Characteristics

Parameter		Symbol BCR08DS-14A#B10 BCR08DS-14A#BD0			BCR08DS-14A#B11			Unit	Test conditions	
			Min.	Тур.	Max.	Min.	Тур.	Max.		
Repetitive peak off-s	state	I _{DRM}	_	_	1.0	_	_	1.0	mA	Tj = 125°C V _{DRM} applied
On-state voltage		V _{TM}	_	_	2.0	_	_	2.0	V	Tc = 25°C, I _{TM} =1.2 A instantaneous measurement
Gate trigger	I	V_{FGTI}	_	_	2.0	_	_	2.0	V	Tj = 25°C, V _D = 6 V
voltage ^{Note2}	II	V_{RGTI}	_	_	2.0	_	_	2.0	V	$R_L = 6 \Omega$, $R_G = 330 \Omega$
	III	V_{RGTIII}	_	_	2.0	_	_	2.0	V	
	IV	V_{FGTIII}	_	_	_	_	_	2.0	V	
Gate trigger	I	I_{FGTI}		_	5	_	_	5	mA	$Tj = 25^{\circ}C, V_D = 6 V$
current ^{Note2}	II	I_{RGTI}	_	_	5	_	_	5	mA	$R_L = 6 \Omega$, $R_G = 330 \Omega$
	III	I _{RGTIII}	_	_	5	_	_	5	mA	
	IV	I _{FGTIII}	_	_	_	_	_	7	mA	
Gate non-trigger vol	tage	V_{GD}	0.2	_	_	0.2	_	_	V	$Tj = 125^{\circ}C$ $V_D = 1/2 V_{DRM}$
Thermal resistance		R _{th (j-c)}	_	_	25	_	_	25	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off- state commutating voltage Note4		(dv/dt)c	0.5	_	_	0.5	_	_	V/µs	Tj = 125°C
Critical-rate of rise o state voltage Note5	f off-	dv/dt	200	_	_	200	_	_	V/µs	Tj = 125°C

Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

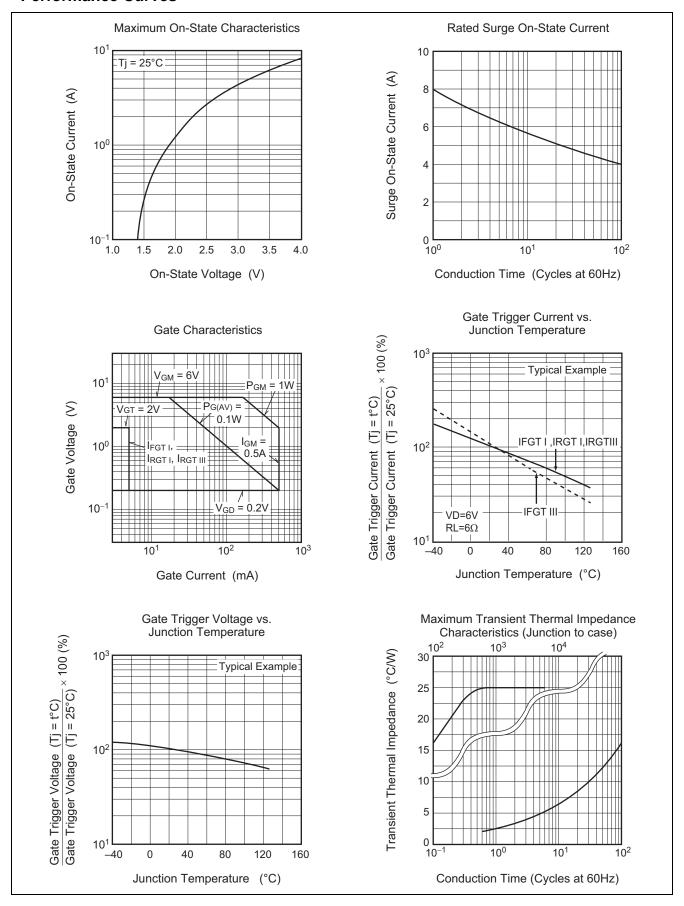
- 3. Case temperature is measured on the T_2 tab.
- 4. Test conditions of the critical-rate of rise of off-state commutating voltage are shown in the table below.

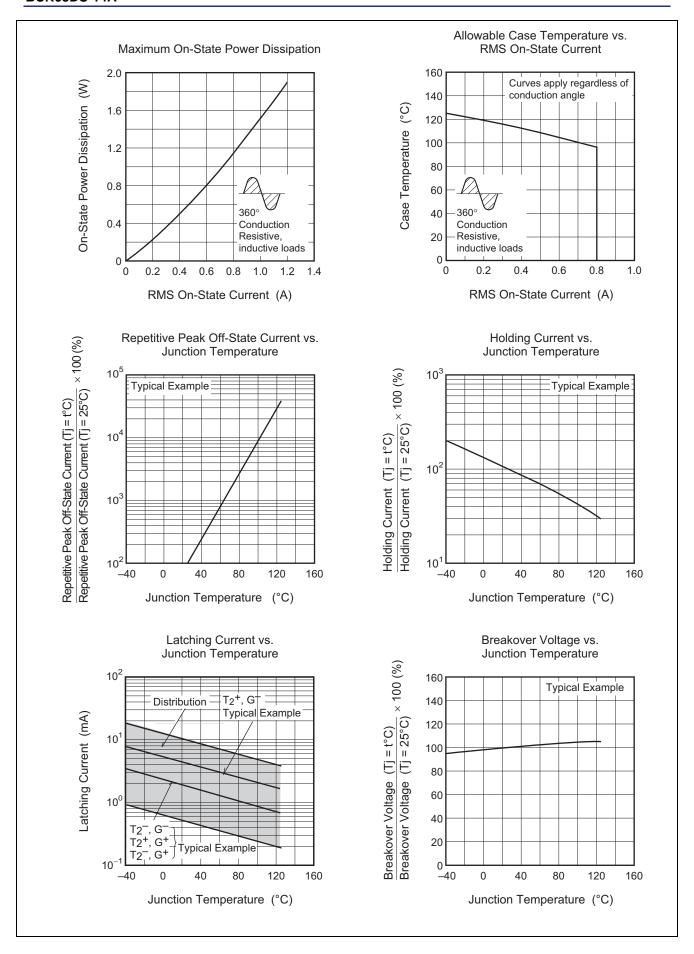
Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage → Time
2. Rate of decay of on-state commutating current (di/dt)c = - 0.4 A/ms	Main Current (di/dt)c Time
3. Peak off-state voltage $V_D = 400 \text{ V}$	Main Voltage — Time (dv/dt)c

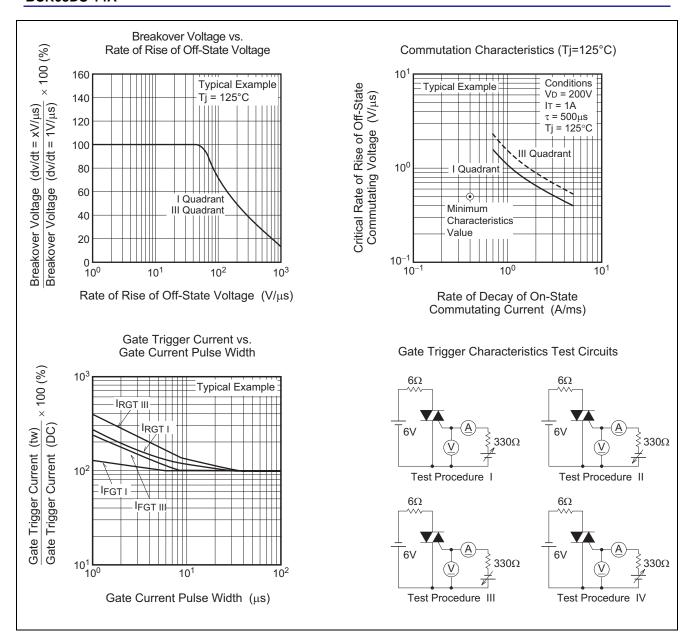
5. Test conditions of the critical-rate of rise of off-state voltage are shown in the table below.

Test conditions	Off-state voltage waveforms		
 Junction temperature Tj = 125°C Off-state voltage waveform Linear waveform Peak off-state voltage V_D = 200 V Gate open 	V_{D} $0.9V_{D}$ $ -$		

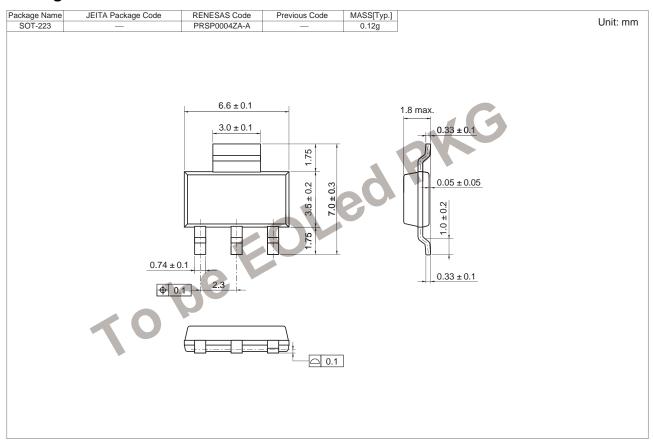
Performance Curves

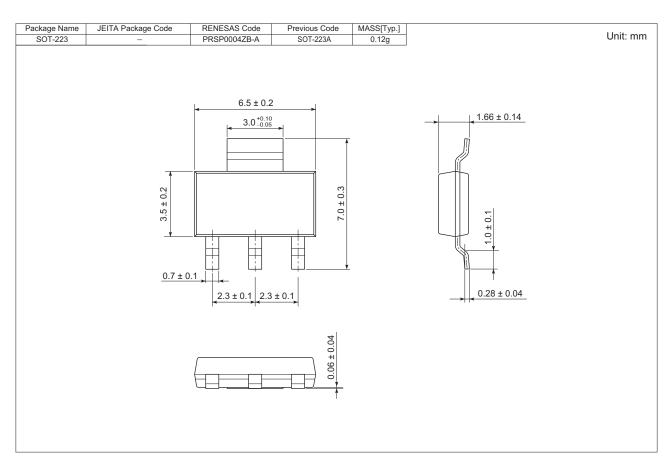






Package Dimensions





Ordering Information

Orderable Part Number	Packing	Quantity	Remark
BCR08DS-14AT13#B10	Embossed Tape	3000 pcs.	Not Recommended for New Design
BCR08DS-14AT13#B11	Embossed Tape	3000 pcs.	Not Recommended for New Design
BCR08DS-14AT13#BD0	Embossed Tape	3000 pcs.	Taping direction "T1"

Note: Please confirm the specification about the shipping in detail.

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