

CR05AS-8

Thyristor

Low Power Use

Features

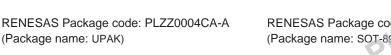
- I_{T (AV)} : 0.5 A
- V_{DRM} : 400 V
- I_{GT} : 100 μA

Outline

R07DS0135EJ0500 (Previous: REJ03G0348-0400) Rev.5.00 Sep 15, 2010

Datasheet

- Non-Insulated Type
- Planar Passivation Type
- Surface Mounted type





RENESAS Package code: PLZZ0004CB-A (Package name: SOT-89)



2.4

1. Cathode 2. Anode

3. Gate 4. Anode

Applications

Solid state relay, strobe flasher, igniter, and hybrid IC

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
	Symbol	8 (Mark CD)		
Repetitive peak reverse voltage	V _{RRM}	400	V	
Non-repetitive peak reverse voltage	V _{RSM}	500	V	
DC reverse voltage	V _{R (DC)}	320	V	
Repetitive peak off-state voltage ^{Note1}	V _{DRM}	400	V	
DC off-state voltage ^{Note1}	V _{D (DC)}	320	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.79	А	
Average on-state current	I _{T (AV)}	0.5	A	Commercial frequency, sine half wave 180° conduction, Ta = $57^{\circ}C^{Note2}$
Surge on-state current	I _{TSM}	10	A	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.4	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	0.1	W	
Average gate power dissipation	P _{G (AV)}	0.01	W	
Peak gate forward voltage	V _{FGM}	6	V	
Peak gate reverse voltage	V _{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.1	Α	
Junction temperature	Tj	– 40 to +125	°C	
Storage temperature	Tstg	– 40 to +125	°C	
Mass	_	50	mg	Typical value

Notes: 1. With gate to cathode resistance $R_{GK} = 1 \ k\Omega$.



Electrical Characteristics

Parameter	Symbol	Rated value			Unit	Test conditions
		Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	—	—	0.1	mA	Tj = 125°C, V _{RRM} applied
Repetitive peak off-state current	I _{DRM}	—	—	0.1	mA	Tj = 125°C, V _{DRM} applied, R _{GK} = 1 k Ω
On-state voltage	V _{TM}	—	—	1.9	V	Ta = 25°C, I_{TM} = 1.5 A, instantaneous value
Gate trigger voltage	V _{GT}	—	—	0.8	V	$Tj = 25^{\circ}C, V_D = 6 V,$ $I_T = 0.1 A^{Note4}$
Gate non-trigger voltage	V _{GD}	0.2	—	—	V	$\label{eq:tilde} \begin{split} Tj &= 125^\circ\text{C}, \ V_\text{D} = 1/2 \ V_\text{DRM}, \\ R_\text{GK} &= 1 \ k\Omega \end{split}$
Gate trigger current	I _{GT}	20	—	100 ^{Note3}	μA	$Tj = 25^{\circ}C, V_D = 6 V,$ $I_T = 0.1 A^{Note4}$
Holding current	I _H	—	—	3	mA	$\label{eq:tilde} \begin{split} Tj &= 25^\circC, V_D = 12 V, \\ R_{GK} &= 1 k\Omega \end{split}$
Thermal resistance	R _{th (j-a)}	_		70	°C/W	Junction to ambient ^{Note2}

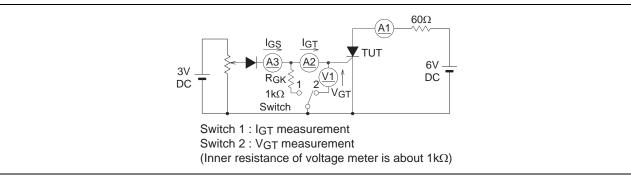
Notes: 2. Soldering with ceramic plate (25 mm \times 25 mm \times t0.7 mm).

3. If special values of I_{GT} are required, choose item E from those listed in the table below if possible.

Item	В	E
I _{GT} (μΑ)	20 to 50	20 to 100

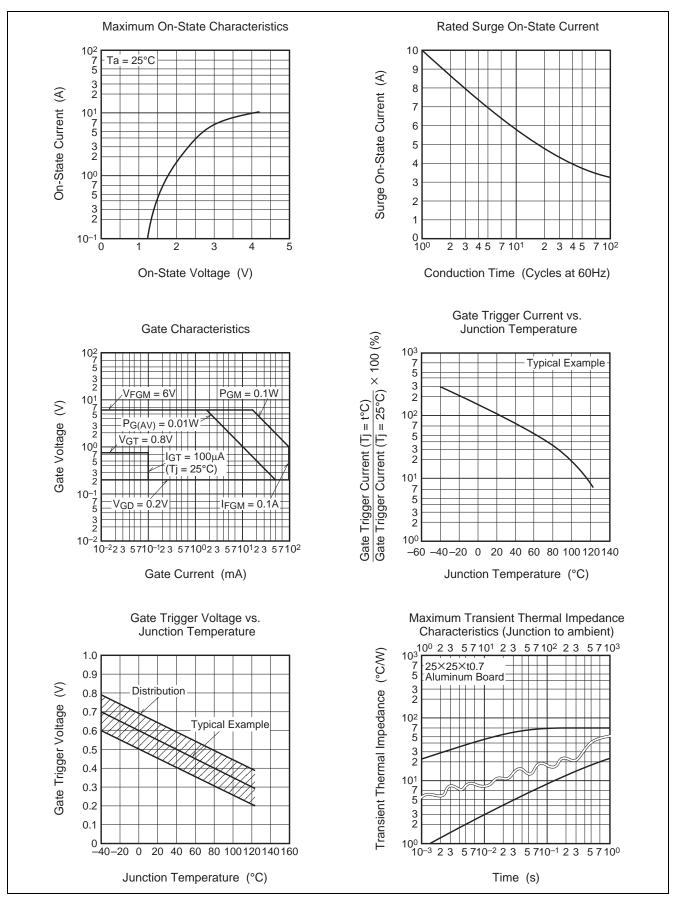
The above values do not include the current flowing through the 1 $k\Omega$ resistance between the gate and cathode.

4. I_{GT} , V_{GT} measurement circuit.

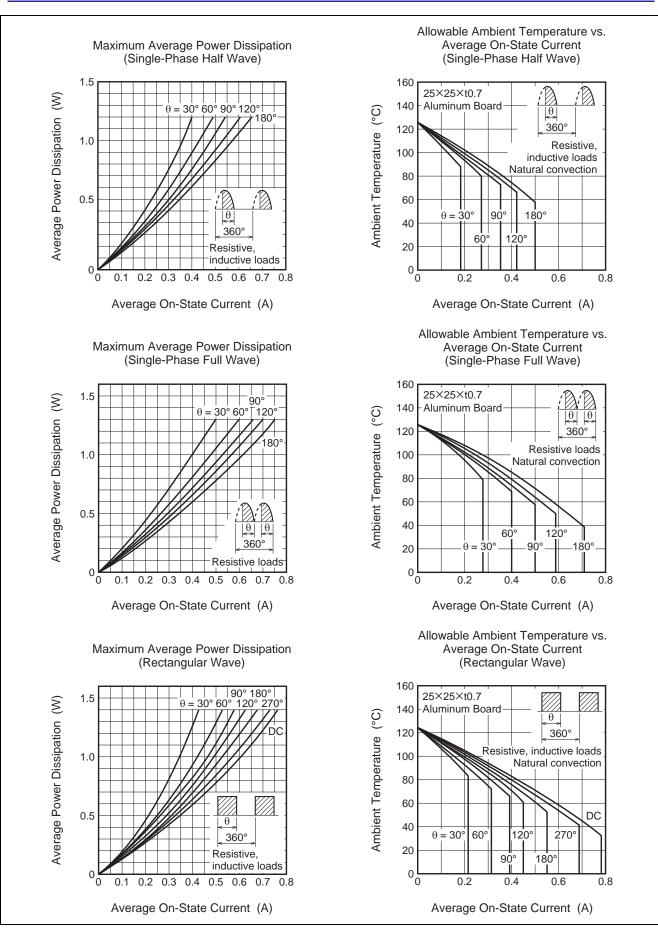


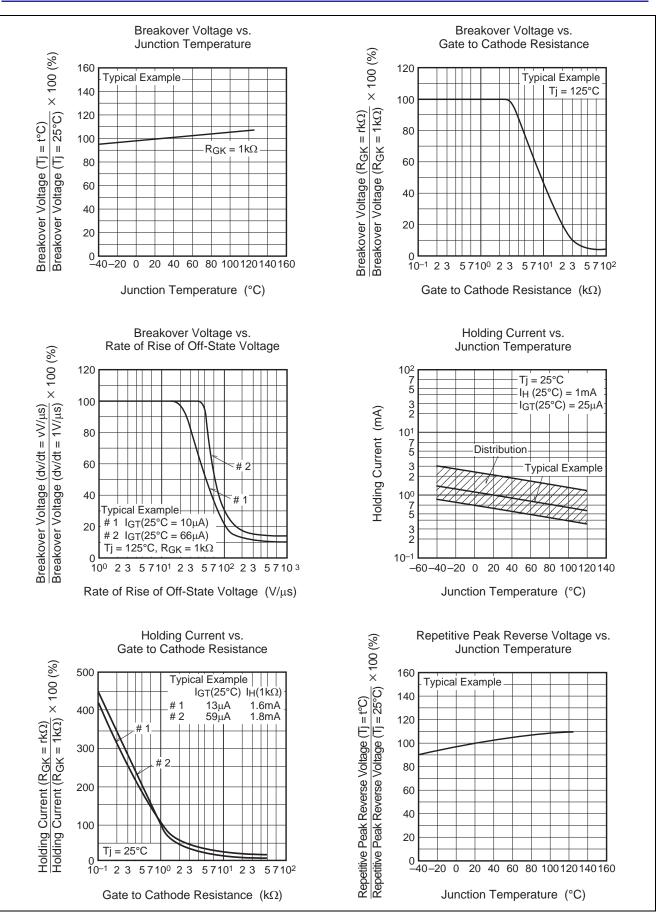


Performance Curves

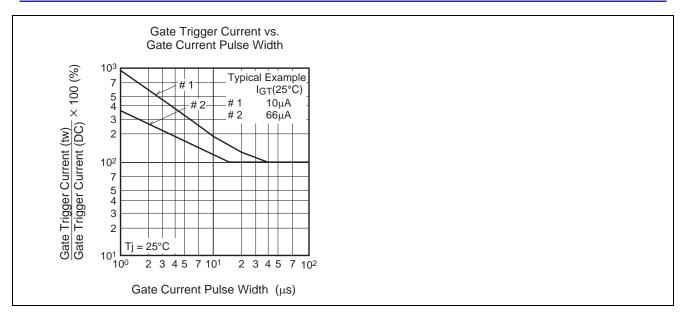






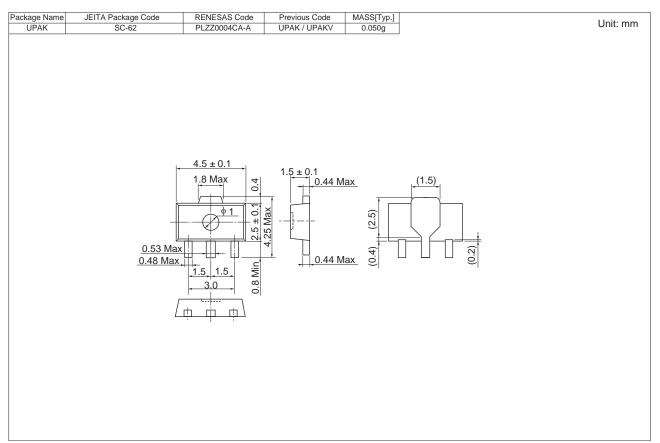


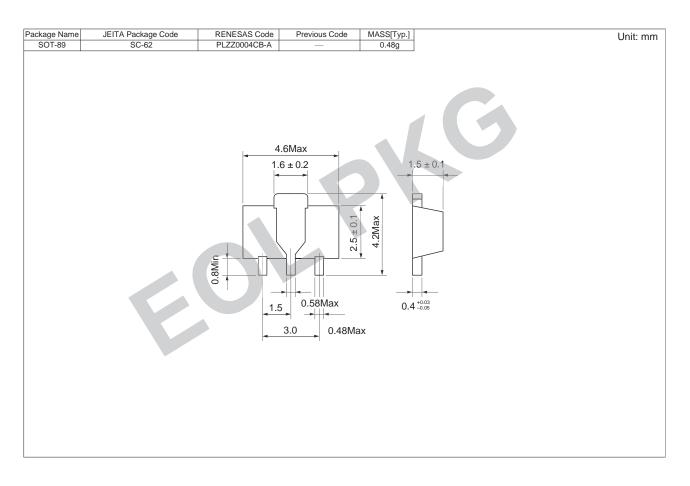






Package Dimensions







Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	4000	Type name – ET +Direction (1 or 2) + 4	CR05AS-8-ET14

Note : Please confirm the specification about the shipping in detail.



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