

# ISL54210EVAL1Z

Evaluation Board

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## Description

The ISL54210EVAL1Z evaluation board is designed to provide a quick and easy method for evaluating the ISL54210 USB/Audio Switch IC.

The ISL54210 device is an unique IC. To use this evaluation board properly requires a thorough knowledge of the operation of the IC. Refer to the data sheet for an understanding of the functions and features of the device. Studying the device's truth-table along with its pinout diagram is the best way to get a quick understanding of how the part works.

A picture of the main evaluation board is shown in Figure 2. The ISL54210 TDFN IC is soldered onto the evaluation board. It is located in the center of the board and is designated as U1.

The ISL54210 IC has three modes of operation called Audio mode, USB mode and Mute mode. The evaluation board contains standard Audio and USB connectors to allow the user to easily interface with the IC to evaluate its functions, features, and performance in these modes of operation.

For example, when the board is properly powered, a headphone can be plugged into the headphone jack (J7) and a stereo source, such as an MP3 player, can be connected at the RCA connectors J3 (AUDIO LEFT) and J4 (AUDIO RIGHT) to play music through the audio switches of the IC. See Figure 2. You can then turn-off the audio source, remove the headphones, configure the logic for USB operation and then attach a USB cable from a computer to the USB connector J1 (USB TO HOST). This will establish a connection from the computer through the USB switches of the IC to the USB device connected at the USB connector J2 (USB TO DEVICE), to send and receive data.

This application note will guide the user through the process of configuring and using the evaluation board to evaluate the ISL54210 device in both the Audio mode and the USB mode.

## Features

- Standard USB Connectors
- RCA Audio Input Jacks and Headphone Speaker Jack
- Convenient Test Points and Connections for Test Equipment

## Picture of Evaluation Board (Top View)



FIGURE 1. ISL54210EVAL1Z EVALUATION BOARD

## Board Architecture/Layout

### Basic Layout of Evaluation Board

- The basic layout of the main board is as follows (refer to Figure 1):
- The Power and Ground connections are at the top of the board (J10 and J11).
- The Logic control connections are at the top of the board (J12 and J13).
- The Audio Source Inputs are at the lower right side of the board (J3 - J6). Audio outputs are at the lower left side of the board (J7, J8, J9). Connector J7 is a standard headphone jack for connecting a 32Ω headphone.
- The USB connection to an upstream host controller (Computer) is made at connector J1, located on left under-side of the board. USB connection to a downstream USB device is made at connector J2, located on right under-side of the board.
- Located in the center of the board is the ISL54210 IC (U1). The evaluation board has a pin 1 dot, to show how the IC should be oriented on to the evaluation board. The IC pin 1 indicator dot needs to be aligned with the evaluation board pin 1 dot indicator.

## Power Supply

The DC power supply connected at banana jacks J10 (VDD) and J11 (GND) provides power to the part. The IC requires a 2.7V to 3.6V DC power supply for proper operation. The power supply should be capable of delivering 100 $\mu$ A of current.

## Logic Control

The state of the ISL54210 device is determined by the voltage at the  $V_{BUS}$  pin and the CTRL pin. These logic pins have internal 4M $\Omega$  pull-down resistors and can be left floating.

Access to the CTRL pin is through the banana jack J12 (CTRL).

Access to the  $V_{BUS}$  pin is through banana jack J13 ( $V_{BUS}$ ) when jumper JP3 is in position 2 to 3 and through the USB J1 (USB TO HOST) connector  $V_{BUS}$  line when the jumper JP3 is in position 2 to 1.

If  $V_{BUS}$  is floating (no USB host controller connected at the J1 connector) and CTRL = High (J12 connected to J10), the part will be in the Audio mode.

If  $V_{BUS}$  is floating (no USB host controller connected at the J1 connector) and CTRL = Low or Floating, the part will be in the MUTE mode. In MUTE mode all switches are OPEN and the click and pop circuitry on the L and R side of the switch is activated.

If  $V_{BUS}$  is greater than  $V_{DD}$  by 0.8V (USB computer  $V_{BUS}$  line connected at the J1 USB connector), the part will be in the USB mode. In a typical application, the  $V_{BUS}$  voltage from computer/host controller will be in the range of 4.4V to 5.25V and the  $V_{DD}$  voltage will be in the range of 2.7V to 3.6V.

When the computer is not connected at J1, the internal pull-down will pull the  $V_{BUS}$  pin low putting the ISL54210 in the Audio mode or Mute mode depending on the condition of the CTRL pin.

When a USB cable from the computer is connected at J1, the voltage at the  $V_{BUS}$  pin will be driven high and the part will automatically go into the USB mode.

When the USB cable is disconnected from the J1 connector, the voltage at the  $V_{BUS}$  pin will be pulled low by the pull-down resistor and return to the Audio mode or Mute mode depending on the condition of the CTRL pin.

Note: The evaluation board has the option of putting a 4M $\Omega$  pull-down resistor from the  $V_{BUS}$  pin to ground by putting a jumper at JP4. However, it is not required for the ISL54210 part because the  $V_{BUS}$  and CTRL pins have internal 4M $\Omega$  pull-down resistors. These resistors pull the logic pins to ground when J13, J1, or J12 are floated.

Rather than using the  $V_{BUS}$  voltage from the computer to control the  $V_{BUS}$  logic pin of the part through USB connector J1 (USB TO HOST), you can drive the  $V_{BUS}$  pin through the banana jack J13 ( $V_{BUS}$ ). To control the part through the J13 banana jack make the following connections: Install jumper

JP3 at location 2 to 3. This connects the J13 banana jack to the  $V_{BUS}$  pin of the part.

## Audio Inputs

The evaluation board has two audio inputs labeled AUDIO LEFT (J3 or J5) and AUDIO RIGHT (J4 or J6). Each of these inputs have a BNC connector wired in parallel with an RCA jack connector. You can connect an audio source/generator at either the BNC connector or the RCA connector.

In a typical application the left and right outputs of a stereo source, such as an MP3 player, would be connect to the RCA jacks J3 and J4, respectfully.

Over a signal range of  $\pm 1V$  (0.707V<sub>RMS</sub>), these switches have an extremely low  $r_{ON}$  resistance variation. They can pass ground referenced audio signals with very low distortion (<0.05% THD+N) when delivering 15.6mW into a 32 $\Omega$  headphone speaker load. A 3V<sub>P-P</sub> signal is acceptable but performance will degrade.

The audio channels are AC coupled with 100 $\mu$ F capacitors. These capacitors remove any DC voltage from the audio signals and reference them to ground of the device. If the audio source being connected to the evaluation board already has internal AC coupling capacitors at the output of its drivers, then the capacitors on the evaluation board should be shorted out (replaced with 0 $\Omega$  resistors). This will prevent further attenuation of the low frequency content of the audio signal due to the increase of the high-pass corner frequency resulting from the evaluation board capacitor in series with the audio source capacitor.

## Audio Outputs

The evaluation board has a standard headphone jack labeled HEADPHONE JACK (J7). A 32 $\Omega$  stereo headphone should be plugged into this jack.

The BNC connector J8 (COM-) is in parallel with the audio left line of the headphone jack and the D- line of the J1 (USB To HOST) connector. The BNC connector J9 (COM+) is in parallel with the audio right line of the headphone jack and the D+ line of the J1 (USB To DEVICE) connector. These BNC connectors can be used to connect resistive loads and/or test equipment to evaluate the performance of the IC.

## USB Connections

A "B" type USB receptacle labeled USB TO HOST (J1) is located on the left under-side of the board. This receptacle should be connected using a standard USB cable to the up-stream USB host controller, which is usually a PC computer or hub. When this connection is made, the ISL54210 device will sense the  $V_{BUS}$  voltage from the host controller and go into USB mode. The USB switches will be turned ON.

An "A" type USB receptacle labeled USB TO DEVICE (J2) is located on the right under-side of the board. The USB device can be plugged directly into this receptacle or through a standard USB cable.

In USB mode, the USB host controller and USB device will be connected through the internal USB switches of the IC. Unlike the audio switches, the USB switches are bi-directional, which allow the host (computer) and downstream USB device to both send and receive data.

Note: Before connecting the USB host controller at connector J1, the headphones must be removed from the headphone speaker jack, J7. This will prevent the audio load from interfering with USB signal transmission. The main purpose of the ISL54210 device is to allow the use of a common USB and Audio Headphone Connector in Personal Media Players and other portable battery powered equipment. Either a headphone will be in the common connector to play music or a USB cable from a computer will be in the common connector to transmit digital data, such as music, into the memory of the Media Player.

The BNC connector J8 (COM-) is in parallel with the D- line of the J1 USB connector. The BNC connector J9 (COM+) is in parallel with the D+ line of the J1 USB connector.

The BNC connector J14 (D-) is in parallel with the D- line of the J2 USB connector. The BNC connector J15 (D+) is in parallel with the D+ line of the J2 USB connector. If you want to use the J14 and J15 BNC connectors, you must populate R1 and R2 with  $0\Omega$  resistors. These BNC connectors can be used to connect test equipment to evaluate the performance of the IC.

### Board Component Definitions

| DESIGNATOR      | DESCRIPTION   |
|-----------------|---|
| <b>U1</b>       | ISL54210IRUZ IC   |
| <b>J1</b>       | "B" type USB Receptacle   |
| <b>J2</b>       | "A" type USB Receptacle   |
| <b>J3, J5</b>   | Left Stereo Audio Input   |
| <b>J4, J6</b>   | Right Stereo Audio Input  |
| <b>J7</b>       | Headphone Speaker Jack  |
| <b>J8</b>       | COM- BNC Connector  |
| <b>J9</b>       | COM+ BNC Connector  |
| <b>J10</b>      | VDD Positive Connection   |
| <b>J11</b>      | VDD Negative Connection   |
| <b>J12</b>      | CTRL Logic Control  |
| <b>J13</b>      | V <sub>BUS</sub> Logic Control  |
| <b>J14</b>      | D- BNC Connector  |
| <b>J15</b>      | D+ BNC Connector  |
| <b>JP1, JP2</b> | D-/D+ Differential Probe Connection   |
| <b>JP3</b>      | V <sub>BUS</sub> Pin Connector Selection<br>Jumper Position 2 - 3 Control through J13<br>Jumper Position 2 - 1 Control through J1 |
| <b>JP4</b>      | Connect 4M $\Omega$ Pull-down Resistor to IN Pin  |

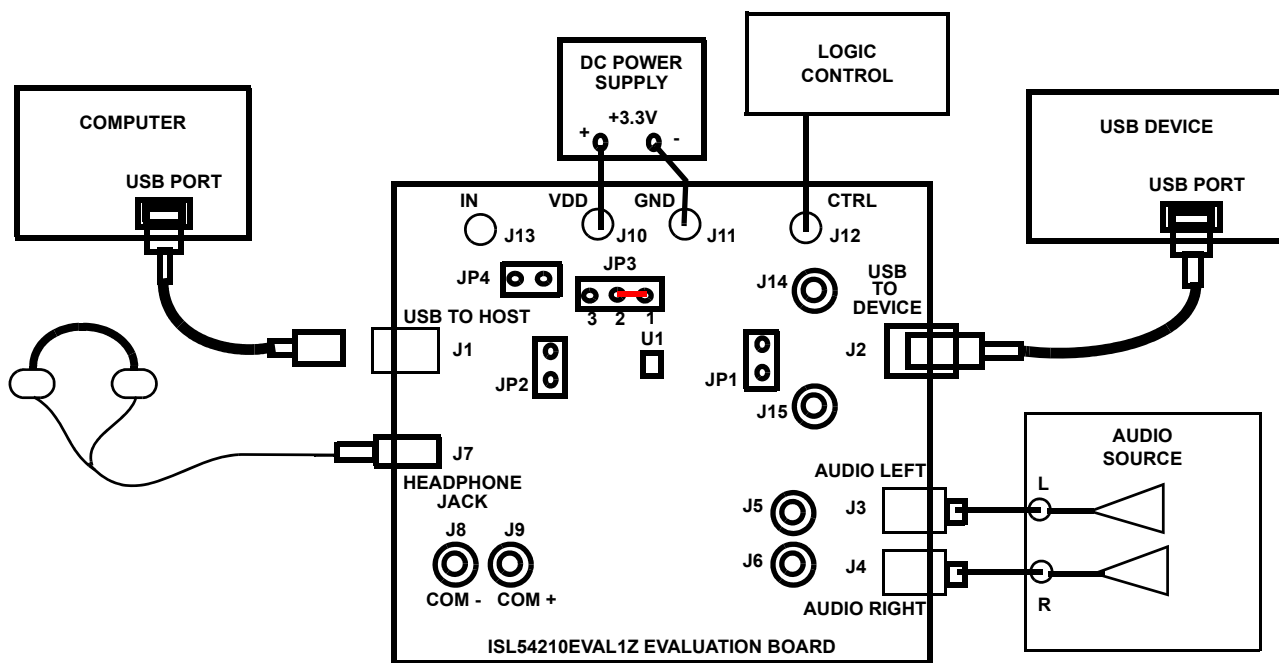


FIGURE 2. BASIC EVALUATION TEST SETUP BLOCK DIAGRAM

## Using The Board (Refer to Figure 2)

### Lab Equipment

The equipment, external supplies and signal sources needed to operate the board are listed in the following:

1. +2.7V to 3.6V DC Power Supply
2. Stereo Audio Source (MP3/Radio/CD Player) or Audio Generator
3. Headphone Set or Audio Analyzer/Oscilloscope
4. Two standard High-speed USB cables
5. Computer with USB port
6. USB device (i.e. USB memory stick, MP3 Player with USB port, etc.)

### Initial Board Setup Procedure

1. Attach the main evaluation board to a DC power supply at J10 (VDD) and J11 (GND). Positive terminal at J10 and negative terminal at J11. The supply should be capable of delivering 2.7V to 3.6V and 100 $\mu$ A of current. Set the supply voltage to 3.3V.
2. Connect the stereo audio source left channel to J3 RCA connector or J5 BNC connector and right channel to J4 RCA connector or J6 BNC connector.
3. Connect the USB device to the USB "B" type receptacle, J2 (USB TO DEVICE).
4. Install a jumper at position 2 to 1 of JP3. This allows the  $V_{BUS}$  voltage from the computer to drive the  $V_{BUS}$  pin of the IC. No jumper required at JP4.

### Audio Mode Operation

1. Install headphone in the headphone jack, J7. No USB cable should be connected at the J1 (USB TO HOST) connector.
2. Connect J12 (CTRL) to J10 (VDD). You will now be in Audio mode (2.4 $\Omega$  audio switches are ON).
3. Turn the audio source ON. You should hear the music in the headphones.
4. You can observe the audio signals at the output of the IC on an oscilloscope or audio analyzer by using the J8 and J9 BNC connectors. Connect to J8 for left audio and J9 for right audio.

### USB Mode Operation

1. Turn OFF audio stereo source.
2. Remove headphone from J7 Headphone Jack.
3. Connect USB cable from host (PC computer) to the USB "A" type receptacle, J1 (USB TO HOST). You will now be in USB mode (5.4 $\Omega$  USB switches are ON).
4. You should now be able to send and receive data between the host and USB device.
5. You can observe the D- and D+ USB digital signals on an oscilloscope or other test equipment by connecting to the J8, J14, J9 and J15 BNC connectors. Connect to J8/J14 for D- signal and to J9/J15 for D+ signal.
6. Remove the 0 $\Omega$  resistors R1, R2, R3, R4 when evaluating the performance (i.e. eye diagram, etc.) of the high speed USB signal. Removal of these resistors disconnects the headphone jack (J7) and BNC connectors (J8, J9, J14, J15) from the USB differential lines. These components when connected will degrade the USB signal. These components will not be there in an actual application.

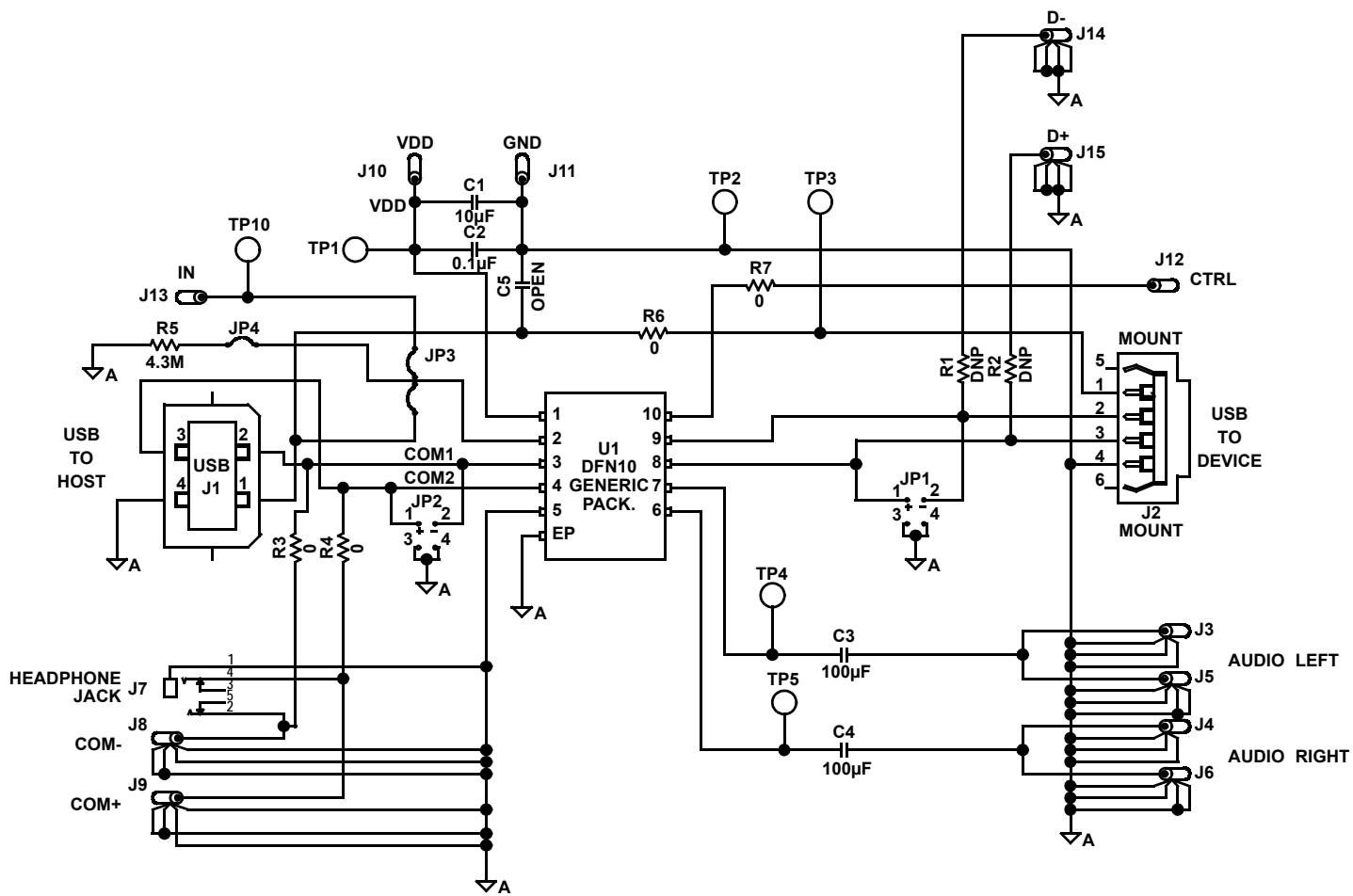
If you disconnect the USB cable from J1, the IC will return to Audio mode. You can now plug the headphone set into the headphone jack. Turn the audio source ON to once again play music.

### ***Test Points***

The board has various test points for ease of connecting probes to make measurements. The test points available are described in the following table.

| DESIGNATOR | DESCRIPTION   |
|------------|---|
| <b>TP1</b> | V <sub>DD</sub> test point                                      |
| <b>TP2</b> | Ground Test Point   |
| <b>TP3</b> | V <sub>BUS</sub> Test Point                                     |
| <b>TP4</b> | Audio Left Input Test Point                                     |
| <b>TP5</b> | Audio Right Input Test Point                                    |
| <b>JP1</b> | D-/D+ Differential Probe Connection - USB Device Side of Switch |
| <b>JP2</b> | D-/D+ Differential Probe Connection - COM Side of Switch        |

## Board Schematic





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