2N2326A

SILICON CONTROLLED RECTIFIER 1.6 AMP, 400 VOLT

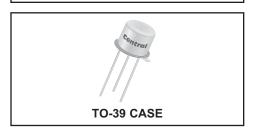


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DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N2326A is a hermetically sealed silicon controlled rectifier designed for sensing circuit applications and control systems.





MAXIMUM RATINGS: (T_C=25°C unless otherwise noted) SYMBOL

	SYMBOL		UNITS	
Peak Repetitive Forward Voltage	V_{DRM}	200	V	
Peak Repetitive Reverse Voltage	V_{RRM}	200	V	
Non-Repetitive Peak Reverse Voltage	V_{RSM}	300	V	
RMS On-State Current	I _T (RMS)	1.6	Α	
Average On-State Current (T _C =85°C)	I _{T(AV)}	1.0	Α	
Peak One Cycle Surge Current (t=8.3ms)	ITSM	15	Α	
Peak Gate Power Dissipation	P_{GM}	100	mW	
Average Gate Power Dissipation	P _{G(AV)}	10	mW	
Peak Gate Current	I _{GM}	100	mA	
Peak Gate Voltage	V_{GM}	6.0	V	
Operating Junction Temperature	T_J	-65 to +125	°C	
Storage Temperature	T _{sta}	-65 to +150	°C	

ELECTRICAL CHARACTERISTICS: (T_C=25°C unless otherwise noted)

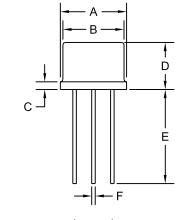
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
IDRM	V_{DRM} =200V, R_{GK} =2.0K Ω		5.0	μΑ
IRRM	V_{RRM} =200V, R_{GK} =2.0K Ω		5.0	μΑ
I_{GT}	V_D =6.0V, R_L =100 Ω		20	μΑ
lΗ	V_D =6.0V, R_{GK} =2.0K Ω		2.0	mA
V_{GT}	V_D =6.0V, R_L =100 Ω		0.6	V
V_{TM}	I _T =1.0A, tp=380μs		1.5	V

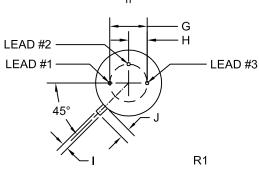
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TO-39 CASE - MECHANICAL OUTLINE





DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A (DIA)	0.335	0.370	8.51	9.40			
B (DIA)	0.315	0.335	8.00	8.51			
С	-	0.040	-	1.02			
D	0.240	0.260	6.10	6.60			
E	0.500	-	12.70	-			
F (DIA)	0.016	0.021	0.41	0.53			
G (DIA)	0.200		5.08				
Н	0.100		2.54				
	0.028	0.034	0.71	0.86			
J	0.029	0.045	0.74	1.14			

TO-39 (REV: R1)

LEAD CODE:

- 1) Cathode
- 2) Gate
- 3) Anode (case)

MARKING: FULL PART NUMBER

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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