

SURFACE MOUNT 0.8 AMP SILICON SCR 600 THRU 800 VOLTS





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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CS89M series type is an Epoxy Molded Silicon Controlled Rectifier designed for sensing circuit applications and control systems.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T _C =25°C unless otherwise noted)	SYMBOL	<u>CS89M</u>	<u>CS89N</u>	UNITS
Peak Repetitive Off-State Voltage	V _{DRM,} V _{RRM}	600	800	V
RMS On-State Current (T _C =60°C)	IT(RMS)	0	.8	А
Peak One Cycle Surge, t=10ms	ITSM	1	0	А
I ² t Value for Fusing, t=10ms	l ² t	0.	24	A ² s
Peak Gate Power, tp=10µs	PGM	2	.0	W
Average Gate Power Dissipation	PG(AV)	0	.1	W
Peak Gate Current, tp=10µs	IGM	1	.0	А
Peak Gate Voltage, tp=10µs	V _{GM}	8	.0	V
Operating Junction Temperature	ТJ	-40 to	+125	°C
Storage Temperature	T _{stg}	-40 to	+150	°C
Thermal Resistance	Θ_{JA}	1(04	°C/W
ELECTRICAL CHARACTERISTICS: (T _C =25°C unless other	erwise noted)			

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS		
I _{DRM} , I _{RRM}	Rated V _{DRM} , V _{RRM} , R _{GK} =1KΩ			1.0	μA		
I _{DRM,} I _{RRM}	Rated V _{DRM} , V _{RRM,} R _{GK} =1KΩ, T _C =125°C			100	μA		
I _{GT}	V _D =12V		20	200	μA		
IН	R _{GK} =1KΩ		0.25	5.0	mA		
V _{GT}	V _D =12V		0.61	0.8	V		
V_{TM}	I _{TM} =1.0A, tp=380µs		1.2	1.7	V		
dv/dt	V _D = ² / ₃ V _{DRM} , R _{GK} =1KΩ, T _C =125°C	25			V/µs		

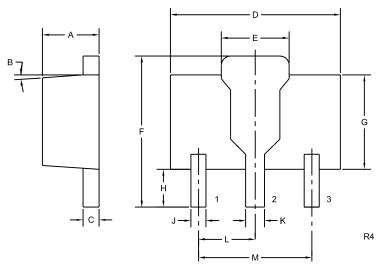
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SOT-89 CASE - MECHANICAL OUTLINE



(Bottom View)

I FAD	CODE:
	OODL.

- 1) Gate
- 2) Anode 3) Cathode
- MARKING: FULL PART NUMBER

DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
А	0.055	0.067	1.40	1.70	
В	4°		4°		
С	0.014	0.018	0.35	0.46	
D	0.173	0.185	4.40	4.70	
E	0.064	0.074	1.62	1.87	
F	0.146	0.177	3.70	4.50	
G	0.090	0.106	2.29	2.70	
Н	0.028	0.051	0.70	1.30	
J	0.014	0.019	0.36	0.48	
K	0.017	0.023	0.44	0.58	
L	0.059		1.50		
М	0.118		3.00		
SOT-89 (REV: R4)					

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- · Up-screening capabilities

· Custom product packing

Custom bar coding for shipments

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when 1. ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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