CTLZ3V9 THRU CTLZ8V2

SURFACE MOUNT SILICON ZENER DIODES



FEATURES:

• Ultra small, ultra low profile 0.3mm x 0.6mm x 0.3mm TLP[™] leadless surface mount package.

MAXIMUM RATINGS: (T_A=25°C)

Power Dissipation

Operating and Storage Junction Temperature



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CTLZ3V9 thru CTLZ8V2 silicon Zener diodes are high quality voltage regulators, each packaged in the ultra small, ultra low profile TLM2D3D6 leadless surface mount case.

MARKING CODE: SEE MARKING CODES ON ELECTRICAL CHARACTERISTICS TABLE

APPLICATIONS:

- Reference voltage
- Voltage regulation
- Over-voltage protection
- Medical electronics
- Handheld devices such as mobile phones and MP3 players.

SYMBOL		UNITS
PD	100	mW
T _J , T _{stg}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C) V_F=0.9V MAX @ I_F=10mA (for all types)

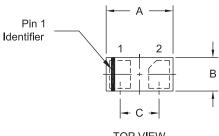
TYPE	ZENER VOLTAGE TYPE V _Z @ I _{ZT}		TEST CURRENT	MAXIMUM REVERSE LEAKAGE CURRENT I _R @ V _R		MARKING CODE	
	MIN V	NOM V	MAX V	mA	μΑ	v	
CTLZ3V9	3.74	3.9	4.16	5.0	5.0	1.0	F
CTLZ4V7	4.42	4.7	4.90	5.0	2.0	1.0	Н
CTLZ5V1	4.84	5.1	5.37	5.0	2.0	1.5	К
CTLZ5V6	5.31	5.6	5.92	5.0	1.0	2.5	L
CTLZ6V2	5.86	6.2	6.53	5.0	1.0	3.0	Ν
CTLZ6V8	6.47	6.8	7.14	5.0	0.5	3.5	Р
CTLZ7V5	7.06	7.5	7.84	5.0	0.5	4.0	Т
CTLZ8V2	7.76	8.2	8.64	5.0	0.5	5.0	U

CTLZ3V9 THRU CTLZ8V2

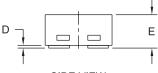
SURFACE MOUNT SILICON ZENER DIODES

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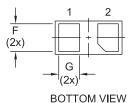
TLM2D3D6 CASE - MECHANICAL OUTLINE







SIDE VIEW



R2

DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
А	0.022	0.026	0.55	0.65			
В	0.010	0.014	0.25	0.35			
С	0.014		0.35				
D	0.000	0.002	0.00	0.05			
E	0.011	0.013	0.28	0.32			
F	0.008	0.012	0.20	0.30			
G	0.005	0.009	0.13	0.24			
TLM2D3D6 (REV: R2)							

LEAD CODE:

1) Cathode

2) Anode

MARKING CODE: SEE MARKING CODES ON ELECTRICAL CHARACTERISTICS TABLE

OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA Main Tel: (631) 435-1110 Main Fax: (631) 435-1824 Support Team Fax: (631) 435-3388 www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

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