CMLT7820G

SURFACE MOUNT SILICON VERY LOW V_{CE(SAT)} PNP TRANSISTOR



APPLICATIONS:

- DC/DC Converters
- Voltage Clamping
- Protection Circuits
- Battery powered Cell Phones, Pagers, Digital Cameras, PDAs, Laptops, etc.

SOT-563 CASE

Central Semiconductor Corp.

www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT7820G is a very low $V_{\text{CE}(\text{SAT})}$ PNP Transistor, designed for applications where small size and efficiency are the prime requirements. Packaged in a space saving SOT-563 surface mount package, this component provides performance characteristics suitable for the most demanding size constrained applications.

MARKING CODE: 78G

FEATURES:

- Device is Halogen Free by design
- High Current (IC=1.0A)
- VCE(SAT)=0.34V MAX @ IC=1.0A
- SOT563 surface mount package
- Complementary NPN device CMLT3820G

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V _{CBO}	80	V
Collector-Emitter Voltage	VCEO	60	V
Emitter-Base Voltage	V _{EBO}	5.0	V
Continuous Collector Current	I _C	1.0	Α
Peak Collector Current	ICM	2.0	Α
Continuous Base Current	lΒ	300	mA
Power Dissipation	P_{D}	250	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	ΘιΔ	500	°C/W

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =60V		100	nA
I _{EBO}	V _{EB} =5.0V		100	nA
BV _{CBO}	I _C =100μA	80		V
BV CEO	I _C =10mA	60		V
BVEBO	I _E =100μA	5.0		V
V _{CE} (SAT)	I _C =100mA, I _B =1.0mA		0.175	V
VCE(SAT)	I _C =500mA, I _B =50mA		0.18	V
VCE(SAT)	I _C =1.0A, I _B =100mA		0.34	V
V _{BE} (SAT)	I _C =1.0A, I _B =50mA		1.1	V
V _{BE} (ON)	V_{CE} =5.0V, I_{C} =1.0A		0.9	V
h _{FE} ` ´	V_{CE} =5.0V, I_{C} =1.0mA	200		
hFE	V_{CE} =5.0V, I_{C} =500mA	150		
h _{FE}	V_{CE} =5.0V, I_{C} =1.0A	100		
f_{T}	V_{CE} =10V, I_{C} =50mA	150		MHz
C _{ob}	V_{CB} =10V, I_E =0, f=1.0MHz		15	pF

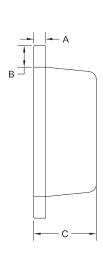
R4 (29-June 2015)

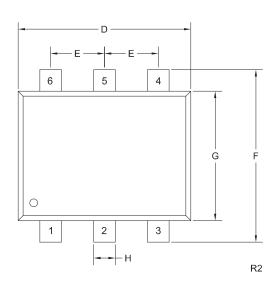
CMLT7820G

SURFACE MOUNT SILICON VERY LOW V_{CE(SAT)} PNP TRANSISTOR

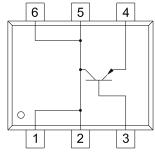


SOT-563 CASE - MECHANICAL OUTLINE





PIN CONFIGURATION



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.0027	0.007	0.07	0.18			
В	0.008		0.20				
С	0.017	0.024	0.45	0.60			
D	0.059	0.067	1.50	1.70			
E	0.020		0.50				
F	0.059	0.067	1.50	1.70			
G	0.043	0.051	1.10	1.30			
Н	0.006	0.012	0.15	0.30			
SOT-563 (REV: R2)							

LEAD CODE:

- 1) Collector
- 2) Collector
- 3) Base
- 4) Emitter
- 5) Collector
- 6) Collector

Pins 1, 2, 5 and 6 are common.

MARKING CODE:78G

R4 (29-June 2015)

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- · Consolidated shipping options

- · Custom bar coding for shipments
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- · Application notes
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