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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMXD2004TO consists of three (3) Isolated High Voltage Silicon Switching Diodes arranged in an alternating configuration in a SUPERmini SOT-26 surface mount package, and designed for high voltage switching applications. This device can be configured as a 900V switching diode. See optional mounting pad configuration.

MARKING CODE: X04TO

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Continuous Reverse Voltage	V _R	240	V
Peak Repetitive Reverse Voltage	V _{RRM}	300	V
Average Forward Current	IO	200	mA
Continuous Forward Current	١ _F	225	mA
Peak Repetitive Forward Current	IFRM	625	mA
Peak Forward Surge Current, tp=1.0µs	IFSM	4.0	А
Peak Forward Surge Current, tp=1.0s	IFSM	1.0	А
Power Dissipation	PD	350	mW
Operating and Storage Junction Temperature	TJ, T _{stg}	-65 to +150	°C
Thermal Resistance	ΘJΑ	357	°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted)

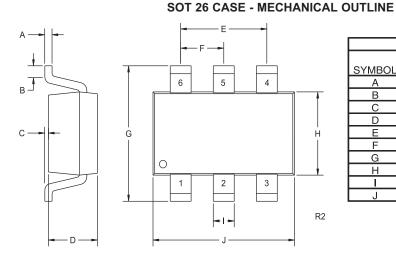
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SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
IR	V _R =240V		100	nA
I _R	V _R =240V, T _A =150°C		100	μA
BVR	I _R =100μΑ	300		V
VF	I _F =100mA		1.0	V
CT	V _R =0, f=1.0MHz		5.0	pF
t _{rr}	$I_F = I_R = 30 \text{mA}, I_{rr} = 3.0 \text{mA}, R_L = 100 \Omega$		50	ns

R2 (12-February 2010)



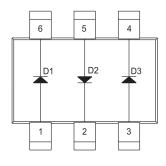
CMXD2004TO

SURFACE MOUNT TRIPLE ISOLATED OPPOSING **HIGH VOLTAGE** SILICON SWITCHING DIODES



DIMENSIONS INCHES MILLIMETERS SYMBOL MIN MIN MAX MAX А 0.004 0.007 0.11 0.19 В 0.016 0.40 --0.10 0.004 С --D 0.039 0.047 1.00 1.20 Е 1.88 1.92 0.074 0.075 F 0.037 0.038 0.93 0.97 G 0.102 0.118 2.60 3.00 Н 0.059 0.067 1.50 1.70 Т 0.016 0.41 0.110 0.118 2.80 3.00 J SOT-26 (REV: R2)

PIN CONFIGURATION

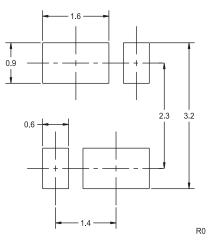


LEAD CODE:

- 1) Anode D1
- 2) Cathode D2
- 3) Anode D3
- 4) Cathode D3 5) Anode D2
- 6) Cathode D1

MARKING CODE: X04TO

OPTIONAL MOUNTING PADS For 900V Series Configuration (Dimensions in mm)



R2 (12-February 2010)

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