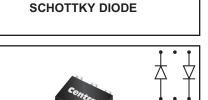
SURFACE MOUNT SILICON DUAL, ISOLATED OPPOSING



SOT-563 CASE



www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLSH-4DO are two individual electrically isolated 40 volt Schottky diodes of opposing polarity, in a space saving SOT-563 surface mount package. This device has been designed for applications requiring fast switching speeds and a low forward voltage drop.

MARKING CODE: L40

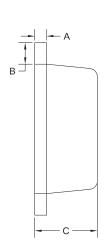
MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	40	V
Continuous Forward Current	l _F	200	mA
Peak Repetitive Forward Current	I _{FRM}	350	mA
Peak Forward Surge Current, tp=10ms	I _{FSM}	750	mA
Power Dissipation	P_{D}	250	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	500	°C/W

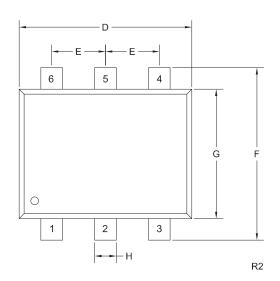
ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted) SYMBOL **TEST CONDITIONS** UNITS **TYP** MAX $V_R = 25V$ 90 500 nΑ I_R μΑ V_R=25V, T_A=100°C 25 100 I_R BV_{R} I_R=100μA 40 50 V I_F=2.0mA 0.29 0.33 ٧ V_{F} I_F=15mA 0.37 0.42 V_{F} V_{F} I_F=100mA 0.51 0.80 V_{F} I_F=200mA 0.65 1.0 ٧ V_R=1.0V, f=1.0MHz 7.0 C_{J} pF $I_F = I_R = 10$ mA, $I_{rr} = 1.0$ mA, $R_L = 100$ Ω 5.0 ns t_{rr}

SURFACE MOUNT SILICON DUAL, ISOLATED OPPOSING SCHOTTKY DIODE

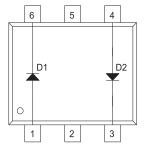


SOT-563 CASE - MECHANICAL OUTLINE





PIN CONFIGURATION



DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.0027	0.007	0.07	0.18		
В	0.008		0.20			
С	0.017	0.024	0.45	0.60		
D	0.059	0.067	1.50	1.70		
E	0.020		0.50			
F	0.059	0.067	1.50	1.70		
G	0.043	0.051	1.10	1.30		
Н	0.006	0.012	0.15	0.30		
SOT-563 (REV: R2)						

LEAD CODE:

- 1) Anode D1
- 2) NC
- 3) Cathode D2
- 4) Anode D2
- 5) NC
- 6) Cathode D1

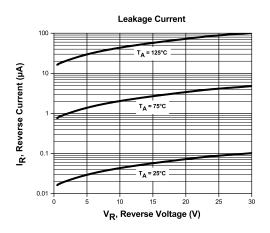
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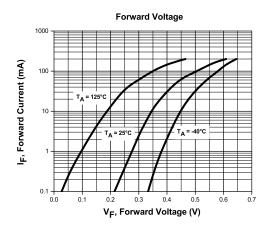
R6 (16-February 2016)

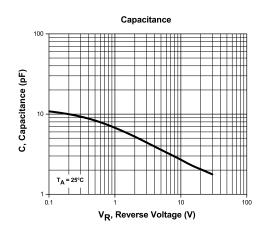


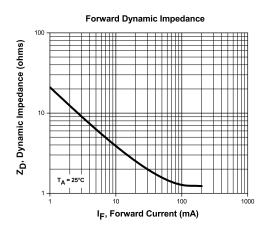


TYPICAL ELECTRICAL CHARACTERISTICS





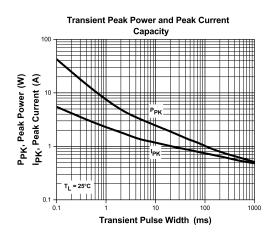


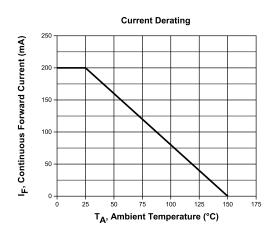


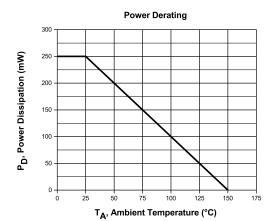




TYPICAL ELECTRICAL CHARACTERISTICS







OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- · Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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