Data Sheet



2N4404 2N4405

PNP SILICON TRANSISTOR

JEDEC TO-39 CASE

Manufacturers of World Class Discrete Semiconductors

DESCRIPTION

The CENTRAL SEMICONDUCTOR 2N4404, 2N4405 types are PNP Silicon Epitaxial Planar Transistors designed for general purpose and switching applications.

<u>MAXIMUM RATINGS</u> ($T_C = 25^{\circ}C$)

	<u>SYMBOL</u>		<u>UNITS</u>
Collector-Base Voltage	VCBO	80	V
Collector-Emitter Voltage	V _{CEO}	80	v
Emitter-Base Voltage	V _{EBO}	5.0	V
Collector Current	Чс	1.0	Α
Power Dissipation ($T_A = 25^{\circ}C$)	PD	1.25	W
Power Dissipation	PD	8.75	W
Operating and Storage			
Junction Temperature	T _J ,T _{stg}	-65 to +200	٥C
Thermal Resistance	Θ_{JA}	140	°C/W
Thermal Resistance	θ _{JC}	25	°C/W

<u>ELECTRICAL CHARACTERISTICS</u> ($T_C = 25^{\circ}C$ unless otherwise noted)

		<u>2N4</u>	<u>4404</u>		<u>2N4</u>	405	
SYMBOL	TEST CONDITIONS	MIN	<u>MAX</u>		<u>MIN</u>	<u>MAX</u>	<u>UNITS</u>
ICBO	V _{CB} =60V		25			25	nA
I _{EBO}	V _{EB} =3.0V		25			25	nA
BV _{CEO}	I _C = 10mA	80			80		V
BV _{CBO}	$I_{C} = 10\mu A$	80			80		V
BVEBO	$I_{E} = 10 \mu A$	5.0			5.0		v
V _{CE} (SAT)	$l_{c} = 10 mA$, $l_{B} = 1.0 mA$		0.15			0.15	V
V _{CE} (SAT)	$I_{C} = 150 \text{mA}, I_{B} = 15 \text{mA}$		0.2			0.2	V
VCE(SAT)	I _C = 500mA, I _B = 50mA		0.5			0.5	V
VBE(SAT)	I _C = 10mA, I _B = 1.0mA		0.8			0.8	V
VBE(SAT)	I _C = 500mA, I _B = 50mA	0.85	1.2		0.85	1.2	V
V _{BE} (ON)	$V_{CE} = 1.0V, I_{C} = 150mA$		0. 9			0.9	V
hFE	$V_{CE} = 5.0V, I_{C} = 0.1 \text{mA}$		30		75		
h _{FE}	$V_{CE} = 5.0V, I_{C} = 10mA$		40		100		
h _{FE}	$V_{CE} = 5.0V, I_{C} = 150mA$		40	120	100	300	
h _{FE}	$V_{CE} = 5.0V, I_{C} = 500mA$		30		50		

ELECTRICAL CHARACTERISTICS (Continued)

	MHz
f_{T} $V_{CE} = 20V, I_{C} = 50 \text{mA}, f = 100 \text{MHz}$ 150 600	
C_{cb} $V_{CB} = 10V, I_E = 0, f = 1.0MHz$ 20	рF
C_{eb} $V_{BE} = 0.5V, I_{C} = 0, f = 1.0MHz$ 110	pF
$V_{CC} = 30V, V_{BE(off)} = 2.0V, I_C = 500mA, I_{B1} = 50mA$ 40	ns
t_r $V_{CC} = 30V, V_{BE(off)} = 2.0V, I_C = 500mA, I_{B1} = 50mA$ 60	ns
$V_{CC} = 30V, I_C = 500 \text{mA}, I_{B1} = I_{B2} = 50 \text{mA}$ 350	ns
$v_{CC} = 30V, I_C = 500mA, I_{B1} = I_{B2} = 50mA$ 50	ns



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Central's applications engineering team is ready to discuss your design challenges. Just ask.

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- Customer specific screening
- · Up-screening capabilities

· Custom product packing

Custom bar coding for shipments

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when 1. ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

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