

PCI 9030RDK-LITE

PCI SMARTarget™ Rapid Development Kit

Features

- A PCI v2.2 prototype adapter board design based on the PLX PCI 9030 SMARTarget I/O Accelerator
- Surface mount footprints on the board support industry standard embedded CPUs, DSPs, FPGAs, CPLDs and other devices
- ISA connector footprint
- 25 x 25 0.1" through-hole grid space for expansion purposes
- PLX SDK-LITE with PLXMon, a comprehensive GUI tool for debugging, configuration and code download
- Complete Schematics, Gerber files, BOM, PLXMon 2000 Manual, Hardware Reference Manual and PCI 9030 Data Book on CD-ROM
- 100-pin PLX Option Module (POM) connector
- One sample PCI 9030 chip

The PLX PCI 9030RDK-LITE (RDK-LITE) simplifies the design process by providing a complete development environment for PCI embedded application designs.

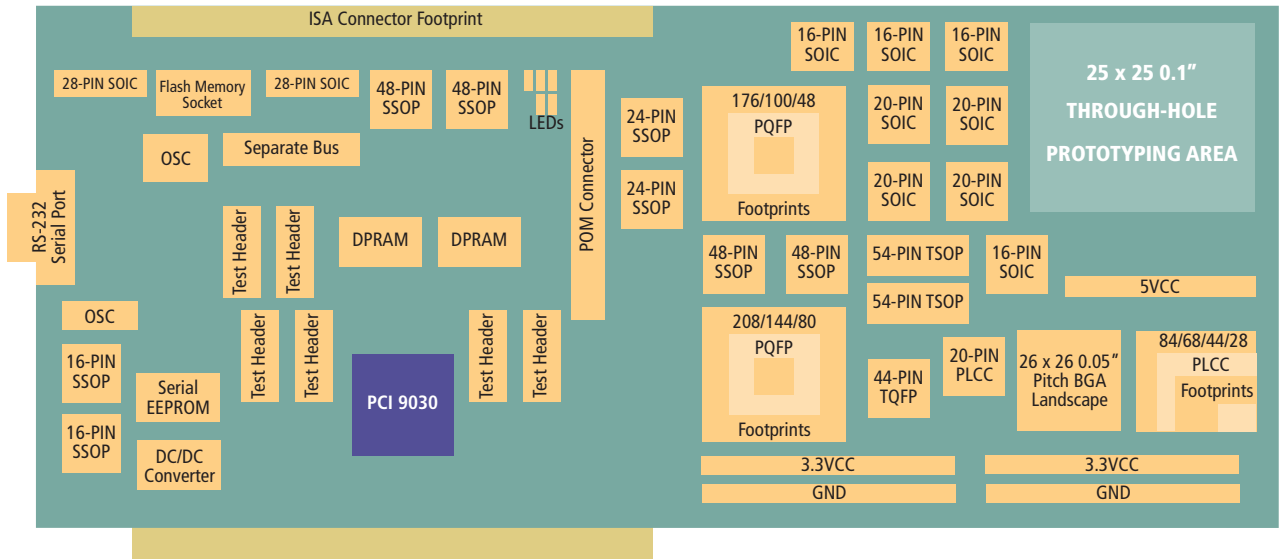
At the heart of the PCI 9030RDK-LITE is the PLX PCI 9030 SMARTarget™ I/O Accelerator which supports a 32-bit, 33MHz PCI bus and a 60MHz local bus. It provides a complete PCI v2.2 specification implementation enabling PCI bus burst transfers up to 132 Mbytes/second, local bus burst transfer up to 240 Mbytes/second, and is the industry's first CompactPCI Hot Swap Ready Target device. The PCI 9030 incorporates many advanced features including PCI Target Read Ahead Mode, PCI Target programmable Burst, PCI Target Delayed Write, Posted Memory Writes, Nine (9) programmable General Purpose I/Os (GPIOs), and Four (4) Programmable Chip Selects.

The RDK-LITE comes with the PCI SDK-LITE which includes PCI host APIs, Windows drivers, PLXMon debug tool, manufacturing test program and comprehensive documentation. This extensive support allows customers to quickly and easily add PCI to a variety of embedded applications in telecommunications, data communications, and others.

There are various sizes of BGA and QFP footprints on the PCI board, supporting most industry standard embedded CPU, DSP, FPGA, CPLD and other devices, including IBM PowerPC, Motorola MPC & ColdFire, Hitachi MCU & MPU, TI DSP, Analog Devices DSP and many others. The ISA connector footprint provides an option for customers who want to use the PCI 9030 to connect ISA and PCI together. Also there is a standard 0.1" through-hole grid for expansion purposes.



PCI 9030RDK-LITE Block Diagram



PCI 9030RDK-LITE

Feature	Description
PCI Bus Interface	PLX PCI 9030 SMARTarget I/O Accelerator
PCI Bus Speed	33 MHz Max
Local Bus Speed	60 MHz Max
Through Hole Matrices	Support BGA 0.050" pitch up to 26x26
Prototype Footprints	0.8mm pitch: 44-pin PQFP, 54-pin TSOP(2) 0.5mm pitch: 208-pin PQFP, 176-pin PQFP, 144-pin PQFP, 100-pin TQFP, 80-pin TQFP, 48-pin PQFP 0.05" pitch: 84-pin PLCC, 68-pin PLCC, 44-pin PLCC, 32-pin PLCC, 28-pin PLCC, 28-pin SOIC wide (2), 20-pin PLCC, 20-pin SOIC wide(4), 16-pin SOIC(4) narrow, 14-pin SOIC(2) 0.025" pitch: 48-pin SSOP(4), 24-pin SSOP(2), 16-pin SSOP(2) ISA Connector Footprint
EEPROM Socket	Allows custom configuration of PCI 9030
Dual Port SRAM	Up to 8Kx32 (8Kx16 installed)
RS232 Serial Port	Used for debugging or code downloading
Logic Analyzer Test Header	Six (6) 2x10-pin HP format
POM Connector	Used for PLX Option Modules
25 x 25 0.1" Through Hole Grid Space	Easy to add other devices on board
PLX SDK-LITE	Windows host side Software Development Kit with PLXMon debug tool
PCI 9030HDK CD-ROM	Hardware Reference Manual, Schematics, Gerber Files, BOM, Data Book, Product Brief on one CD-ROM
PLX PCI 9030 chip	One sample chip



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Product Ordering Information

Part Number	Description
PCI 9030-AA60PI	PCI 9030 SMARTarget I/O Accelerator Chip (PQFP)
PCI 9030-AA60BI	PCI 9030 SMARTarget I/O Accelerator Chip (μ BGA)
PCI 9030RDK-LITE	PCI 9030RDK-LITE Reference Design Kit
CompactPCI 9030RDK-LITE	CompactPCI 9030RDK-LITE Reference Design Kit
PLX SDK-LITE	Windows host side Software Development Kit with PLXMon debug tool

Please visit the PLX Web site at <http://www.plxtech.com> or contact PLX sales at 408-774-9060 for pricing and samples.

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