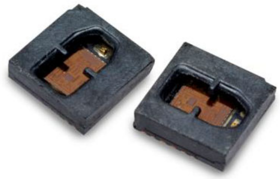
BROADCOM®

Data Sheet

AEDR-9820

Three Channels Reflective Incremental Encoders Analog and Digital Output (225 LPI)



Description

The Broadcom® AEDR-9820 is a three-channel reflective optical encoder. It can be configured to analog or digital outputs employing reflective technology for motion control purposes. The selectable options available are two channels differential analog with a third channel differential digital or analog index output or three-channel digital differential A, B, and I output.

The AEDR-9820 in analog encoder modes, with two-channel differential analog outputs (Sin, /Sin, Cos, /Cos), can be interfaced directly with external interpolators available.

The AEDR-9820 in digital encoder mode offers two-channel (AB) quadrature digital outputs and a third channel digital index output. Being TTL compatible, the outputs of the AEDR-9820 encoder can be interfaced with most of the signal processing circuitries. Therefore, the encoder provides easy integration and flexible design into existing systems.

The AEDR-9820 encoder is designed to operate over –40°C to 115°C temperature range and is suitable for commercial, industrial, and automotive end applications.

The encoder houses an infrared LED light source and a photodetecting circuitry in a single package. The small size of 4.00 mm (L) × 4.00 mm (W) × 1.05 mm (H) allows it to be used in a wide range of miniature commercial applications, where size and space are primary concerns.

Features

- Analog Output option – Two-channel differential analog output and differential digital or analog index output.
- Digital Output option – Three-channel differential or TTL compatible; two-channel quadrature (AB) digital outputs for direction sensing and a third channel, index digital output.
- Built in interpolator for 1x, 2x, 4x, 8x, and 16x interpolation.
- Surface mount leadless package – 4.0 mm (L) × 4.0 mm (W) × 1.05 mm (H).
- Operating voltage of 3.3V and 5.0V supply.
- Built-in LED current regulation.
- Wide operating temperature range from –40°C to 115°C.
- High encoding resolution: 225 LPI (lines/in.) or 8.86 LPmm (lines/mm).

Applications

- Closed-loop stepper motors
- Small motors, actuators
- Industrial printers
- Robotics
- Card readers
- Pan-tilt-zoom camera
- Portable medical equipment
- Optometric equipment
- Linear stages

Output Waveforms

Analog Output Option

Figure 1: Analog Output Option

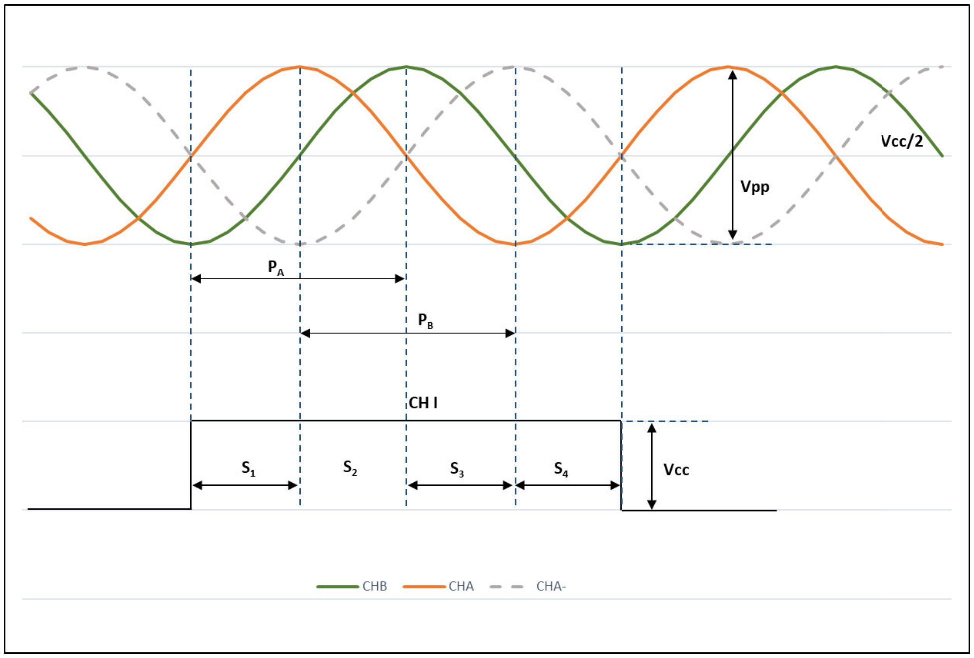
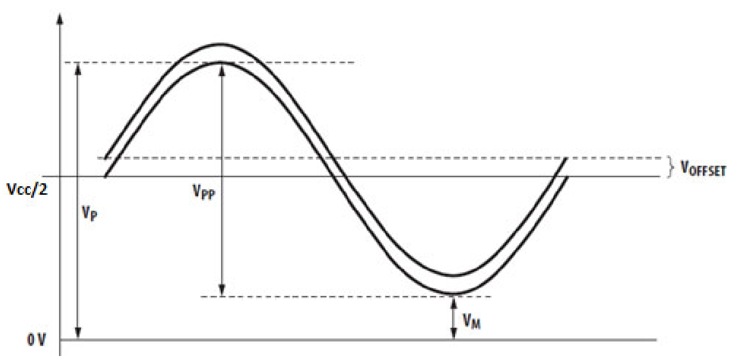


Figure 2: Code Wheel Rotation Movement (Counter-clockwise)

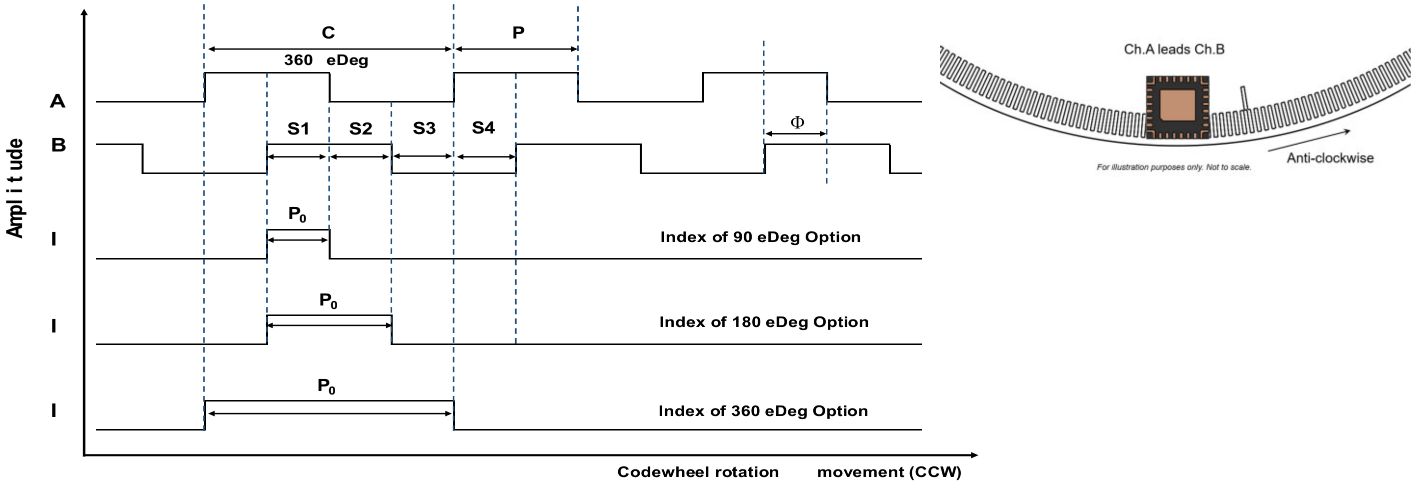


Test Parameter Definitions

Analog Peak-to-Peak	Vpp	The peak-to-peak signal magnitude in V of the analog signal.
Analog Offset	V_{OFFSET}	The offset in mV from the mid-point of the analog peak-to-peak signal to the zero voltage point.
Analog Peak/Valley Voltage	V_{PA}, V_{PB} V_{MA}, V_{MB}	The value in V of the peak/valley of the analog signal (that is, one-sided reading).
Analog Peak to Peak Voltage	V_{PPA}, V_{PPB}	The absolute difference between V_P and V_M of channel A or B.

Digital Output Option

Figure 3: Digital Output Option



Quadrature Signals A,B and I

Digital Parameter Definitions

Count	N	The number of bar and window pairs or counts per revolution (CPR) of the code wheel.
Cycle	C	360 electrical degrees (°e), 1 bar and window pair. One shaft rotation: 360 mechanical degrees, N cycles.
Cycle Error	ΔC	An indication of cycle uniformity. The difference between an observed shaft angle that gives rise to one electrical cycle, and the nominal angular increment of 1/N of a revolution.
Pulse Width (Duty) Error	ΔP	The deviation, in electrical degrees, of the pulse width from its ideal value of 180°e.
State	S	The number of electrical degrees between a transition in the output of channel A and the neighboring transition in the output of channel B. There are four states per cycle, each nominally 90°e.
Phase	φ	The number of electrical degrees between the center of the high state of channel A and the center of the high state of channel B. This value is nominally 90°e for quadrature output.
Optical Radius	ROP	The distance from the code wheel's center of rotation to the optical center (O.C.) of the encoder module.
Index Pulse Width	P_0	The number of electrical degrees that an index is high in one cycle.

Absolute Maximum Ratings

Description and Symbol	Units
Storage Temperature, T_S	–40°C to 125°C
Operating Temperature, T_A	–40°C to 115°C
Supply Voltage, V_{CC}	7V

NOTE:

1. Proper operation of the encoder cannot be guaranteed if the maximum ratings are exceeded.
2. Remove the kapton tape only after the SMT reflow process and just before final assembly. Take precautions to keep the encoder ASIC clean at all times.
3. Some particles may be present on the surface of the encoder ASIC surface. The presence of these particles do not degrade the performance of the encoder.

CAUTION! Take anti-static discharge precautions when handling the encoder to avoid damage, degradation, or both, induced by ESD.

Recommended Operating Conditions

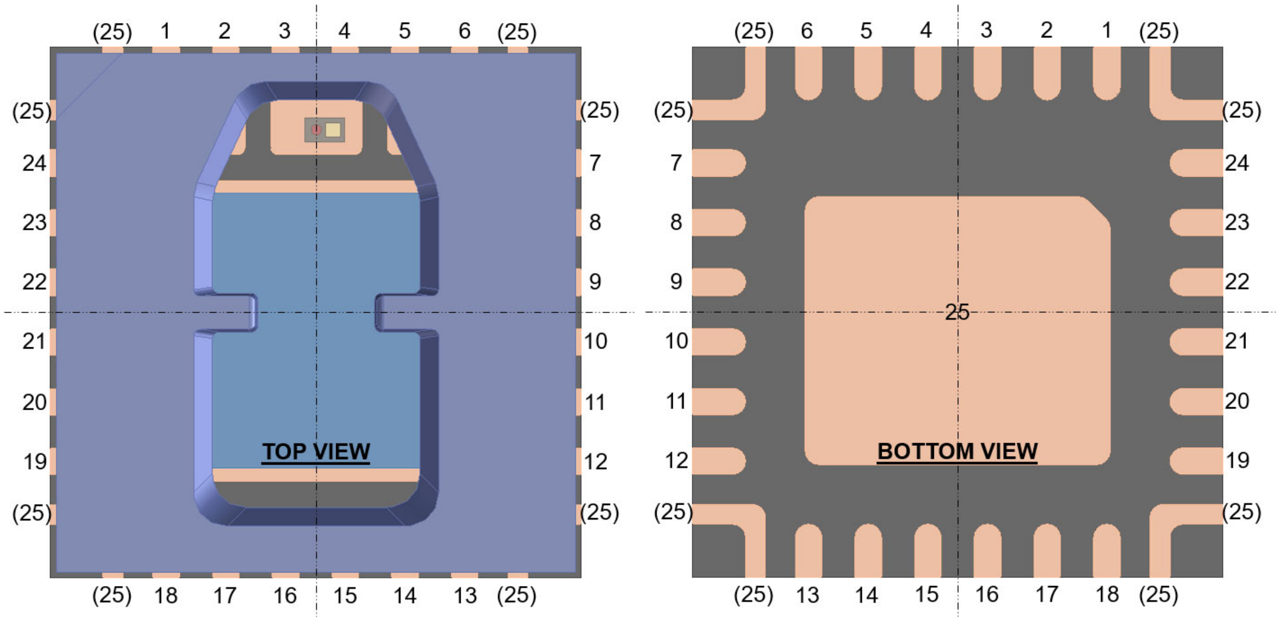
Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Operating Temperature	T_A	–40	25	115	°C	
Supply Voltage	V_{CC}	2.97	3.3	3.63	V	Ripple < 100 mVp-p
		4.5	5	5.5		
Current	Icc	—	30	65	mA	No load
Pin Current (All I/O Outputs)	I	–20	—	20	mA	
Maximum Output Frequency	F	—	—	200	kHz	At 1x interpolation
		—	—	400	kHz	At 2x interpolation
		—	—	800	kHz	At 4x interpolation
		—	—	1.6	MHz	At 8x interpolation
		—	—	2	MHz	At 16x interpolation
Radial Misalignment	ER	—	—	± 0.2	mm	
Tangential Misalignment	ET	—	—	± 0.2	mm	
Tilt Misalignment	Eθ	—	—	± 2.0	deg	
Code Wheel Gap	G	0.5	1.00	1.5	mm	

Power-Up Behavior

When the AEDR-9820 is powered on, the A, B, and I digital outputs are invalid until after the initial first state of either the Ch A or Ch B signal.

Encoder Pinout

Figure 4: Encoder Pinout



Pin	Name	Function
1	CH_A/A+	Digital A+/Analog Sin+
2	N.C.[a]	—
3	LED ANODE	LED Anode
4	LED ANODE	LED Anode
5	LED CATHODE	LED Cathode
6	LED REG	LED Regulation
7	VDDA/VCC	Analog Supply Voltage
8	VSSA/AGND	Analog Ground
9	SEL2	Mode Selection 2
10	SEL1	Mode Selection 1
11	INDEX_N/I-	Index Output Z- (Digital/Analog)
12	INDEX_P/I+	Index Output Z+ (Digital/Analog)
13	N.C.	—

a. N.C. – No connect.

Pin	Name	Function
14	N.C.	—
15	N.C.	—
16	N.C.	—
17	N.C.	—
18	N.C.	—
19	INDEX_SEL	Index Control
20	CH_BB/B-	Digital B-/Analog Cos-
21	CH_B/B+	Digital B+/Analog Cos+
22	VSSD/DGND	Digital Ground
23	VDD	Digital Supply Voltage
24	CH_AB/A-	Digital A-/Analog Sin-
25	VSSA	Analog Ground
(25)	N.C.	—

NOTE:

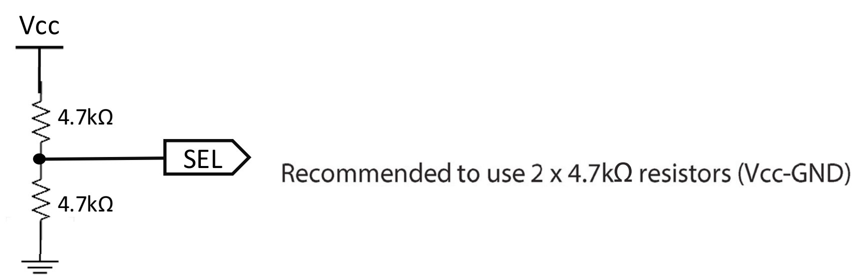
1. No connection to all corner pads indicated as (25).
2. Connect pin 8, pin 22, and pin 25 to common ground for all digital or analog mode applications. Pin 25 is the center pad of the package.
3. Both pin 7 and pin 23 must be powered during operation.
4. Both pin 5 and Pin 6 must be connected together.

Select Options – Encoder Built-in Interpolation

SEL 1	SEL 2	IND SEL	Interpolation Factor	Index	Maximum Output Frequency	CPR at ROP 4.6 mm	CPR at ROP 11 mm
Open	Open	Low	1X	Gated 90°	200 kHz	256	612
		High		Gated 180°			
		Open		Ungated raw			
Open	Low	Low	2X	Gated 90°	400 kHz	512	1224
		High		Gated 180°			
		Open		Gated 360°			
High	High	Low	4X	Gated 90°	800 kHz	1024	2448
		High		Gated 180°			
		Open		Gated 360°			
Low	Low	Low	8X	Gated 90°	1.6 MHz	2048	4896
		High		Gated 180°			
		Open		Gated 360°			
High	Low	Low	16X	Gated 90°	2.0 MHz	4096	9792
		High		Gated 180°			
		Open		Gated 360°			
Open	High	N/A	Analog (500 mVpp)	Analog	200 kHz	N/A	N/A
Low	High	N/A	Analog 1 Vpp	Ungated Digital			
High/Low	Open	N/A	Analog 1 Vpp	Analog			

NOTE: Open selection must be connected to the middle of a voltage divider circuit.

Figure 5: Open Selection



The digital interpolation factor above may be used with the following equations to cater to various rotational speed (RPM) and count per revolution (CPR).

RPM = (Count Frequency × 60) / CPR.

The CPR (at 1X interpolation) is based on the following equation that is dependent on radius of operation (ROP).

CPR = LPI × 2π × ROP (in.) or CPR = LP mm × 2π × ROP (mm).

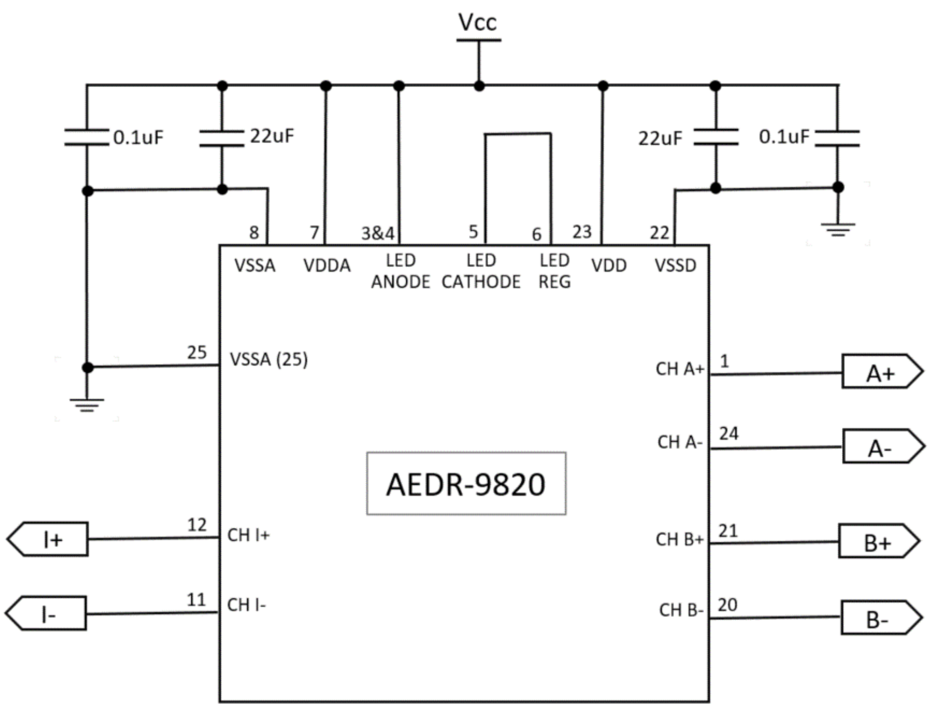
NOTE: LPmm (lines per mm) = LPI / 25.4.

Recommended Setup for the Power Supply Pins and General Routing

Both VDDA, VDD, and the respective grounds (VSSA and VSSD) are to be connected as follows. Follow these recommended schematic design rules:

- Use a pair of 22-μF and 0.1-μF capacitors as bypass on VDD and VDDA. Place them in parallel as close as possible to the encoder ASIC package, in between the power and ground pins.
- Avoid routing the INDEX trace in parallel and close to the analog signals to reduce the INDEX signal switching noise from coupling into the analog signal.
- Design separate VDD and VDDA traces.
- Minimize trace or cable length wherever possible.
- For single-ended applications, do not ground the Output- from the encoder. Allow the output to float.

Figure 6: Connection Diagram



NOTE:

1. Pin 25 is the center pad of the package and is designated as AGND.
2. See the table in Select Options – Encoder Built-in Interpolation for SEL1X, SEL2X, and IND SEL configurations.

Analog Encoder Characteristics

Parameter	Symbol	Min.	Typ.[a]	Max.	Units
Peak to Peak Voltage (Average)	V_{PPA}, V_{PPB}	0.9	1.0	1.1	V
		0.45	0.50	0.55	V
Analog Offset Voltage	$V_{OFFSETA}$, $V_{OFFSETB}$	0.45 Vcc	0.5 Vcc	0.55 Vcc	V
Voltage Reference (Midpoint of Signal Vpp)	Vref	—	Vcc/2	—	V

a. Typical values represent the average value of encoder performance in Broadcom factory-based setup conditions.

NOTE: The optimal performance of encoder depends on the motor/system setup condition of the individual customer.

Digital Encoder Characteristics (Code Wheel of ROP at 4.6 mm)

Parameter	Symbol	Dynamic Performance					Units
		Typical[a]					
Interpolation Factor		1X	2X	4X	8X	16X	
Cycle Error	ΔC	± 7	± 12	± 21	± 28	± 35	°e
Pulse Width (Duty) Error	ΔP	± 6	± 13	± 14	± 18	± 25	°e
Phase Error	Δϕ	± 3	± 7	± 7	± 9	± 9	°e
State Error	ΔS	± 6	± 8	± 11	± 12	± 14	°e
Index Pulse Width (Gated 90°)	Po	90	90	90	90	90	°e
Index Pulse Width (Gated 180°)	Po	180	180	180	180	180	°e
Index Pulse Width (Gated 360°)	Po	NA	360	360	360	360	°e
Index Pulse Width (Raw Ungated)	Po	330	N/A	N/A	N/A	N/A	°e

a. Typical values represent the average value of encoder performance based on factory setup conditions at 12k RPM with a metal code wheel.

NOTE: The optimal performance of the encoder depends on the motor/system setup and the code wheel type and condition.

Electrical Characteristics

Characteristics over recommended operating conditions at 25°C.

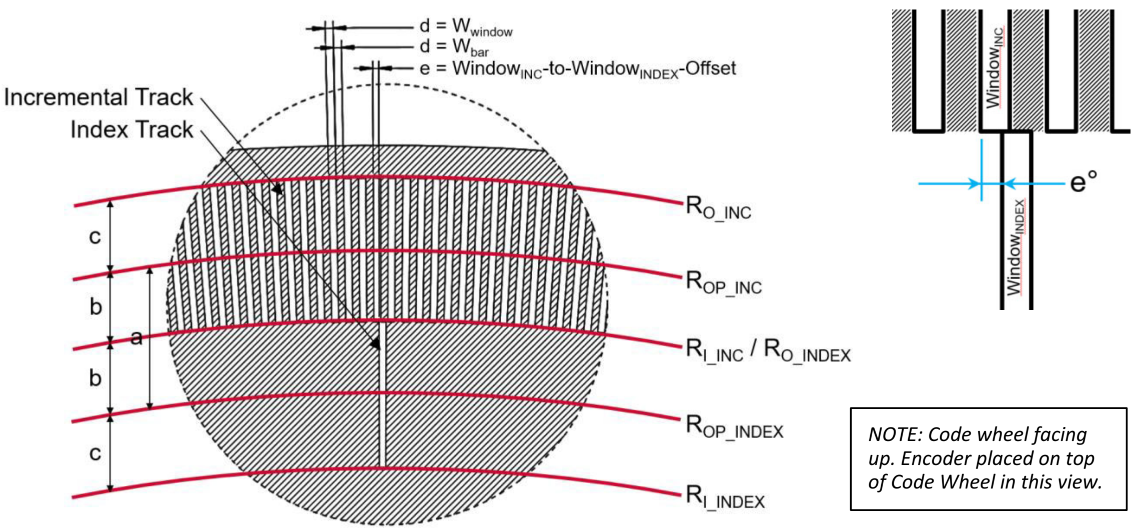
Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
High Level Output Voltage	VOH	2.4	—	—	V	IOH = –20 mA
Low Level Output Voltage	VOL	—	—	0.4	V	IOH = +20 mA
Output Current per Channel, lout	Io	—	—	20	mA	
Rise Time[a]	tr	—	< 50	—	ns	CL ≤ 50 pF
Fall Time[a]	tf	—	< 50	—	ns	

a. Applicable for all digital modes except Index in Analog mode.

Code Wheel Design Guideline

The Incremental/Index window track is a reflective surface and the Incremental bar track is opaque.
The window width is denoted by W_{window}° and the bar width is denoted by W_{bar}°.
All windows and bars has the same width value, d°.
There is an offset between Incremental window track and Index window track, denoted by e°.
There should be minimum one Index window track, while the number of Incremental window/bar tracks depends on the CPR.
The W_{window}/W_{bar} ratio is recommended to be within a range of 0.9 to 1.1.
Reflectance of window and bar surface is 60% and 5%, respectively.

Figure 7: Code Wheel Design



Dimension	Formula	225 LPI
a (mm)	$R_{OP\_INC} - R_{OP\_INDEX}$	0.551
b (mm)	$R_{OP\_INC} - R_{I\_INC}$ or $R_{O\_INDEX} - R_{OP\_INDEX}$	a / 2
c (mm)	$R_{O\_INC} - R_{OP\_INC}$ or $R_{OP\_INDEX} - R_{I\_INDEX}$	0.35
d (°)	(360 / CPR) / 2	—
e (°)	(3 / 4) × d	—

Code Wheel Design Example

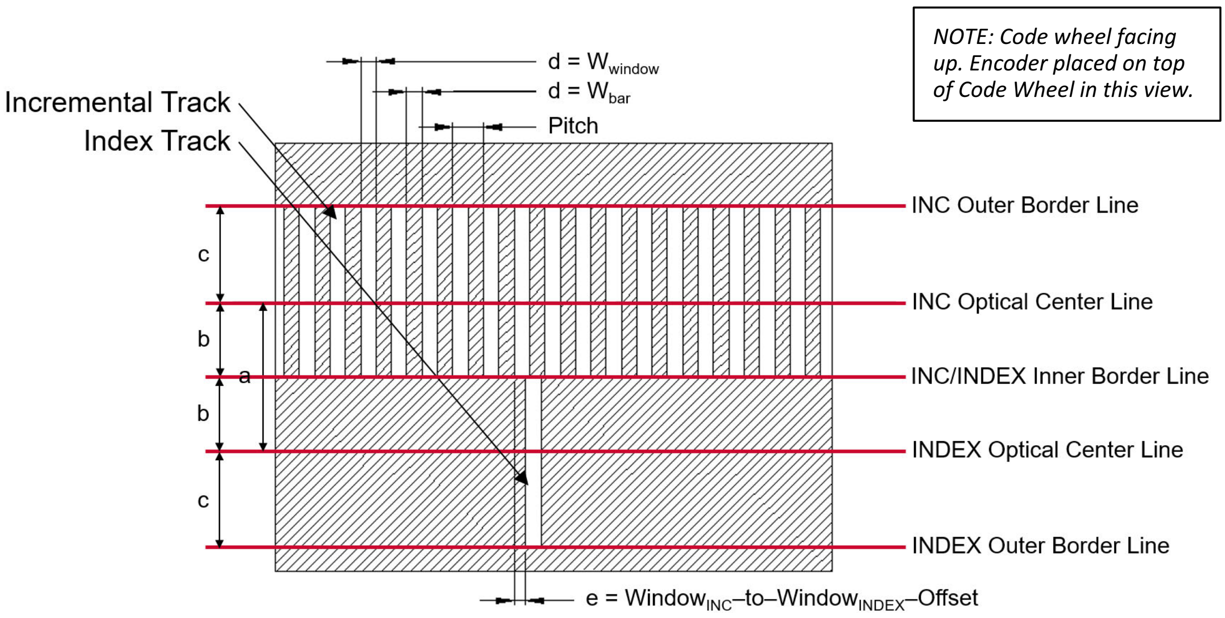
The following demonstrates a code wheel design for 225 LPI at 256 CPR.

Determine R_{OP_INC};	(25.4 / 225) × (256 / 2π)	≈ 4.600 mm
Determine R_{OP_INDEX};	4.600 – 0.551	= 4.049 mm
Determine R_{O_INC};	4.600 + 0.350	= 4.950 mm
Determine R_{I_INC} and R_{O_INDEX};	4.600 – (0.551 / 2)	= 4.325 mm
Determine R_{I_INDEX};	4.049 – 0.350	= 3.699 mm
Determine W_{window} and W_{bar};	(360 / 256) / 2	= 0.703°
Determine offset between $Window_{INC}$ and $Window_{INDEX}$;	(3 / 4) × 0.703	= 0.527°

Code Strip Design Guideline

The Incremental/Index window track is reflective surface and the Incremental bar track is opaque.
The window width is denoted by W_{window} and the bar width is denoted by W_{bar}.
All windows and bars has the same width value, d.
There is an offset between Incremental window track and Index window track, denoted by e.

Figure 8: Code Strip Design



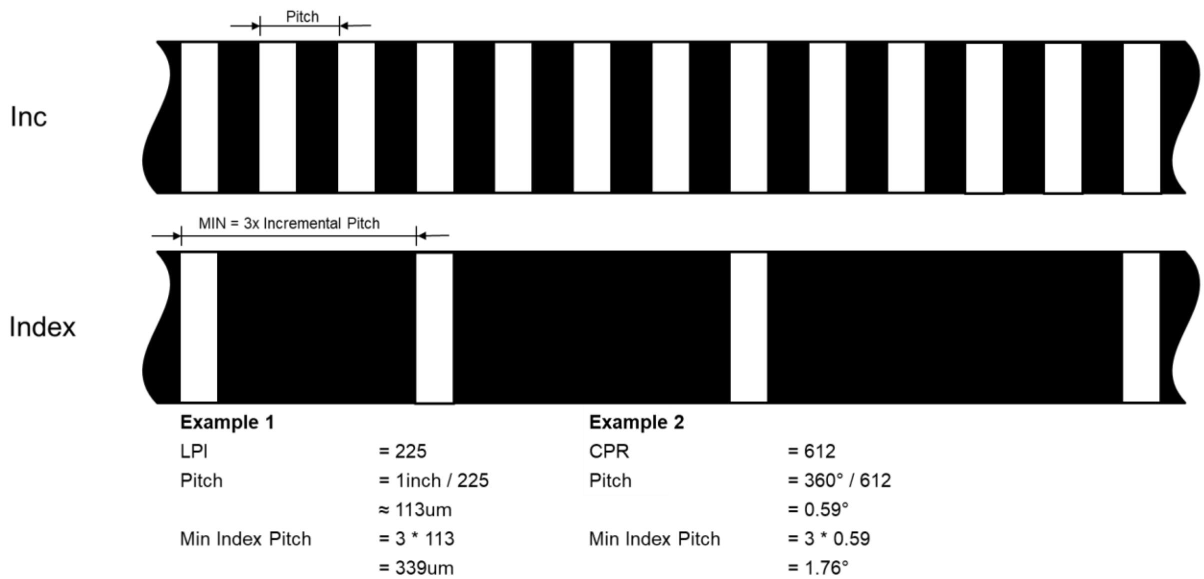
Dimension	Formula	225 LPI
Pitch (mm)	25.4 / LPI	0.113
a (mm)	$R_{OP\_INC} – R_{OP\_INDEX}$	0.551
b (mm)	$R_{OP\_INC} – R_{I\_INC}$ or $R_{O\_INDEX} – R_{OP\_INDEX}$	a / 2
c (mm)	$R_{O\_INC} – R_{OP\_INC}$ or $R_{OP\_INDEX} – R_{I\_INDEX}$	0.35
d (mm)	Pitch / 2	0.057
e (mm)	(3 / 4) × d	0.042

Multiple Index Pulse Code Wheel or Strip Design Guideline

For a pseudo absolute encoder application, the multiple Index pulse can be designed into the code wheel or the code strip. The minimum index bar width is 3x the incremental pitch.

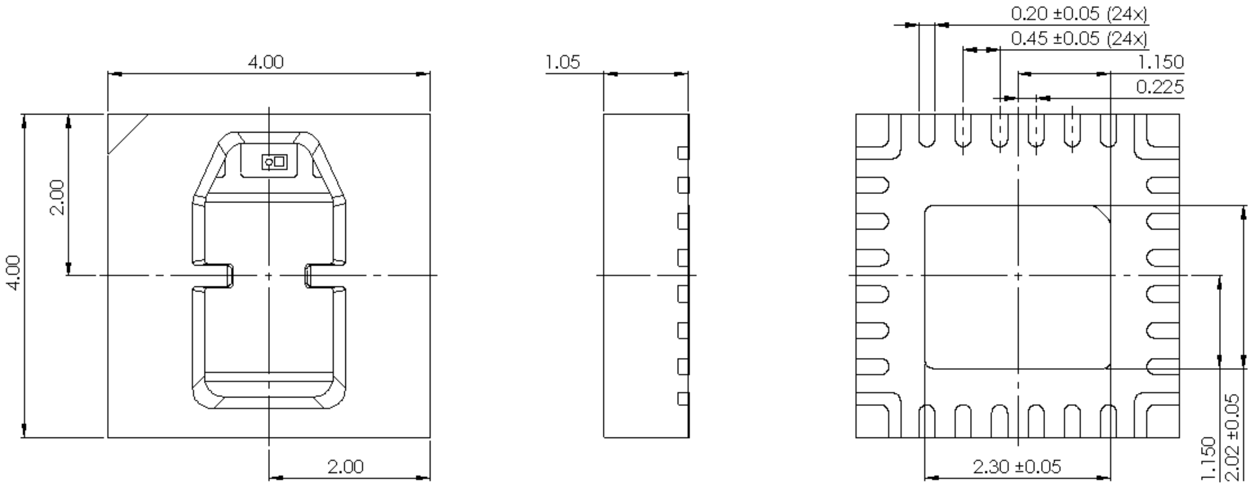
Figure 9 shows the recommended multiple Index pulse design guideline.

Figure 9: Multiple Index Pulse Design



Package Outline Drawing

Figure 10: Package Outline Drawing

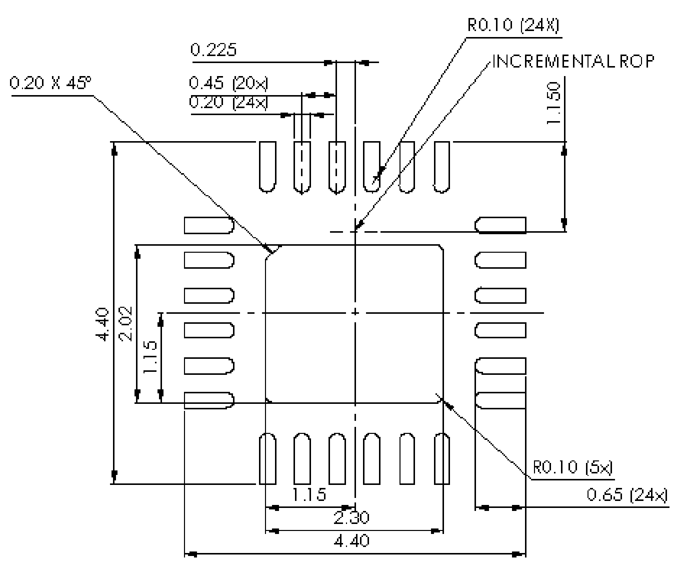


NOTE:

1. All dimensions are in mm.
2. Tolerance is x.xx ± 0.15 mm.

Recommended Land Pattern

Figure 11: Recommended Land Pattern



NOTE:

1. All dimensions are in mm.
2. Tolerance is x.xx ± 0.05 mm.

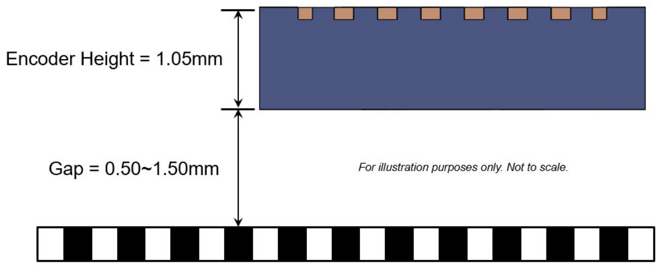
Encoder Placement Orientation, Position, and Direction of Movement

The AEDR-9820 is designed with both the emitter and detector dice placed in parallel to the code wheel window/bar orientation. The encoder package mounted on the top, facing down onto the code wheel. When properly aligned, the detector side will be closer to the center of the code wheel than the emitter.

The optical center of the encoder package must be aligned tangential to the code wheel's ROP. The optimal gap setting recommended is 1.00 mm, with the range of 0.50 mm to 1.50 mm.

Channel A leads Channel B when the code wheel rotates counter-clockwise and vice versa.

Figure 12: Encoder Placement Orientation



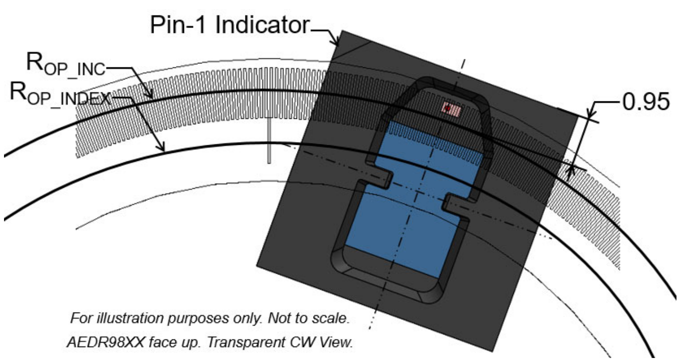
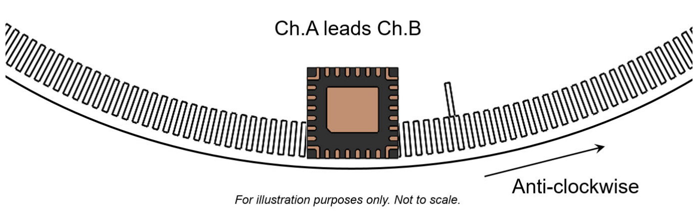
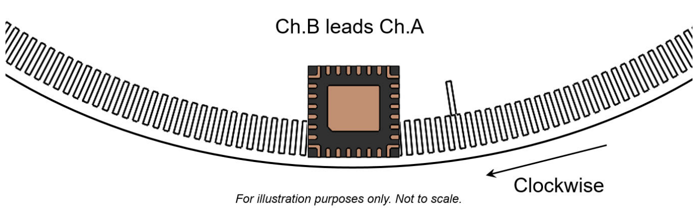


Figure 13: Channel Leads





Moisture Sensitivity Level

The AEDR-9820 package is specified to moisture sensitive level 3 (MSL 3). Take precautions when handling this moisture-sensitive product to ensure the reliability of the product.

Storage before use

- An unopen moisture barrier bag (MBB) can be stored at <40°C/90% RH for 12 months.
- Open the MBB just prior to assembly.

Control after opening the MBB

The encoder that will be subjected to reflow solder must be mounted within 168 hours of factory condition <30°C/60% RH.

Control for unfinished reel

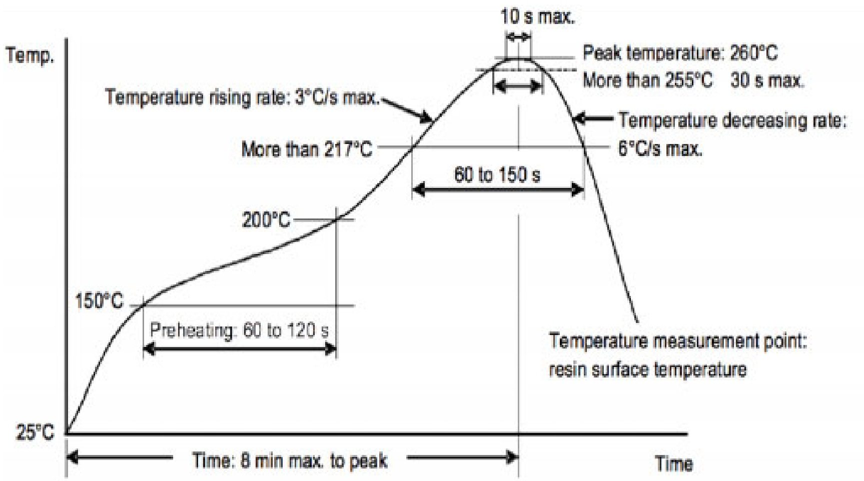
Store a sealed MBB with desiccant or desiccators at <5% RH condition.

Baking is required if the following conditions exist:

- The humidity indicator card (HIC) is >10% when read at 23°C ± 5°C.
- The encoder floor life exceeded 168 hours.

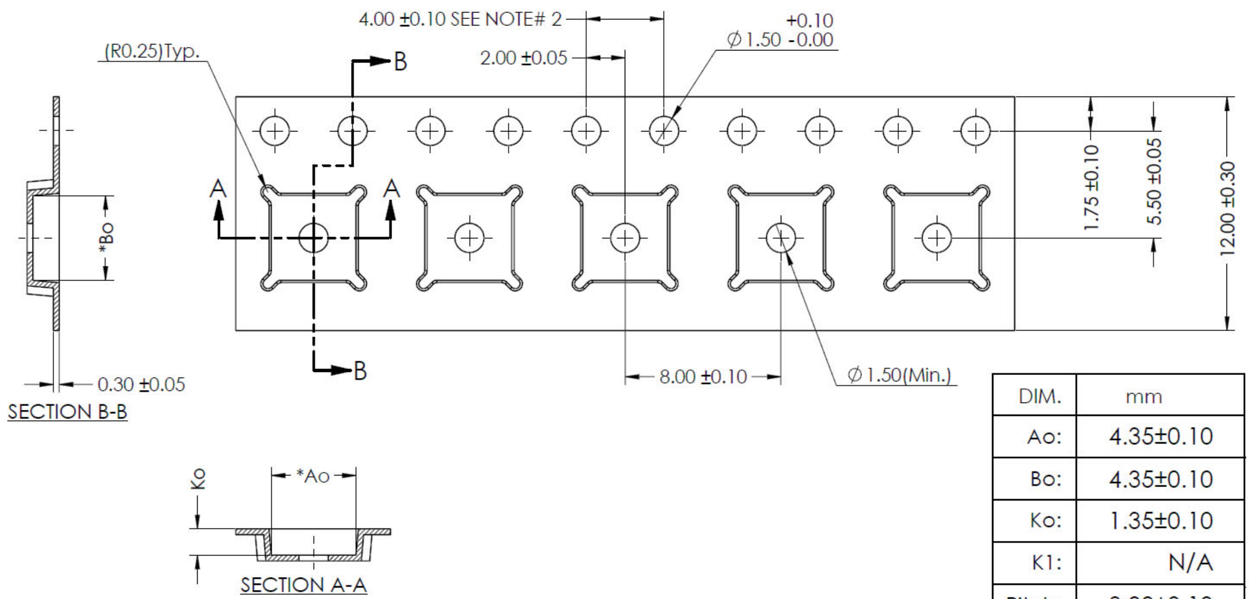
The recommended baking condition: 60°C ± 5°C for 20 hours (tape and reel) or 125°C ± 5°C for 5 hours (loose units).

Figure 14: Recommended Lead Free Solder Reflow Soldering Temperature Profile



CAUTION! Exercise care when handling the encoder ASIC because it is a sensitive optical device. Remove the protective kapton tape only after the reflow process and just before final assembly.

Figure 15: Tape and Reel Information



DIM.	mm
Ao:	4.35±0.10
Bo:	4.35±0.10
Ko:	1.35±0.10
K1:	N/A
Pitch:	8.00±0.10
Width:	12.00±0.30

Figure 16: Ordering Information

A E D R - 9 8 2 0 - 1 0 X

Resolution (2)

2 - 225

Packaging (X)

0 - 1000 pcs
2 - 100 pcs

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