

PLC High Density Analog I/O Module

Features • 8-pt (0-10V DC / 4-20mA) input module

- 2-pt (-10 to +10VDC / 4-20mA) output module
- 16-bit Resolution
- Fast Conversion Times
- Configure up to 56 analog I/Os

Specifications

General Specifications				
Part Number	FC4A-J8C1	FC4A-K2C1		
Rated Power Voltage	24V DC			
Allowable Voltage Range	20.4 to 28.8V DC			
Terminal Arrangement	See Analog I/O Module User's Manual -pages 2-8 to 2-11			
Connector on Mother Board	MC1.5/10-G-3.81BK (Phoenix Contact)			
Connector Insertion/ Removal Durability	100 times minimum			
Internal Current Draw	40mA (5V DC)	60mA (5V DC)		
	0mA (24V DC)	0mA (24V DC)		
External Current Draw (Note)	50mA (24V DC)	85mA (24V DC)		
Weight	140g	110g		

Analog Input Specifications					
Part Number FC4A-J8C1					
Analog Input Signal Type		Voltage Input	Current Input		
Input Range		0 to 10V DC	4 to 20mA		
Input Impedance		1 MΩ	100Ω		
	Sample Duration Time	2ms maximum			
_	Sample Repetition Time	2ms maximum			
AD Conversion	Total Input System Transfer Time ^{Note 1}	8ms x channels + 1 scan time			
Con	Type of Input	Single-ended input Self-scan			
AD	Operating Mode				
	Conversion Method	Successive approximation register method			
Input Error	Maximum Error at 25°C	±0.2% of full scale			
	Temperature Coefficient	±0.005% of full scale / °C			
	Repeatability after Stabilization Time	±0.5% of full scale			
	Non-lineality	±0.04% of full scale			
	Maximum Error	±1% of full scale			



For more information on these and other IDEC PLCs, visit:

www.idec.com/PLC

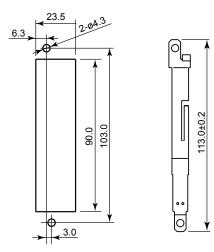
Analog Output Specifications					
Part Number			FC4A-K2C1		
Output Range Voltage Current		-10 to +10V DC			
		4 to 20mA DC			
Load	Load Impedance		$2 k\Omega$ minimum (voltage), 300Ω maximum (current)		
	Applicable Load Type		Resistive load		
DA	Settling Time		1ms / ch		
Conversion	Total Output System Transfer Time		1ms x channels + 1 scan time		
Output	Maximum Error at 25°C		±0.2% of full scale		
	Temperature Coefficient		±0.005% of full scale/°C		
	Repeatability after Stabilization Time		±0.5% of full scale		
	Output Voltage Drop		±1% of full scale		
Error	Non-lineality		±0.2% of full scale		
	Output Ripple		±0.1% of full scale		
	Overshoot		0%		
	Total Error		±1% of full scale		
	Digital Resolution		50000 increments (16 bits)		
	Output Value of LSB	Voltage	0.4mV		
D /		Current	0.32µA		
	Data Type in Application Program		–25000 to 25000 (voltage)		
Data			0 to 50000 (current)		
			Optional: -32768 to 32767 (selectable for each channel) ^{Note 2}		
	Monotonicity		Yes		

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Analog Input Specifications					
Data	Digital Resolution	50000 increments (16 bits)			
	Input Value of LSB	0.2mV	0.32µA		
	Data Type in Application Program	Default: 0 to 50000			
	Monotonicity	Optional: -32768 to 32767 (selectable for each channel) ^{Note 2}			
	Input Data Out of Range	Detectable ^{Note 3}			
Noise Resistance	Maximum Temporary Deviation during Electrical Noise Tests ^{Note 4}	±3% maximum			
	Input Filter	Software			
	Recommended Cable for Noise Immunity	Twisted pair cable			
ž	Crosstalk	2 LSB maximum			
Isolation		Isolated between input and power circuit			
		Photocoupler-isolated between input and internal circuit			
Effect of Improper Input Connection		No damage			
Maximum Permanent Allowed Overload (No Damage)		11V DC	22 mA DC		
Sele	ction of Analog Input Signal Type	Using software programming			

Analog Output Specifications **Current Loop Open** Not detectable **Maximum Temporary Deviation during** ±3% maximum **Electrical Noise Tests**^{Note 4} Noise Resistance **Recommended Cable for** Twisted pair cable **Noise Immunity** 2 LSB maximum Crosstalk Isolated between output and power circuit Isolation Photocoupler-isolated between output and internal circuit **Effect of Improper Output Connection** No damage **Selection of Analog Output Signal Type** Using software programming

Mounting Hole Layout (mm)

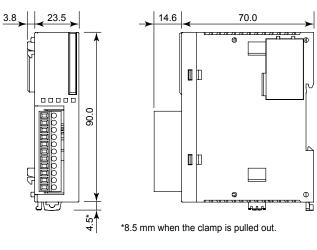


Notes:

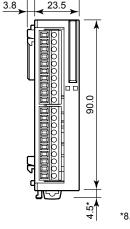
- Total input system transfer time = Sample repetition time + Internal processing time. The total input system transfer time increases in proportion to the number of of channels used.
- 2. The data processed in the analog I/O module can be linear-converted to a a value between -32768 and 32767. The the optional range designation, and analog I/O data minimum and maximum values can be selected using data registers allocated to analog I/O modules.
- When an error is detected, a corresponding error code is stored to a data register allocated to analog I/O operating status.
- 4. The value is measured when a 500V clamp voltage is applied to the power supply and I/O lines.

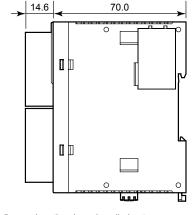
Dimensions (mm)

FC4A-K2C1









*8.5 mm when the clamp is pulled out.



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IDEC:

FC4A-PM32 FC4A-J8C1 FC4A-K2C1 FC4A-J4CN1 FC4A-L03A1 FC4A-USB FC4A-KC4CA FC4A-C24R2 FC4A-C16R2 FC4A-DS824-SW14 FC4A-MBUS485 FC4A-C10R2C FC4A-C24R2C FC4A-C24R2C-DS0831-DC-KIT FC4A-K1A1 FC4A-KC2CA FC4A-PMC26 FC4A-PMT11 FC4A-C10R2 FC4A-PM64 FC4A-C16R2C FC4A-J8AT1 FC4A-PMT13 FC4A-DS824-SW6 FC4A-KC1CA-12 FC4A-PMT10 FC4A-KC2CA-15 FC4A-L03AP1 FC4A-ENET FC4A-DS824-SW9 FC4A-KC3C FC4A-PMC20 FC4A-PMTS16 FC4A-HPH1 FC4A-PMAC2 FC4A-DSP1 FC4A-KC1C FC4A-PMTS16P FC4A-C16R2B FC4A-PSP1PN05 FC4A-PMC26P FC4A-PMT11P FC4A-C24R2B FC4A-KC2C FC4A-PMT13P FC4A-PMTK16P FC4A-C10R2B FC4A-PM128 FC4A-PMTK16 FC4A-PMAC2P FC4A-PSP1P FC4A-J8C1-DS0833 FC4A-PMC20P FC4A-PMT10P FC4A-USB-BF812Q FC4A-PMC20PN02 FC4A-KC1CA FC4A-K4A1