



Features

- Wide input voltage ranges up to 150 VDC
- 1 or 2 isolated outputs up to 48 V
- RoHS lead-free-solder and lead-solder-exempted products are available.
- 5 year warranty for RoHS lead-free-solder products with temperature index -8
- 1500 to 3000 VAC I/O voltage withstand test
- Emissions EN 55011, group 1, level A
- Immunity to EN 50155 and EN 50121-3-2
- High efficiency (typ. 86%)
- Input undervoltage lockout
- Shutdown input, adjustable output voltages
- Flex power: Flexible load distribution on outputs
- · Outputs no-load, overload, and short-circuit proof
- Operating ambient temperature –40 to 85 °C
- Thermal protection
- Dimensions 51.0 x 10.5 x 40.6 mm (2.0 x 1.6 x 0.42 in)
- Basic insulation: 20/40IMX15 models; Double or reinforced insulation: 110IMY15 models
- Safety-approved to the latest edition of UL/CSA 60950-1, comply with IEC/EN 62368-1 standard



10.5

0.42"

51

2.0"

40.6

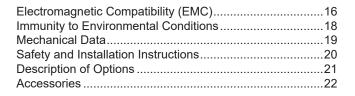
1.6'



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YEAR







Description

The IMX15, IMS15, and IMY15 Series of board mountable 15 Watt DC-DC converters have been designed according to the latest industry requirements and standards. The converters are particularly suitable for use in mobile or stationary applications in transport, railways, industry, or telecommunication, where variable input voltages or high transient voltages are prevalent.

Covering a total input voltage range from 8.4 up to 150 V with different models, the converters are available with single and electrically-isolated double outputs from 3.3 up to 48 V, externally adjustable, with flexible load distribution on double-output models. A shutdown input allows for remote on/off.

Features include efficient input and output filtering and consistently high efficiency over the entire input voltage range, high reliability, and excellent dynamic response to load and line changes.

The converters have been approved by CSA. 20IMS/20IMX15 and 40IMS/40IMX15 models exhibit a basic insulation. The 110IMY15 models provide double insulation and are CE marked. They may be connected to a rectified 110 VAC source or a 110 V battery without any further insulation barrier.

The circuitry is comprised of integrated planar magnetics. All components are automatically assembled and solidly soldered onto a single PCB without any wire connection. Magnetic feedback ensures maximum reliability and repeatability in the control loop over all operating conditions. Careful consideration of possible thermal stress ensures the absence of hot spots providing long life in environments, where temperature cycles are frequent. The thermal design allows operation at full load up to an ambient temperature of 71 °C in free air without using any potting material. For extremely high vibration environments the case has holes for screw fastening.

Model Selection

Table 1: Model Selection

| Outp | out 1 | Outp | out 2 | Output Power | Input voltage | ge Efficiency ⁶ | | Model | Options ² |
|--|--------------------------------------|--|----------------------------------|--------------------------------------|--|----------------------------|----------------------------|---|--|
| V _{o nom} [VDC] | I _{o nom} 1 [mA] | V _{o nom} [VDC] | I _{o nom} 1 [mA] | P _{o nom} [W] | V _{i min} to V _{i max} [VDC] | ղ _{ուո} [%] | η _{max} [%] | | |
| 3.3 3.3 3.3 3.3 3.3 | 4.5 4.0 4.5 4.5 | | | 14.9 13.2 14.9 14.9 | 8.4 to 36 ⁵ 16.8 to 75 ³ 16.8 to 75 ³ 50 to 150 ⁴ | 80 78 81 77 | 82 80 83 79 | 20IMX15-03-8RG-G ^{SR} 40IMX15-03-8RG 40IMX15-03-8RG-G ^{SR} 110IMY15-03-8RG-G ^{SR} | i, Z, <mark>non-G</mark> non-G i, Z, non-G i, Z, <mark>non-G</mark> |
| 5.1 5.1 5.1 | 3.5 3.5 3.5 | - - - | - - - | 17.5 17.5 17.5 | 8.4 to 36 ⁵ 16.8 to 75 ³ 50 to 150 ⁴ | 83 85 79.5 | 86 87 82 | 20IMX15-05-8RG-G ^{SR} 40IMX15-05-8RG-G ^{SR} 110IMY15-05-8RG-G ^{SR} | i, Z, <mark>non-G</mark> i, Z, non-G i, Z, non-G |
| 5.1 5.1 5.1 5.1 | 2.3 2.7 2.5 2.5 | | | 11.7 13.8 12.8 12.8 | 8.4 to 36 ⁵ 14 to 36 16.8 to 75 ³ 50 to 150 ⁴ | 82 81.5 81.5 78 | 84 84 83 81 | 20IMX15-05-8R-G 24IMS15-05-9R 40IMX15-05-8R-G 110IMY15-05-8R-G | Z, <mark>non-G</mark> Z, <mark>non-G</mark> |
| 5.1 5.1 5.1 5.1 5.1 5.1 | 1.35 1.6 1.5 1.6 1.5 | 3.3 3.3 3.3 3.3 3.3 3.3 | 1.35 1.6 1.5 1.6 1.5 | 11.3 13.5 12.6 13.5 12.6 | 8.4 to 36 ⁵ 14 to 36 16.8 to 75 ³ 36 to 75 50 to 150 ⁴ | 82 81 82 79 79 | 84 83 84 82 82 | 20IMX15-0503-8RG 24IMS15-0503-9R 40IMX15-0503-8RG 48IMS15-0503-9R 110IMY15-0503-8RG | i, Z, non-G i, Z, non-G i, Z, non-G |
| 5 5 5 5 | 1.3 1.4 1.4 1.4 | 5 5 5 5 | 1.3 1.4 1.4 1.4 | 13.0 14.0 14.0 14.0 14.0 | 8.4 to 36 ⁵ 14 to 36 16.8 to 75 ³ 50 to 150 ⁴ | 82 81 81 80 | 85 84 84 82 | 20IMX15-05-05-8G 24IMS15-05-05-9 40IMX15-05-05-8G 110IMY15-05-05-8G | i, Z, <mark>non-G</mark> i, Z, <mark>non-G</mark> i, Z, <mark>non-G</mark> |
| 12 12 12 12 | 0.65 0.7 0.7 0.7 | 12 12 12 12 | 0.65 0.7 0.7 0.7 | 15.6 16.8 16.8 16.8 | 8.4 to 36 ⁵ 14 to 36 16.8 to 75 ³ 50 to 150 ⁴ | 83 83 84 82 | 86 86 87 85 | 20IMX15-12-12-8G 24IMS15-12-12-9 40IMX15-12-12-8G 110IMY15-12-12-8G | i, Z, <mark>non-G</mark> i, Z, <mark>non-G</mark> i, Z, <mark>non-G</mark> |
| 15 15 15 15 | 0.5 0.56 0.56 0.56 | 15 15 15 15 | 0.5 0.56 0.56 0.56 | 15.0 16.8 16.8 16.8 | 8.4 to 36 ⁵ 14 to 36 16.8 to 75 ³ 50 to 150 ⁴ | 85 83 83 82 | 88 86 86 85 | 20IMX15-15-15-8G 24IMS15-15-15-9 40IMX15-15-15-8G 110IMY15-15-15-8G | i, Z, <mark>non-G</mark> i, Z, <mark>non-G</mark> i, Z, <mark>non-G</mark> |
| 24 24 24 24 24 | 0.32 0.35 0.35 0.35 0.35 | 24 24 24 24 24 | 0.32 0.35 0.35 0.35 | 15.4 16.8 16.8 16.8 | 8.4 to 36 ⁵ 14 to 36 16.8 to 75 ³ 50 to 150 ⁴ | 83 84 84 82 | 86 87 86 85 | 20IMX15-24-24-8G 24IMS15-24-24-9G 40IMX15-24-24-8G 110IMY15-24-24-8G | i, Z, <mark>non-G</mark> non-G i, Z, non-G i, Z, <mark>non-G</mark> |

Flexible load distribution on dual and double outputs possible; up to 75% of the total output power *P*_{onem} on one of the 2 outputs. IMX/IMY15-0503 models have reduced load distribution flexibility; 1.8 A max. on one of the 2 outputs. The total output power should not exceed *P*_{onem}; the total output power should not

exceed $P_{o \text{ nom}}$: 2 See Description of Options. 3 Short-time operation down to V = 14.4 V possible (P_{o} reduced to approx. 85% of $P_{o \text{ nom}}$) 4 Short-time operation down to V = 43.2 V possible (P_{o} reduced to approx. 85% of $P_{o \text{ nom}}$) and up to 154 V 5 Initial start-up at 9 V, main output voltage regulation down to 8.4 V 6 Efficiency at $T_{A} = 25 \text{ °C}$, $V_{i \text{ nom}}$, $I_{o \text{ nom}}$. 5R Models with synchronous rectifier. For the RoHS version of such models, add -G !

NFND: Not for new designs. Preferred for new designs.

Replace 24IMS15 models by 20IMX15 and 48IMS15 by 40IMX15;

Replace 20IMX15-05, 40IMX15-05, 110IMY15-05 by models with synchronos rectifier SR; for new designs, chose always the RoHS version !



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RoHS-Compliant Models

The type designation of RoHS-compliant models (compliant for the restriction of all six substances) for the IMX/IMY15 Series ends with "G".

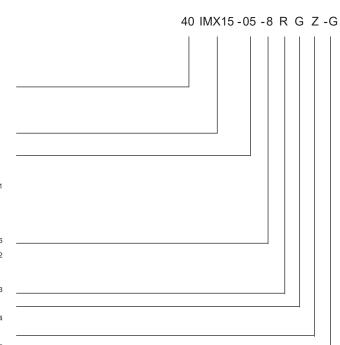
However, in **single-output models** with 3.3 V or 5.1 V output an extra hyphen (-) is added after the existing "G", which already signifies a synchronous rectifier^{SR}. This is an exception to our normal nomenclature to identify this type of product, since G was already used to designate products for higher output current fitted with a synchronous rectifier. Some examples

- 20IMX15-05-8R-G designates a standard version with RoHS compliance for all six substances.
- 20IMX15-05-8RG designates a synchronous rectifier^{SR} version which is not RoHS-compliant.
- 20IMX15-05-8RG-G designates a synchronous rectifier^{SR} version and RoHS compliant for all six substances.
- 20IMX15-05-05-8G designates a double-output model with RoHS compliance for all six substances
- 110IMY15-24-24-8RG designates a double-output model with RoHS compliance for all six substances.

Part Number Description

Input voltage range V_{i}

| input voitage | | | |
|---------------|--|-------------|---|
| | 8.4 to 36 V 20 14 to 36 V 24 16.8 to 75 V 40 36 to 75 V 40 50 to 150 V 110 | 4 0 8 | |
| Series | IMX15, IMS15, IMY15 | | |
| Output volta | ge of output 1 | 4 | |
| Hyphen desi | gnating double-output models with two electrically isolated outputs | - | 1 |
| Output voltag | ge of output 2 | 4 | |
| Operating ar | nbient temperature range <i>T</i> _A -40 [°] to 85 °C -40to 71 °C | | |
| Options and | features: R input and magnetic feedback Synchronous rectification | G | |
| | Open frame | Ζ | |



¹ Not applicable to -0503 models. They have a common ground.

- ² IMS15 models are not recommended for new designs.
 ³ Standard for single-output and -0503 models.
- ³ Standard for single-output and -0503 models
- ⁴ Option i replaces the standard shutdown function; see *Description of Options*.
- ⁵ RoHS models with synchronous rectifier are ending with -G, as an exception.

⁶ Only –25 °C for older -8RG^{SR} models (Rev. <BA); see table 7a.

Note: The sequence of options must follow the order above !

Examples:

20IMX15-05-05-8G: DC-DC converter, input 9 to 36 V, 2 galvanically isolated outputs each providing 5 V, 1.3 A, RoHS-compliant. 110IMY15-0503-8RG: DC-DC converter, input 50 to 150 V, 2 outputs with common return providing +5.1 V, 1.5 A and +3.3 V, 1.5 A, RoHS-compliant. Converter fitted with magnetic feedback for tight output voltage regulation.

Product Marking

The converters without option Z are marked with basic type designation, input and output voltages and currents, applicable safety approval and recognition marks, company logo, date code, and serial number.



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Functional Description

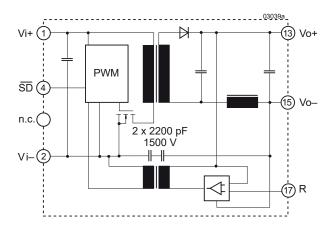
The IMX15/IMS15/IMS15/IMY15 Series of DC-DC converters are magnetic feedback-controlled flyback converters using current mode PWM (Pulse Width Modulation). The -05- and -0503-output voltage models exhibit an active magnetic feedback loop via a pulse transformer, resulting in very tight regulation of the output voltage (see the block diagrams). The output voltages of these models can be adjusted via the R-input. The R-input is referenced to the secondary side and allows for programming the output voltages in the range of approximately 80 to 105% of $V_{o nom}$, using either an external resistor or an external voltage source.

Several single-output models with 3.3 or 5.1 V output exhibit a synchronous rectifier to improve the efficiency.

The voltage regulation on the double-output models is achieved with an auxiliary winding of the main transformer. The output voltages can be adjusted via the Trim input, which is referenced to the primary side and allows for programming the output voltage in the range of 100 to 105% of $V_{o nom}$ via an external resistor, or within 75 to 105% if using an external voltage source. The load regulation output characteristic allows for paralleling of one or more double-output models with equal output voltage.

Current limitation is provided by the primary circuit, thus limiting the total output power of double-output models. The shutdown input allows remote converter on/off.

Overtemperature protection will disable the converter under excessive overload conditions with automatic restart.



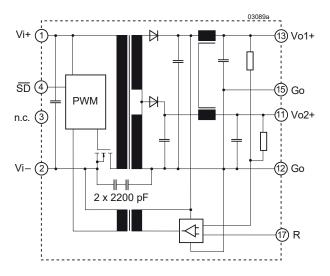


Fig. 1 Block diagram of single-output models

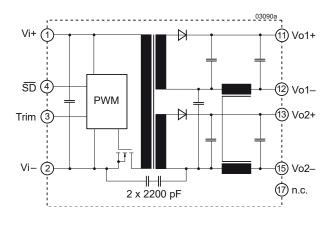


Fig. 3 Block diagram of double-output models



Fig. 2 Block diagram of -0503-models

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Electrical Input Data

General conditions:

- $T_A = 25$ °C, unless T_C is specified. Shut-down pin left open-circuit.
- Trim or R input left open-circuit.

Table 2a: Input data of IMX15 and IMY15 models

| Mode | el | | | | 20IMX1 | 5 | 4 | 40IMX1 | 5 | 1 | 10IMY1 | 5 | Unit |
|----------------------------|------------------------------|---------------------|---|------------------|-----------|-----|---------------------|-----------|-----|------------|-----------|------------------|------------------|
| Char | acteristics | | Conditions | min | typ | max | min | typ | max | min | typ | max | |
| V | Operating inpu | it voltage 1 | $T_{\rm Amin} - T_{\rm Amax,}$ | 9 ^{5,7} | | 36 | 16.8 ^{5,6} | | 75 | 50⁵ | | 150 ⁸ | |
| V _{i nom} | Nominal input voltage | | $I_{o} = 0 - I_{o \text{ nom}}$ | | 20 | | | 40 | | | 110 | | v |
| V _{isur} | Repetitive surge voltage | | Abs. max input (3 s) | | | 40 | | | 100 | | | 168 | |
| | Converter | Switch on | Worst case condition | | 0.25 | 0.5 | | 0.25 | 0.5 | | 0.5 | 0.8 | |
| t _{start-up} | start-up time ² | SD high | at $V_{i \min}$, $I_o = I_{o nom}$ | | | 0.1 | | | 0.1 | | | 0.1 | S |
| 4 | Dies times? | | V _{i nom} , resistive load | | 5 | | | 5 | | | 5 | | |
| t _{rise} | Rise time ² | | I _{o nom} , capacitive load | | 10 | 20 | | 10 | 20 | | 10 | 20 | ms |
| I, | No-load input | current | $I_{o} = 0, V_{i\min} - V_{i\max}$ | | | 65 | | | 45 | | | 15 | mA |
| I _{irr} | Reflected rippl | e current | $I_{o} = 0 - I_{o \text{ nom}}$ | | | 30 | | | 30 | | | 20 | mA _{pp} |
| I _{i inr p} | Inrush peak cu | ırrent ³ | $V_{i} = V_{i \text{ nom}}$ | | | 8 | | | 9 | | | 10 | Α |
| C _i | Input capacita | nce | For surge calculation | | 1.5 | | | 0.75 | | | 0.35 | | μF |
| V | Shutdown volt | 222 | Converter disabled | - | 10 to 0.7 | , | | 10 to 0.7 | | - | 10 to 0.7 | 7 | v |
| | Shutdown voltage | | Converter operating | ope | en or 2 – | 20 | ope | n or 2 – | 20 | ope | en or 2 – | 20 | V |
| $R_{\overline{\text{SD}}}$ | Shutdown input resistance | | | а | pprox. 10 | C | a | oprox. 10 | C | approx. 10 | | 0 | kΩ |
| I _{sd} | Input current, when disabled | | $\frac{V_{i\min} - V_{i\max}}{SD}$ connected to Vi- | | | 10 | | | 3 | | | 1 | mA |
| f | Switching freq | uency | $V_{i \min} - V_{i \max}, I_o = 0 - I_{o nom}$ | a | oprox. 30 | 0 | ар | prox. 30 | 0 | ap | prox. 30 | 00 | kHz |

¹ If V_o is set above V_{o nom} by use of the R or Trim input, V_{i min} will be proportionately increased.
 ² Measured with resistive and max. admissible capacitive load. Valid for models with rev. Al or greater.

³ Source impedance according to ETS 300132-2, version 4.3.

4 Double-output models with both outputs in parallel. External filter as in fig. 6, table 6.

⁵ Input undervoltage lockout at typ. 80% of $V_{i \min}$. ⁶ Short time operation down to $V_{i \min} > 14.4 \text{ V}$ possible. P_{o} reduced to approx. 85% of $P_{o nom}$. ⁷ Initial start-up at $V_i = 9 \text{ V}$, main output voltage regulation down to 8.4 V

8 Short time operation up to 154 V possible for 2 s.



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Table 2b: Input Data of IMS15 models; general conditions as in table 2a

| Mod | el | | | | 24IMS1 | 5 | | 48IMS1 | 5 | Unit |
|----------------------------|----------------------------|---------------------|---|-----|-----------|------|-------------|------------|-----|------------------|
| Char | racteristics | | Conditions | min | typ | max | min | typ | max | |
| V | Operating input | It voltage 1 | $T_{\rm Amin} - T_{\rm Amax}$ | 14 | | 36 | 36 | | 75 | |
| V _{i nom} | Nominal input | voltage | $I_{o} = 0 - I_{o \text{ nom}}$ | | 24 | | | 48 | | V |
| $V_{\rm isur}$ | Repetitive surge voltage | | Abs. max input (3 s) | | | 50 | | | 100 | 1 |
| | Converter | Switch on | Worst case condition | | 0.25 | 0.5 | | 0.25 | 0.5 | |
| t _{start-up} | start-up time ² | SD high | at $V_{i \min}$, $I_o = I_{o nom}$ | | 0.1 | | | | 0.1 | s |
| 4 | Diag time? | | V _{i nom} , resistive load | | 5 | | | 5 | | |
| t _{rise} | Rise time ² | | I _{o nom} , capacitive load | | 10 | 20 | | 10 | 20 | ms |
| I, , , | No-load input current | | $I_{o} = 0, V_{i\min} - V_{i\max}$ | | 20 | 40 | | 10 | 20 | mA |
| I _{irr} | Reflected ripple | e current | $I_{o} = 0 - I_{o \text{ nom}}$ | | 30 | | | 30 | | mA _{pp} |
| I _{i inr p} | Inrush peak cu | ırrent ³ | $V_{i} = V_{i \text{ nom}}$ | | | 5 | | | 4.5 | A |
| C _i | Input capacitar | nce | For surge calculation | | 4 | | | 2 | | μF |
| V | Shutdown volta | 222 | Converter disabled | | -10 to 0. | 7 | | -10 to 0.7 | 7 | V |
| $V_{\overline{\text{SD}}}$ | | aye | Converter operating | ор | en or 2 – | - 20 | ор | en or 2 – | 20 | V |
| $R_{\overline{\text{SD}}}$ | Shutdown input resistance | | | a | approx. 1 | 0 | a | approx. 1 | 0 | kΩ |
| $I_{\overline{\text{SD}}}$ | Input current, v | when disabled | $\frac{V_{i\min} - V_{i\max}}{SD}$ connected to Vi- | | 1.2 | 3 | | 1.2 | 3 | mA |
| f _s | Switching frequ | uency | $V_{i \min} - V_{i \max}, I_o = 0 - I_{o nom}$ | а | pprox. 30 | 00 | approx. 300 | | | kHz |

If V_{o} is set above V_{onom} by use of the R or Trim input, V_{imin} will be proportionately increased. Measured with resistive and max. admissible capacitive load. 1

2

3 Source impedance according to ETS 300132-2, version 4.3.

Double-output models with both outputs in parallel. External filter as in fig. 6, table 6. 4

5 Input undervoltage lockout at typ. 80% of $V_{\rm i min}$.

Inrush Current

The inrush current has been made as low as possible by choosing a very small input capacitance. A series resistor may be installed in the input line to further reduce this current.

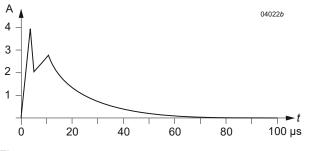


Fig. 4

Typical inrush current at $V_{i nom}$, $P_{o nom}$ versus time (40IMX15). Source impedance according to ETS 300132-2 at $V_{i nom}$.

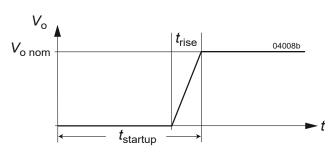


Fig. 5 Converter start-up and rise time



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Input Undervoltage Lockout

A special feature of these converters is the accurate undervoltage lockout protection, which protects against large currents caused by operation at low voltages. This ensures easier start-up in distributed power systems.

Tab. 3: Turn on and turn off voltage

| Model | Turi | n on | Turi | Unit | | |
|----------|------|------|------|------|---|--|
| | min | max | min | max | | |
| 20IMX15 | 7.5 | 8 | 7 | 7.5 | | |
| 24IMS15 | 12.5 | 13.5 | 12 | 13 | | |
| 40IMX15 | 12.5 | 13.5 | 12 | 13 | V | |
| 48IMS15 | 31.5 | 32.5 | 31 | 32 | | |
| 110IMY15 | 40 | 42.5 | 38 | 40.5 | 1 | |

Fuse and Reverse Polarity Protection

The built-in suppressor diode also provides for reverse polarity protection at the input by conducting current in the reverse direction. An external fuse is required to limit this current.

Table 4: Recommended external fuses

| Model | Fuse type |
|------------------|-----------|
| 20IMX15 | F 4.0 A |
| 24IMS15 | F 3.15 A |
| 40IMX15, 48IMS15 | F 2.0 A |
| 110IMY15 | F 1.0 A |

Input Transient Voltage Protection

A built-in suppressor diode provides effective protection against input transients, which typically occur in many installations.

Table 5: Built-in transient voltage suppressor

| Model | Breakdown voltage V _{BR nom} | Peak power at 1 ms <i>P</i> _P | Peak pulse current I _{PP} |
|----------|---|--|--|
| 20IMX15 | 40 V | 1500 W | 22 A |
| 24IMS15 | 53 V | 600 W | 7.7 A |
| 40IMX15 | 100 V | 1500 W | 9.7 A |
| 48IMS15 | 100 V | 600 W | 4.1 A |
| 110IMY15 | 168 V | 600 W | 0.5 A |

For very high energy transients as for example to achieve compliance to IEC/EN 61000-4-5 (see table *Electromagnetic Immunity*), an external inductor and a capacitor are required, see Fig. 6. The components are specified in table 6.

Table 6:

Components for external circuitry for IEC/EN 61000-4-5, level 3 compliance.

| Model | Inductor (L) | Capacitor (C) | Diode (D) |
|----------|--------------|---------------|-----------|
| 20IMX15 | 1 mH, 3.5 A | 330 µF, 63 V | 1.5k E47A |
| 24IMS15 | 1 mH, 2.5 A | 330 µF, 63 V | |
| 40IMX15 | 1 mH, 2.5 A | 330 µF, 100 V | |
| 48IMS15 | 1 mH, 1.5 A | 330 µF, 100 V | |
| 110IMY15 | 1 mH, 1.5 A | 330 µF, 200 V | |

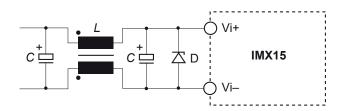


Fig. 6

Example for external circuitry to achieve better transient immunity and required EMC performance



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Electrical Output Data

We recommend connecting an external 1 µF ceramic capacitor across the output pins.

General conditions:

- $-T_{A} = 25$ °C, unless T_{C} is specified
- Shutdown pin and Trim or R pin left open-circuit (not connected)

Table 7a: Output data for single-output models -03-8RG¹ and 05-8RG¹

| Outp | out | | | | | 3.3 V | | | 5 V | | Unit |
|------------------|---|---|--|-------------------|------|-------|-------------------|------|------|-------------------|------------------|
| Char | racteristics | | Conditions | | min | typ | max | min | typ | max | |
| V。 | Output voltage | | $V_{\rm i nom}, I_{\rm o} = 0.5 I_{\rm o nom}$ | | 3.25 | | 3.35 | 5.05 | | 5.15 | V |
| , | | Output current $\frac{20IMX}{40IMX}$ V _{imin} to V _{imax} | | | | 4.5 | | | 3.5 | | |
| I o nom | 40IMX / 110IMY | | v _{imin} to v _{imax} | | | 4.5 | | | 3.5 | | A |
| I _{0 L} | Current limit ² | | $V_{\text{i nom}}$, T_{C} = 25 °C, $V_{\text{o}} \le 93$ | 8% V _。 | | 6.0 | | | 4.6 | | |
| ΔV_{o} | Line / load regulation | | $V_{\rm i min}$ to $V_{\rm i max}$, (0.1 to 1) $I_{\rm o}$ | nom | | | ±0.5 | | | ±0.5 | % |
| V | Output voltage | noico | $V_{i \min}$ to $V_{i \max}$ | 4 | | | 100 | | | 100 | m\/ |
| V _° | Oulput voltage | noise | $I_{o} = I_{o nom}$ | 5 | | | 60 | | | 60 | mV _{pp} |
| V _{oL} | Output overvolt | age limit. ³ | | | 115 | | 130 | 115 | | 130 | % |
| $C_{_{ m oext}}$ | Admissible capa | acitive load | | | 0 | | 4000 ⁶ | 0 | | 4000 ⁶ | μF |
| V _{od} | Dynamic load | Voltage deviation | $V_{i \text{ nom}}, I_{o \text{ nom}} \leftrightarrow 1/2 I_{o \text{ nom}}$ | | | ±250 | | | ±250 | | mV |
| t _{od} | regulation | Recovery time | IEC/EN 61204 | | | | 1 | | | 1 | ms |
| $\alpha_{_{Vo}}$ | Temperature coefficient $\Delta V_{o} / \Delta T_{c}$ | | $V_{\rm i min}$ to $V_{\rm i max}$, (0.1 to 1) $I_{\rm o}$ | nom | | | ±0.02 | | | ±0.02 | %/K |

¹ SR Models -8RG (synchr. rectifier) have a minimum case and operating temperature of -25 °C. If the revision is BA (or later), it is -40 °C.
 ² The current limit is primary side controlled. In the event of a sustained overload condition the thermal protection may cause the converter to shut down (automatic restart after cooling down).

³ The overvoltage protection is via a primary side second regulation loop. It is not tracking with R control.

⁴ BW = 20 MHz

⁵ Measured with a probe according to EN 61204

 6 For 110IMY15 models with synchr. rectifier, C_{_{o ext}} is limited to 1000 $\mu F.$



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Table 7b: Output data for -05-8R and -0503-8R models; general conditions as in table 7a

| Output | | | | | 5.1 V | | | 5.1 V / 3.3 | v | Unit |
|--|-----------------------------|---|---|------|-------|-------|-------------|-------------|-------------------|------------------|
| Character | istics | | Conditions | min | typ | max | min | typ | max | |
| V_{o}, V_{o1}, V_{o2} | Output voltage | | $V_{\rm inom}$, $I_{\rm o}$ = 0.5 $I_{\rm onom}$ | 5.05 | | 5.15 | 5.0 3.13 | | 5.12 3.46 | V |
| | | 20IMX | | | 2.3 | | | 2 x 1.35 | 1.64 | |
| I _{o nom} | Output current ¹ | 24IMS / 48IMS | $V_{i \min} - V_{i \max}$ | | 2.7 | | | 2 x 1.6 | 2.04 |] |
| | | 40IMX / 110IMY | | | 2.5 | | | 2 x 1.5 | 1.8 ⁴ | 1 |
| I _{o L} , I _{o1L} , I _{o2L} | | 20IMX | | | 3.2 | | | 2.7 3.8 | | A |
| I _{o L} , I _{o1L} , I _{o2L} | Current limit ² | 24IMS / 48IMS | $V_{\rm inom}, V_{\rm o} \le 93\% V_{\rm onom}$ | | 3.5 | | | 3.0 3.8 | | |
| I _{o L} , I _{o1L} , I _{o2L} | | 40IMX / 110IMY | | | 3.6 | 3.6 | | 2.9 4.0 | | |
| | | 5.1 V | $V_{\rm i min} - V_{\rm i max}$, (0.1 to 1) $I_{\rm o nom}$ | | | ±0.5 | | - | | |
| ΔV_{o} | Line / load regulation | 5.1 V | | | | - | | | +3/-5 | % |
| | regulation | 3.3 V | | | | - | | | ±4.5 |] |
| 14 | | -1 | $V_{i \min}$ to $V_{i \max}$ 5 | | | 70 | | | 80 | |
| <i>V</i> _{o1/2} | Output voltage n | loise | $I_{o} = I_{o \text{ nom}}$ 6 | | | 40 | | | 40 | mV _{pp} |
| V _{oL} | Output overvolta | ge limit. ⁷ | | 115 | | 130 | 115 | | 130 | % |
| C _{o ext} | Admissible capa | citive load | | 0 | | 4000 | 0 | | 4000 ³ | μF |
| V _{od} | Dynamic load | Voltage deviation | $V_{i \text{ nom}}, I_{o \text{ nom}} \leftrightarrow \frac{1}{2}I_{o \text{ nom}}$ | | | ±250 | | | ±150 | mV |
| t _{od} | regulation | Recovery time | IEC/EN 61204 | | | 1 | | | 1 | ms |
| α _{vo} | Temperature coe | efficient $\Delta V_{o} / \Delta T_{c}$ | $V_{\rm imin} - V_{\rm imax}$, (0.1 to 1) $I_{\rm onom}$ | | | ±0.02 | | | ±0.02 | %/K |

¹ Flexible load distribution: I_o max for one of the 2 outputs; however the total load should not exceed P_{o nom} specified in the table *Model Selection*.

² The current limit is primary-side controlled.

³ For -0503-models: total capacitive load of both outputs.

⁴ For -0503-models: Conditions for specified output. Other output loaded with constant current $I_0 = 0.5 I_{o nom}$.

⁵ BW = 20 MHz

⁶ Measured with a probe according to EN 61204

⁷ The overvoltage protection is via a primary side second regulation loop. It is not tracking with R control.



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Table 7c: Output data for double-output models; general conditions as in table 7a

| Out | put | | | | 2 x 5 | v | | 2 x 12 | V | 2 | 2 x 15 | v | 2 x 24 V | | | Unit |
|--------------------------|-------------------------------|---|---|------|---------|-------|-------|----------|-------|-------|----------|-------|----------|----------|-------|------------------|
| Cha | racteristics | | Conditions | min | typ | max | min | typ | max | min | typ | max | min | typ | max | |
| V | Output 1 | 24IMS / 48IMS | | 4.95 | | 5.05 | 11.88 | | 12.12 | 14.85 | | 15.15 | 23.70 | | 24.30 | |
| V _{o1} | | other models | | 4.95 | | 5.05 | 11.90 | | 12.10 | 14.88 | | 15.12 | 23.80 | | 24.20 | v |
| V | Output 2 | 24IMS / 48IMS | $V_{i \text{ nom}}, I_{o} = 0.5 I_{o \text{ nom}}$ | 4.94 | | 5.06 | 11.84 | | 12.16 | 14.80 | | 15.20 | 23.64 | | 24.36 | |
| V _{o2} | Output 2 | other models | | 4.94 | | 5.06 | 11.88 | | 12.12 | 14.85 | | 15.15 | 23.75 | | 24.25 | |
| , | Output current ¹ | 20IMX | V V | | 2 x 1.3 | 5 | | 2 x 0.6 | 5 | | 2 x 0.50 | C | | 2 x 0.32 | 2 | |
| I _{o nom} | | other models | $V_{i\min} - V_{i\max}$ | | 2 x 1.4 | | | 2 x 0.70 |) | | 2 x 0.56 | 6 | | 2 x 0.35 | 5 | |
| | 20IMX | | | | 3.0 | | | 1.6 | | | 1.3 | | | 0.85 | | A |
| I _{ol} | Current limit ^{2, 4} | 24IMS / 48IMS | V _{i nom} , | | 3.5 | | 1.9 | | | 1.6 | | | 0.95 | | | |
| 'o L | | 40IMX / 110IMY | $V_{o1} \leq 93\% V_{o nom}$ | 3.2 | | 1.7 | | | 1.4 | | | 0.90 | | | | |
| ΔV_{o1} | Line / load | Output 1 | V _{i min} -V _{i max} , I _{o nom} | ±1 | | | ±1 | | ±1 | | | | ±1 | | | |
| ΔV_{o2} | regulation ⁸ | Output 2 | | | | ±3 | | | +3 | | | +3 | | | +3 | % |
| V | Output voltage r | | V _{i min} -V _{i max} 5 | | | 80 | | | 120 | | | 150 | | | 240 | m\/ |
| <i>V</i> _{01/2} | Output voltage i | IOISE | $I_{o} = I_{o \text{ nom}}$ 6 | | | 40 | | | 60 | | | 70 | | | 120 | mV _{pp} |
| V _{oL} | Output overvolta | age limit. ^{7, 8} | Min. load 1% | 115 | | 130 | 115 | | 130 | 115 | | 130 | 115 | | 130 | % |
| $C_{_{ m oext}}$ | Admissible capa | acitive load ³ | | 0 | | 4000 | 0 | | 680 | 0 | | 470 | 0 | | 180 | μF |
| V _{od} | Dynamic load | Voltage deviation | V _{i nom} , | | ±250 | | | ±300 | | | ±300 | | | ±600 | | mV |
| t _{od} | regulation | Recovery time | $I_{o \text{ nom}} \leftrightarrow \frac{1}{2} I_{o \text{ nom}}$ | | | 1 | | | 1 | | | 1 | | | 1 | ms |
| α _{vo} | Temperature co | efficient $\Delta V_{o} / \Delta T_{c}$ | $V_{i\min} - V_{i\max}$, (0.1 to 1) $I_{o nom}$ | | | ±0.02 | | | ±0.02 | | | ±0.02 | | | ±0.02 | %/K |

¹ Flexible load distribution: Each output of double-output models is capable of delivering 75% of the total output power; however the total load should not exceed *P*_{o nom} specified in the table *Model Selection*.

² The current limit is primary-side controlled.

³ Measured with both outputs connected in parallel.

⁴ Conditions for specified output. Other output loaded with constant current $I_0 = 0.5 I_{0 \text{ nom}}$.

⁵ BW = 20 MHz

⁶ Measured with a probe according to EN 61204

⁷ The overvoltage protection is via a primary side second regulation loop, not tracking with Trim or R control.

⁸ At start-up occurs a short overshoot at the output.



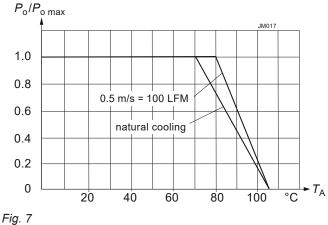
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Thermal Considerations

If a converter, mounted on a PCB, is located in free, quasi-stationary air (convection cooling) at the indicated maximum ambient temperature $T_{A max}$ (see table *Temperature specifications*) and is operated at its nominal input voltage and output power, the case temperature measured at the measuring point of case temperature T_c (see *Mechanical Data*) will approach the indicated value $T_{C max}$ after the warm-up phase. However, the relationship between T_A and T_C depends heavily on the conditions of operation and integration into a system. The thermal conditions are influenced by input voltage, output current, airflow, temperature of surrounding components and surfaces, and the properties of the printed circuit board. $T_{A max}$ is therefore only an indicative value.

Caution: The case temperature T_c measured at the measuring point of case temperature T_c (see *Mechanical Data*) may under no circumstances exceed the specified maximum value. The installer must ensure that under all operating conditions T_c remains within the limits stated in the table *Temperature specifications*.



Maximum output power versus ambient temperature

Overtemperature Protection

The converters are protected from possible overheating by means of an internal temperature monitoring circuit. It shuts down the converter above the internal temperature limit, and attempts to automatically restart in short intervals. This feature helps protect against excessive internal temperatures, which could occur during heavy overload conditions.

Output Overvoltage Protection

The output of single-output models as well as -0503- and -05-05-models are protected against overvoltage by a second control loop. In the event of an overvoltage on one of the outputs, the converter will shut down and attempt to restart in short intervals.

Doubel-output models (except -0503- and -05-05-models) are protected against overvoltage by a Zener diode across the second output. Under worst case conditions the Zener diode will become a short circuit. Since with double-output models both outputs track each other, the protection diode is only provided in one of the outputs. The main purpose of this feature is to protect against possible overvoltage, which could occur due to a failure in the control logic. This protection circuit is not designed to withstand externally applied overvoltages.

Short Circuit Behavior

The current limit characteristic shuts down the converter, whenever a short circuit is applied to its output. It acts self-protecting, and automatically recovers after removal of the overload condition (hiccup mode).

Parallel and Series Connection

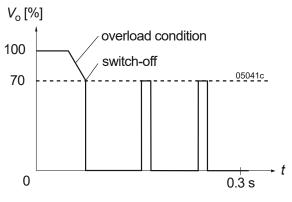
The outputs of one or several single- or double-output models can be connected in series without any precautions, taking into consideration that the highest output voltage should remain below 42 V to ensure that the output remains SELV.

Both outputs of the same converter with equal voltage (e.g. 5 V / 5 V) can be connected in parallel and will share their output currents equally. Parallel operation of single or double outputs of two or more converters with the same output voltage may cause start-up problems at initial start-up. This is only advisable in applications, where one converter is able to deliver the full load current as, for example, required in true redundant systems.

Note: If the 2nd output of double-output models (except 0503) is not used, connect it in parallel to the 1st output.



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Overload switch-off (hiccup mode); typical values

Typical Performance Curves

- General conditions:
- $-T_{A} = 25$ °C, unless T_{C} is specified.
- Shutdown pin left open-circuit.
- Trim or R input not connected.

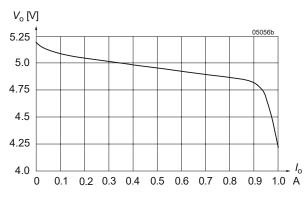


Fig. 9

 V_0 versus I_0 (typ) of converters with $V_0 = 5.1 V$ (110IMY15-05-8R)

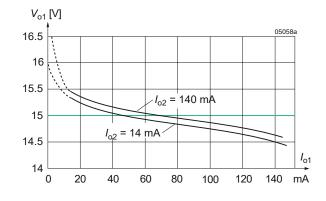
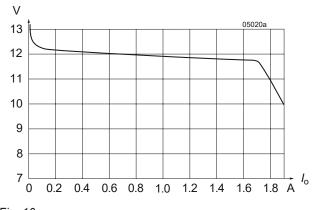


Fig. 11 Cross load regulation V_{01} versus I_{01} (typ.) for various I_{02} (2 x 12 V)



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Fig. 10 V versus I (typ.) of double-output models (2 x 12 V), with both outputs in parallel (110 IMY15-12-12-8)

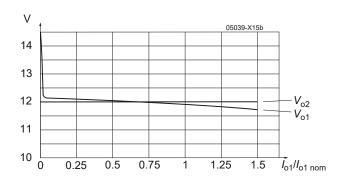


Fig. 12 Flexible load distribution on double-outputs models with option R (110IMY15-12-12-8R): Load variation from 0 to 150% of I_{o1 nom} on output 1; output 2 loaded with 50% of I_{o2 nom}.



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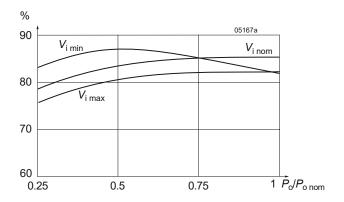
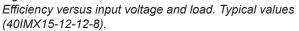


Fig. 13a



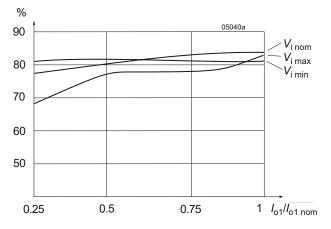
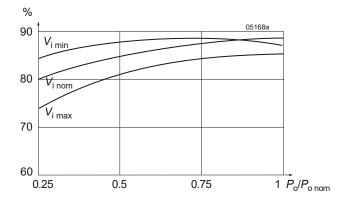


Fig. 13c

Efficiency versus input voltage and load. Typical values (48IMS15-12-12-9)



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Fig. 13b

Efficiency versus input voltage and load. Typical values (110IMY15-12-12-8)



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Auxiliary Functions

Shutdown Function

The outputs of the converters may be enabled or disabled by means of a logic signal (TTL, CMOS, etc.) applied to the shut- down pin. If this function is not required, the shut-down pin should be left open-circuit.

- Converter operating: 2.0 to 20 V
- Converter disabled: -10 to +0.7 V

Adjustable Output Voltage

- R input for single-output models and -0503-models
- Trim input for double-output models

As a standard feature, the single- and double-output models offer adjustable output voltage(s) by using the control input R or Trim. If the control input is left open-circuit, the output voltage is set to $V_{o nom}$. For output voltages $V_o > V_{o nom}$, the minimum input voltage V_{imin} (see *Electrical Input Data*) increases proportionally to $V_o/V_{o nom}$.

Single-output models with synchronous rectifier (G):

The R input is referenced to the secondary side of the converter. Adjustment of the output voltage is possible by means of an external resistor connected between the R pin and either Vo+ or Vo–.

Note: For models with synchronous rectifier the logic for V_o adjustment differs from other models with R input.

Table 8: V_o versus V_{ext} approximate values

| V _{o nom} | Typ. values o | of R _{ext1} | Typ. values of R _{ext2} | | |
|--------------------|-----------------------------|--|----------------------------------|--|--|
| [V] | V_{o} [% of $V_{o nom}$] | $\boldsymbol{R}_{\text{ext1}}$ [k Ω] | V_{o} [% of $V_{o nom}$] | $\boldsymbol{R}_{\text{ext2}}$ [k Ω] | |
| 3.3 | 90 | 0.47 | 100 | ∞ | |
| | 95 | 2.7 | 105 | 15 | |
| | 100 | ∞ | 110 | 6.8 | |
| 5.1 | 90 | 3.3 | 100 | ∞ | |
| | 95 | 8.2 | 105 | 9.1 | |
| | 100 | ∞ | 110 | 3.9 | |

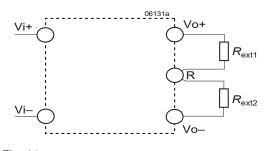


Fig. 14 Output voltage control for single-output models with synchronous rectifier.

All other models fitted with R-input:

The R input is referenced to the secondary side of the converter. Adjustment of the output voltage is possible by means of either an external resistor or a voltage source.

a) Adjustment by means of an external resistor \textbf{R}_{ext} .

Depending upon the value of the required output voltage, the resistor shall be connected:

either: Between the R pin and Vo– to achieve an output voltage adjustment range of $V_{o} \approx 80$ to 100 % of $V_{o nom}$.

$$R_{\text{ext1}} \approx 4 \text{ k}\Omega \cdot \frac{V_{\text{o}}}{V_{\text{o nom}} - V_{\text{o}}}$$

or: Between the R pin and Vo+ to achieve an output voltage range of $V_0 \approx 100$ to 105% of $V_{0 \text{ nom}}$.

$$R_{\text{ext2}} \approx 4 \text{ k}\Omega \cdot \frac{(V_{o} - 2.5 \text{ V})}{2.5 \text{ V} \cdot (V_{o}/V_{o \text{ nom}} - 1)}$$

b) Adjustment by means of an external voltage V_{ext} between Vo- and R pin.

The control voltage range is 1.96 to 2.62 V and allows for adjustment in the range of $V_{o} \approx 80$ to 105% of $V_{o nom}$.

$$V_{\text{ext}} \approx \frac{V_{\text{o}} \bullet 2.5 \text{ V}}{V_{\text{o nom}}}$$

Attempting to adjust the output below this range will cause the converter to shut down (hiccup mode).

Note: Applying an external control voltage >2.75 V may damage the converter.



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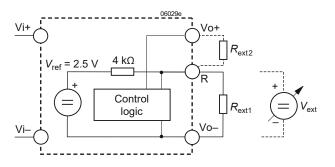


Fig. 15

Output voltage control for single-output models, -0503-models, and double-output models by means of the R input.

Double-output models with Trim input:

The Trim input is referenced to the primary side. The figure below shows the topology. Adjustment is possible trough either an external resistor or an external voltage source V_{ext} .

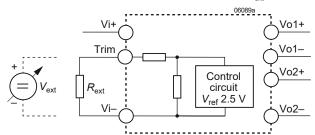


Fig. 16

Output voltage control for double-output models (with Trim input) by means of the Trim input.

a) Adjustment by means of an external resistor R_{ext} :

Programming of the output voltage by means of an external resistor R_{ext} is possible within 100 to 105% of $V_{o nom}$. R_{ext} should be connected between the Trim pin and Vi–. Connection of R_{ext} to Vi+ may damage the converter. Table 9 below indicates suitable resistor values for typical output voltages under nominal conditions ($V_{i nom}$, $I_o = 0.5 I_{o nom}$) with either parallel-connected outputs or equal-load conditions on both outputs.

Table 9: R_{ext} for $V_{o} > V_{o nom}$; approximate values ($V_{i nom}$, $I_{o1, 2} = 0.5 I_{o1/2 nom}$)

| V _o [%V _{o nom}] | R _{ext} [kΩ] |
|---------------------------------------|-----------------------|
| 105 to 108 (107 typically) | 0 |
| 105 | 1.15 |
| 104 | 5.6 |
| 103 | 12 |
| 102 | 27 |
| 101 | 68 |
| 100 | ∞ |

Table 10: V_{o} versus V_{ext} for V_{o} = 75 to 105% $V_{o \text{ nom}}$; typical values ($V_{i \text{ nom}}$, $I_{o1/2}$ = 0.5 $I_{o1/2 \text{ nom}}$)

| V _o [%V _{o nom}] | V _{ext} [V] |
|---------------------------------------|----------------------|
| ≥105 | 0 |
| 102 95 | 1.6 |
| 95 | 4.5 |
| 85 | 9 |
| 75 | 13 |

b) Adjustment by an external voltage source V_{ext} :

For programming the output voltage in the range 75 to 105% of $V_{o nom}$, a source V_{ext} (0 to 20 V) is required, connected between the Trim pin and Vi–. Table 10 above indicates values V_o versus V_{ext} (nominal conditions $V_{i nom}$, $I_o = 0.5 I_{o nom}$), with either parallel-connected outputs or equal load conditions on both outputs. Applying a control voltage >20 V will set the converter into a hiccup mode. Direct paralleling of the Trim pins of parallel connected converters is possible.



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Electromagnetic Compatibility (EMC)

A suppressor diode together with an input filter forms an effective protection against high input transient voltages, which typically occur in many installations, but especially in battery-driven mobile applications.

Electromagnetic Immunity

Table 11: Electromagnetic immunity (type tests)

| Phenomenon | Basic Standard | Level | Coupling mode | Value applied | Waveform | Source imped. | Test procedure | In oper. | Perf. crit. ³ |
|--|---------------------|----------------------------------|--------------------|---|---|-----------------|---|-------------|-----------------------------|
| Electrostatic discharge (to case) R-pin open | IEC/EN 61000-4-2 | 2 4 | contact discharge | ± 6000 V _p | 1/50 ns | 330 Ω 150 pF | 10 pos. & 10 neg. discharges | yes | A |
| Electromagnetic | IEC/EN | X ⁵ | antenna | 20 V/m | AM 80% / 1 kHz | N/A | 80 – 1000 MHz | yes | Α |
| field | 61000-4-3 | 6 | antenna | 20 V/m 10 V/m 5 V/m 3V/m | AM 80% / 1 kHz | N/A | 800 – 1000 MHz 1400 – 2000 MHz 2000 – 2700 MHz 5100 – 6000 MHz | yes | A |
| Electrical fast transients / burst | IEC/EN 61000-4-4 | 3 7 | direct +i/–i ² | 2000 V _p | bursts of 5/50 ns; 5 kHz repet. rate; 15 ms burst; 300 ms period | 50 Ω | 60 s positive 60 s negative transients per coupling mode | yes | A |
| Surges | IEC/EN 61000-4-5 | 3 ¹ | +i/-i ² | 2000 V _p 1000 V _p | 1.2 / 50 µs | 42 Ω 0.5 μF | 5 pos. & 5 neg. surges | yes | А |
| RF Conducted immunity | IEC/EN 61000-4-6 | 3 ⁸ 2 ⁹ | +i/-i ² | 10 Vrms ⁸ 3 Vrms ⁹ | AM 80% / 1 kHz | 150 Ω | 0.15 – 80 MHz | yes | А |

¹ External components required; see Fig. 6 and table 6. Corresponds to the railway standard EN 50121-3-2:2016, table 3.3.

² i = input

³ A = normal operation, no deviation from specs., B = temporary deviation from specs possible.

⁴ Corresponds to the railway standard EN 50121-3-2:2016, table 5.3

⁵ Corresponds to the railway standard EN 50121-3-2:2016, table 5.1.

⁶ Corresponds to the railway standard EN 50121-3-2:2016, table 5.2.

⁷ Corresponds to the railway standard EN 50121-3-2:2016, table 3.2.

⁸ Corresponds to the railway standard EN 50121-3-2:2016, table 3.1.

⁹ Valid for 24IMS15 and 48IMS15



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Conducted Emissions

Compliance with EN 55011, group 1, class A, and EN 50121-4 was tested with filter on site 7 - fig.6 values according table 6. The results are shown in fig. 17, 18, 19.

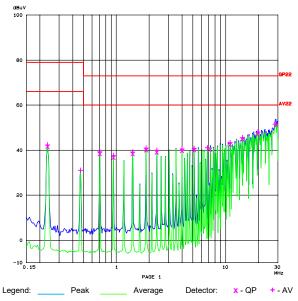
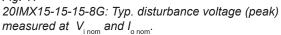


Fig. 17



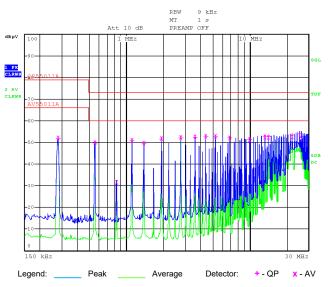


Fig. 18 40IMX15-12-1.

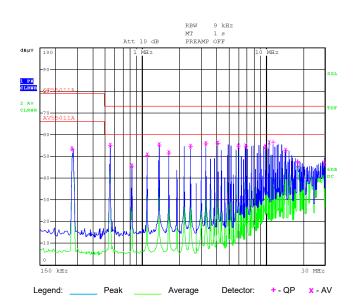
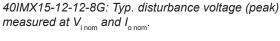


Fig. 19 110IMY15-12-12-8 : Typ. disturbance voltage (peak) measured at $V_{i \text{ nom}}$ and $I_{o \text{ nom}}$.





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Immunity to Environmental Conditions

Table 9: Mechanical and climatic stress

| Test | method | Standard | Test Conditions | | Status | |
|----------------------|------------------------------------|--------------------------------|---|------------------------------|----------------------------|--|
| Са | Damp heat | IEC/EN 60068-2-78 | Temperature: | 40 ±2 °C | Converter | |
| | steady state | MIL-STD-810D section 507.2 | Relative humidity: | 93 +2/-3 % | Converter not operating | |
| | | | Duration: | 56 days | notopolaang | |
| Ea | Shock | IEC/EN 60068-2-27 | Acceleration amplitude: | 50 g _n = 490 m/s² | | |
| | (half-sinusoidal) | MIL-STD-810D section 516.3 | Bump duration: | 11 ms | Converter operating | |
| | | | Number of bumps: | 18 (3 in each direction) | operating | |
| Fc | Vibration | IEC/EN 60068-2-6 | Acceleration amplitude: | 0.35 mm (10 – 60 Hz) | | |
| (sinusoidal) | | | 5 g _n = 49 m/s² (60 - 2000 Hz) | Converter | | |
| | | Frequency (1 Oct/min): | 10 – 2000 Hz | operating | | |
| | | | Test duration: | 7.5 h (2.5 h in each axis) | | |
| Fda | Random vibration | IEC/EN 60068-2-35 | Acceleration spectral density: | 0.05 g _n ²/Hz | | |
| | wide band | | Frequency band: | 20 to 500 Hz | Converter | |
| reproducibility high | | Acceleration magnitude: | 4.9 g _{n rms} | operating | | |
| | | | Test duration: | 3 h (1 h in each axis) | | |
| Kb | Salt mist test | EN 50155:2007, clause 12.2.10, | Temperature: | 35 ^{±2} °C | Converter | |
| | (sodium chloride NaCl solution) | class ST2 | Duration: | 16 h | not operating | |

Temperatures

Table 13: Temperature specifications, valid for air pressure of 800 to 1200 hPa (800 to 1200 mbar)

| Model | | | -9 | | | -8 | | | Unit |
|----------------|---------------------|--------------------------|------|-----|-----|---------------------|-----|-----|------|
| Charact | eristics | Conditions | min | typ | max | min | typ | max | |
| T _A | Ambient temperature | Operational ¹ | - 40 | | 71 | - 40 ^{2,3} | | 85 | |
| T _c | Case temperature | | - 40 | | 95 | - 40 ^{2,3} | | 105 | °C |
| T _s | Storage temperature | Non operational | - 55 | | 85 | - 55 | | 85 | |

¹ See Thermal Considerations

² Start-up at –55 °C, except models with synchronous rectifier ^{SR}

³ –25 °C for all models with synchronous rectifier ^{SR} with Revision lower than BA.

Reliability

Table 14: MTBF

| Ratings | | Ground benign | Groun | d fixed | Ground mobile | Unit |
|-----------------|---------------|-------------------------------|-------------------------------|------------------------|------------------------|------|
| Model | Standard | <i>T</i> _c = 40 °C | <i>T</i> _c = 40 °C | T _c = 70 °C | T _c = 50 °C | |
| 20IMX15-12-12-8 | MIL-HDBK-217F | 697 000 | 366 000 | 229 000 | 312 000 | |
| 20IMX15-15-15-8 | Bellcore | 2 345 000 | 1 172 000 | 632 000 | 317 000 | 1 |
| 48IMS15-05-05-9 | MIL-HDBK-217F | 535 000 | 283 000 | 179 000 | 245 000 | h |
| 110IMY15-05-8R | MIL-HDBK-217F | 485 000 | 255 000 | 167 000 | 223 000 | 1 |
| | Bellcore | 1 547 000 | 774 000 | 394 000 | 206 000 | |



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Mechanical Data

Dimensions in mm (inches). Tolerances ±0.3 mm, unless otherwise noted.

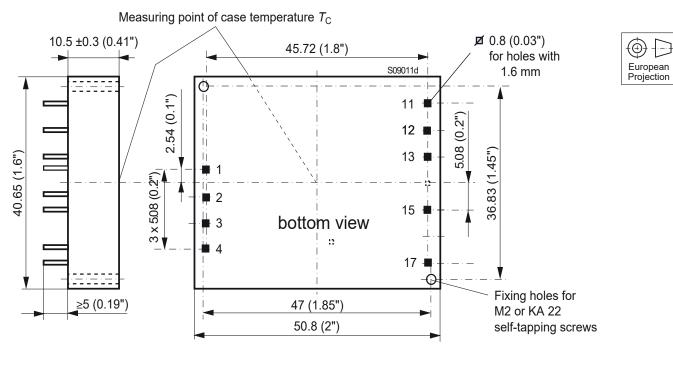


Fig. 20 Case IMX15, IMS15, IMY15 Material: Fortron 1140L6 Weight: 35 g

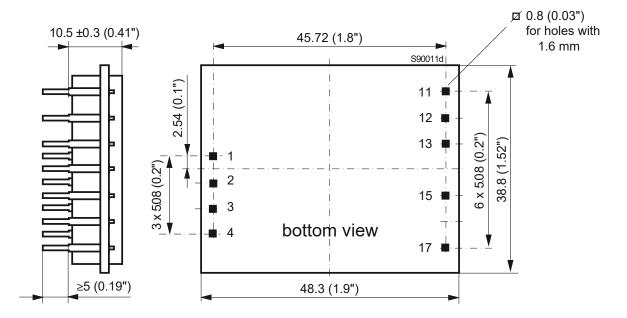


Fig. 21 Open frame (option Z) Weight: 26 g



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10005 11 = 12 = 13 =

> ° 15 □ ° 17 □ O

Safety and Installation Instruction

Pin Allocation

Table 15: Pin allocation

| Pin | Single | Dual | -0503- |
|-----|--------|------|--------|
| 1 | Vi+ | Vi+ | Vi+ |
| 2 | Vi- | Vi- | Vi- |
| 3 | - | Trim | n.c. |
| 4 | SD | SD | SD |
| 5 | - | - | - |
| 6 | - | - | - |
| 11 | - | Vo1+ | Vo2+ |
| 12 | - | Vo1- | Go |
| 13 | Vo+ | Vo2+ | Vo1+ |
| 15 | Vo- | Vo2- | Go |
| 17 | R | n.c. | R |
| | | | |

Installation Instructions

Installation must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings and segregation requirements of the end-use application.

The converter must be connected to a secondary circuit.

Do not open the converter.

Ensure that a converter failure (e.g., by an internal short-circuit) does not result in a hazardous condition.

Input Fuse

To prevent excessive current flowing through the input supply lines in case of a malfunction an external fuse should be installed in a non-earthed input supply line; see *table 4*.

Standards and Approvals

The converters are approved according to the latest edition of UL/CSA 60950-1 and comply with IEC/EN 62368-1 standard. 110IMY models are fitted with a CE mark.

The converters have been evaluated for:

- Building in
- Supplementary insulation input to output, based on their maximum input voltage (IMX15, IMS15)
- Reinforced insulation input to output, based on their maximum input voltage (IMY15 models)
- Pollution degree 2 environment (not option Z)
- Connecting the input to a secondary circuit subject to a maximum transient rating of 1500 V (IMX15, IMS15)
- Connecting the input to a secondary circuit subject to a maximum transient rating of 2500 V (IMY15)

The converters are subject to manufacturing surveillance in accordance with the above mentioned UL standards and with ISO9001:2015.

Railway Applications

To comply with railway standards, all components are coated with a protective lacquer (except option Z).

Protection Degree and Cleaning Liquids

The protection degree of the converters is IP 40, except open-frame models (option Z).

In order to avoid possible damage, any penetration of cleaning fluids should be prevented, since the power supplies are not hermetical sealed.

However, open-frame models (option Z) leave the factory unlacquered; they may be lacquered by the customer, for instance together with the mother board. Cleaning liquids are not permitted – except washing at room temperature with isopropyl alcohol. If necessary, the mother board must be cleaned, before fitting the open-frame converter.

Note: Cleaning liquids may damage the adhesive joints of the ferrite cores.



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Isolation

The electric strength test is performed in the factory as routine test in accordance with EN 62911 and UL/CSA 60950-1. The Company will not honor any warranty claims resulting from incorrectly executed electric strength field tests.

Table 16: Electric strength test voltages

| Characteristics | | Input to Output | Output to Output | Unit | |
|--------------------------------------|---------------------|------------------|------------------|------|------|
| | IMS15 | IMX15 | IMY15 | | |
| Factory test 10 s | 1.2 | 1.5 ¹ | 3.0 | 0.1 | kVAC |
| Equivalent DC test voltage | (1.7) | (2.1) | (4.2) | 0.15 | kVDC |
| Insulation resistance (500 VDC) | >100 | > 100 | > 100 | - | MΩ |
| Partial discharge extinction voltage | Consult the Company | | - | kV | |

1.5 kVAC according to UL/CSA 60950-1, sect. 6.2, Telecom equipment; type test with 1.5 kVAC / 60 s (IEE 802.3). IMX15 units produced before 2013 were tested only with 1.2 kVAC.

² The test voltage between outputs is not applied as routine test.

Description of Options

Table 17: Survey of options

| Option | Function of Option | Characteristics |
|--------|-------------------------|--|
| -9 | Temperature range, NFND | See table Temperatures specifications |
| i | Inhibit | Replaces the shutdown function with inverted logic |
| Z | Open frame | See Mechanical Data |

Option -9 versus -8

IMX15 and IMY15 models with -9 (not for new designs) have a limited temperature range. Standard models with suffix -8 are rated up to $T_{A} = 85$ °C; see table *Temperature specifications*.

Option i: Inhibit

Replaces the shut-down function with inverted logic.

The output(s) of the converter may be enabled or disabled by means of a logic signal (TTL, CMOS, etc.) applied to the inhibit pin. No output voltage overshoot will occur, when the converter is turned on. If the inhibit function is not required, the inhibit pin should be connected to Vi– to enable the output (active low logic, fail safe).

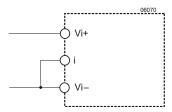


Fig. 23

If the inhibit function is not used, the inhibit pin should be connected to Vi-.

Voltage on pin i:

| Converter operating: | -10 V to 0.8 V |
|----------------------|---|
| Converter inhibited: | 2.4 V to V_{imax} (<75 V) or pin i left open-circuit. |

Option Z

Open frame and not lacquered. This option can be chosen for mounting onto a mother board, which is subject to be lac- quered. See *Protection Degree and Cleaning Liquids* (page 18).

Option G or -G

Products RoHS-compliant for all 6 substances. See RoHS Compliant Models (page 3) for the correct denotation.



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Accessories

Supports are available for chassis mounting HZZ00626-G (previously CMBIMX15) and for DIN-rail mounting HZZ00628-G (previously DMBIMX15).

They exhibit the connectors and allow for placing different external components. For details see <u>Mounting Supports Data Sheet</u> on our website.



Fig. 24 DIN-rail mounting support HZZ00628-G (previously DMBIMX15)

NUCLEAR AND MEDICAL APPLICATIONS - These products are not designed or intended for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems.

TECHNICAL REVISIONS - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.



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