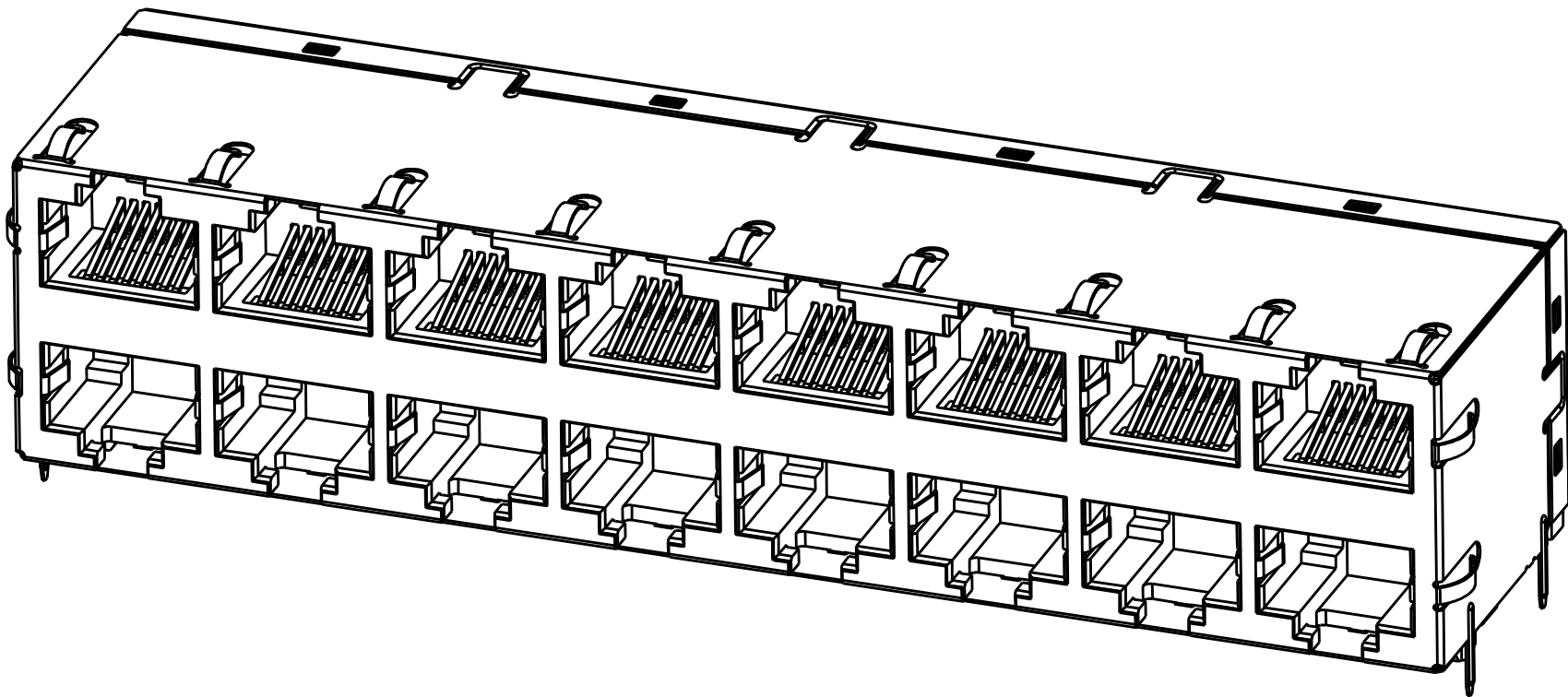
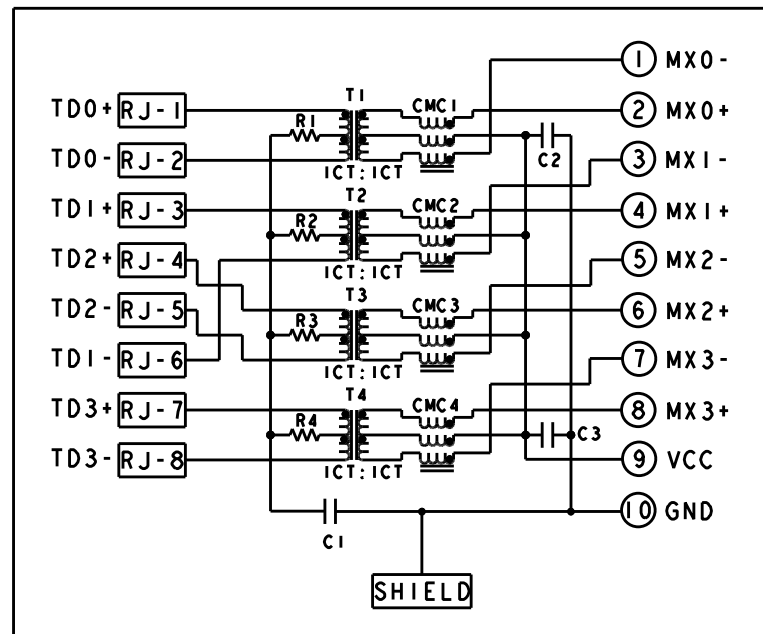




S8G56 GIGABIT CIRCUIT

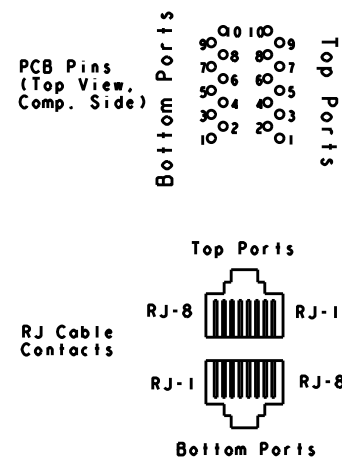
Top and Bottom Ports



C1 = 1000pF, 2kV Capacitor
C2 - C3 = 10nF, 50V Capacitors
R1-R4 = 75 Ohms, 1/16 W, Resistors

Pin Designations

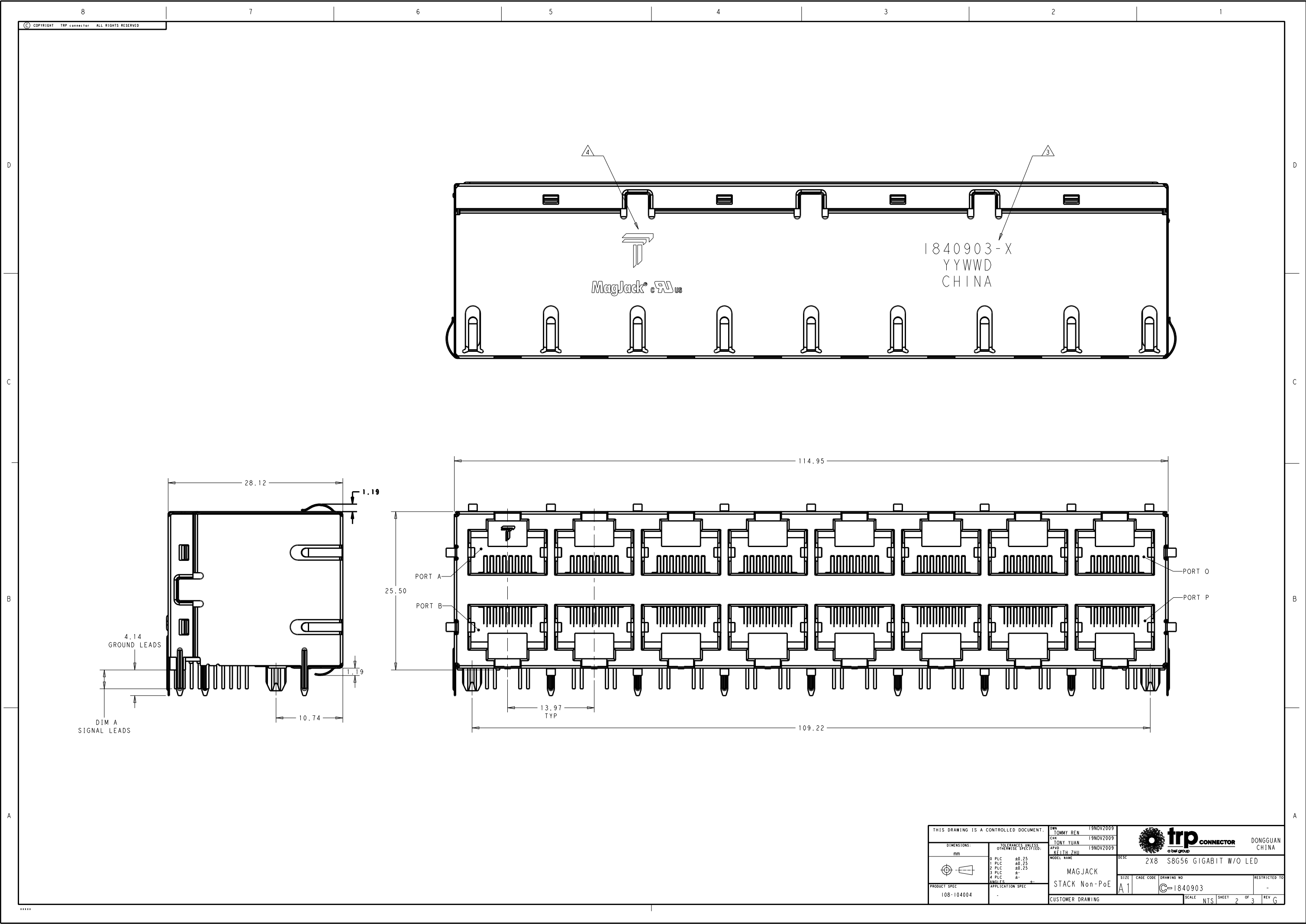
(Repeat for each vertical pair of ports.)



- MATERIALS:**
PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.
CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
UNDERPLATE WITH SELECT 1.27um MIN GOLD AT MATING INTERFACE
AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
ALL PC BOARDS: HIGH TEMPERATURE PCB,TG>170°C
- MAGNETICS**
APPLICATION: 10/100/1000 BASE-T
IMPEDANCE: 100 OHMS
TURNS RATIO (CHIP: CABLE): 1:1 ALL FOUR PAIRS
OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
ALL FOUR PAIRS BI-DIRECTIONAL
PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF
500V/SEC AND WITH ALL PORTS CONNECTED.
- PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE**
LOCATED IN APPROXIMATE AREA SHOWN. DATE CODE: YYWW "YY" IS YEAR, "WW"
IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1
- TRP LOGO AND AGENCY APPROVAL LOGO ARE**
LOCATED IN APPROXIMATE AREA SHOWN.
- OPERATING TEMP:** FROM 0°C TO +70°C.
- RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.**
- INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND**
SUPPORT AUTO-MDI/MDIX.
- DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.**
- BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE**
GREATER THAN 5.08mm.
- THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS,**
PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

3.56	1840903-4
3.30	1840903-3
2.79	1840903-2
3.04	1840903-1
DIM A	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN TOMMY REN 19NOV2009	 trp CONNECTOR a bel group		NUMBER	
DIMENSIONS: mm		CHK TONY YUAN 19NOV2009			DONGGUAN CHINA	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD KEITH ZHU 19NOV2009	DESC 2X8 S8G56 GIGABIT W/O LED		RESTRICTED TO	
 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES		MODEL NAME MAGJACK STACK Non-PoE				
PRODUCT SPEC 108-104004		APPLICATION SPEC		SIZE A1	CAGE CODE C=1840903	DRAWING NO -
		CUSTOMER DRAWING		SCALE	NTS	SHEET 1 OF 3
						REV G



8 7 6 5 4 3 2 1
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4

3

MagJack®

1840903-X
YYWWD
CHINA

4.14
GROUND LEADS

DIM A
SIGNAL LEADS

28.12

1.19

10.74

25.50

PORT A

PORT B

PORT O

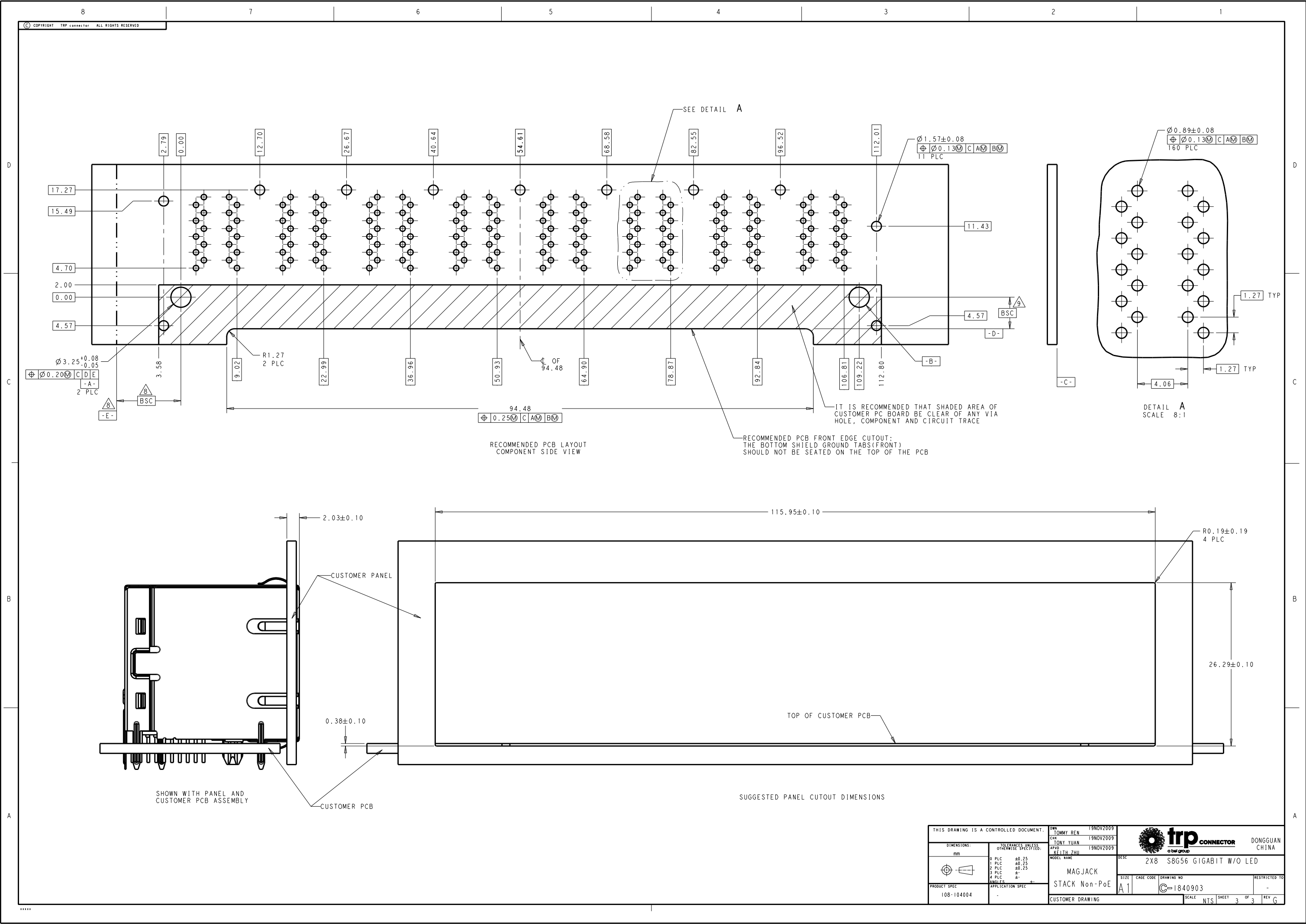
PORT P

13.97
TYP

114.95

109.22

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN TOMMY REN 19NOV2009		 trp CONNECTOR a bel group		DONGGUAN CHINA	
DIMENSIONS:		CHK TONY YUAN 19NOV2009					
mm		APVD KELTH ZHU 19NOV2009					
		TOLERANCES UNLESS OTHERWISE SPECIFIED:		MODEL NAME		DESC	
		0 PLC ±0.25		MAGJACK		2X8 S8G56 GIGABIT W/O LED	
		1 PLC ±0.25		STACK Non-PoE		SIZE	
		2 PLC ±0.25		A1		CAGE CODE	
		3 PLC ±		DRAWING NO		RESTRICTED TO	
PRODUCT SPEC		APPLICATION SPEC		1840903		-	
108-104004		-		CUSTOMER DRAWING		SCALE	
						NTS	
						SHEET	
						2	
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THIS DRAWING IS A CONTROLLED DOCUMENT.				DWN TOMMY REN 19NOV2009		 trp CONNECTOR a bel group		DONGGUAN CHINA					
DIMENSIONS: mm				CHK TONY YUAN 19NOV2009									
TOLERANCES UNLESS OTHERWISE SPECIFIED:				APVD KEITH ZHU 19NOV2009									
				0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ±		MODEL NAME		DESC 2X8 S8G56 GIGABIT W/O LED					
ANGLES				MAGJACK		SIZE		CAGE CODE		DRAWING NO		RESTRICTED TO	
PRODUCT SPEC 108-104004				APPLICATION SPEC		STACK Non-PoE		A1		C=1840903		-	
CUSTOMER DRAWING				SCALE		NTS		SHEET 3 of 3		REV		G	

Mouser Electronics

Authorized Distributor

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Bel:

1840903-2