

B70SP24020A/B

24V Output DC/DC Converter, Box Type Package



FEATURES

- Wide input voltage range, 32~96V
- 500W Output
- Full Load Efficiency up to 91.5% @48Vin and 72Vin
- Parallel Connection of multiple units
- Box type package
 - Package Dimension: 198.0x113.0x45.0mm (7.80"x4.45"x1.77")
- Operating Temperature Range 40°C to +90°C
- Input Reverse Polarity Protection
- Minimized Inrush current
- Input UVLO, Output OCL, Short circuit protection, OVP, OTP
- Enable on/off
- 2250VDC Isolation
- IP67 protection(With fully assembled mating connector)
- RoHs Compliant
- ISO 9001, ISO 14001 certified manufacturing facility
- IEC/EN/UL60950-1, IEC/EN/UL62368-1, CE Mark
- EMC compatible: EN12895-2015, EN55011, EN55014-2, CISPR11 ClassA

The B70SP24020(A code without parallel function, B code with parallel function), a wide input voltage range of 32~96V, and single isolated output converter, is the latest product offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. Such box type DCDC converter can provide 500W, 24V regulated DC output voltage with full load efficiency up to 91.5% @72Vin; The B70SP24020 offers input UVLO, output over current limit, short circuit, output over voltage, over temperature, and input reverse polarity protections. It has parallel function; and allows a wide operating temperature range of -40°C to +90°C. With creative design technology and optimization of component placement, this converter possess outstanding electrical and thermal performance, as well as high reliability under extremely harsh operating conditions. The B70SP24020 meets IP67 protecion (refer to "water protection level" specification)

| Input Characteristics | | | | | | | | |
|--|-----------------------|------|------------|----------------|------|--|--|--|
| Item | Condition | Min. | Тур. | Max. | Unit | | | |
| Continuous Input Voltage | | 32 | 48 | 96 | VDC | | | |
| Input Under-Voltage Lockout, Turn-On Voltage Threshold | | 29 | 30 | 31 | VDC | | | |
| Input Under-Voltage Lockout, Turn-Off Voltage Threshold | | 27 | 28 | 29 | VDC | | | |
| Lockout Hysteresis Voltage | | 1 | 2 | 3 | VDC | | | |
| Maximum Input Current | Vin=32V, 100% Load | | 18 | 19 | А | | | |
| No. Logal Innut Current | Vin=48V | | 80 | | mA | | | |
| No-Load Input Current | Vin=72V, 80V | | 50 | | mA | | | |
| Off converter input current | Vin=48V, enable off | | 20 | | mA | | | |
| Reflected input ripple current | Vin=48V, peak to peak | | | 0.2 | A | | | |
| Max Reverse Polarity Input Voltage | | | | 96 | VDC | | | |
| Max Inrush current | | | | 10 | A | | | |
| Internal Input Fuse | | | 500V/30A F | ast-acting fus | e | | | |



| Item | Cond | itions | Min. | Typ. | Max. | Unit |
|---------------------------------------|---|------------------------------|----------|--------|-------|-------|
| Operating Output Current Range | | | 0 | ·)p. | 21 | A |
| | | lo=0 | 0 | 24.5 | 21 | V |
| | for A code | lo=21 | | 24.5 | | V |
| Output Voltage Set Point | fan Daarda | lo=0 | | 25.6 | | V |
| | for B code lo=21 | | | 23.5 | | V |
| | Vin=48V, lo=21/ 20MHz bandwidth, Co tanta | o=1μF ceramic, 10μF alum | | 120 | 240 | mV |
| | Vin=48V, Io=21A, RM Co=1µF ceramic | | | 35 | 70 | mV |
| Output Voltage Ripple and Noise, | Vin=72V, 80V, lo=2 20MHz bandwidth, Co tanta | o=1µF ceramic, 10µF | | 140 | 280 | mV |
| | Vin=72V, 80V, Io= bandwidth, Co=1µF ce | , , | | 45 | 90 | mV |
| Output Current Limit | bandwidth, Co=1µF ceramic, 10µF tantalum t Current Limit | | | | | А |
| Current share accuracy | only for B co | de , full load | | 10 | 15 | % |
| Start-up delay time by Vin | - , , | from Vin=Turn-on to 10%Vo | | 650 | 800 | mS |
| Start-up delay time by Enable | Vin=48V, full load,f | | | 250 | 400 | mS |
| Rise time | From 10%V | 'o to 90%Vo | | 160 | 300 | mS |
| Output Voltage Protection | | | | | 30 | V |
| | Positive voltage step, dynamic, 0.14 | 15.75A to 10.5A load | | 250 | 500 | mV |
| Output Voltage Current Transient | Nagetive voltage step dynamic, 0.14 | , 10.5A to 15.75A load | | 250 | 500 | mV |
| Maximum Output Capacitance | | | | | 10000 | μF |
| Output voltage start up overshoot | | | | | 3 | % |
| Efficiency @ 100% Load | Vin= | :48V | | 91.5 | | % |
| Efficiency @ 100% Load | Vin= | 72V | | 91.5 | | % |
| Efficiency @ 60% Load | Vin= | :48V | | 91.5 | | % |
| Efficiency @ 60% Load | Vin= | :72V | | 90.5 | | % |
| General Characteristics | | | | | | |
| Item | Cond | itions | Min. | Тур. | Max. | Unit |
| | Input to Output | | <u>-</u> | 2250 | VDC | |
| Isolation Voltage, | Ouput t | | | 550 | VDC | |
| solation Resistance, Input to Output | | | 10 | | | MΩ |
| solation Capacitance, Input to Output | | | | 5000 | | pF |
| Switching Frequency | | | | 175 | | KHz |
| Operating life | Baseplate @ 40°C | | | 131400 | Hours | |
| MTBF | 72Vin, Baser | | 143642 | | | Hours |
| Wight | | | 140042 | 1300 | | g |



| Parameter | Conditions | Min. | | Max. | Unit | |
|-----------------------------|---|---|-----|------|------------|--|
| Storage Temperature Range | | -40 | | +105 | °C | |
| Operating Temperature Range | Baseplate | -40 | | +90 | °C | |
| Over Temperature Protection | NTC Temperature | | 118 | | °C | |
| Humidity (non condensing) | | | | 95 | % rel. H | |
| Thermal Shock Test | Temperature range:-40~125 °C Thermal rate: 20°C /min Dwell time : 60mins Total cycle: 300cycles | ISO 16750-4 | | | | |
| Submersion test | Total cycles : 10 Dwell time at Tmax : 1h Transition duration : <20s Testfluid : De-ionized water,5% NaCl Water Temperature:< 4°C Immersion Time : 5 mins | ISO 16750-4 | | | | |
| Water Protection Level | With fully assembled mating connector | IP67 | | | | |
| Vibration | Sine wave 1.Frequency (Hz) amplitude acceleration 5 – 9 HZ ±15 mm 15-200 HZ 10G 2. Sweep rate 1 Oct / min. 3. Duration 50 Cycles. | IEC 60068-2-6: Sine-wave vibration, test Fc | | | n, test Fc | |
| Mechanical Shock | 50G/11ms 3Shocks for each direction | IEC 60068-2-27: Shock, half sine, test Ea: | | | | |
| Bump | 40G/6ms 1000 Shocks for each direction | IEC 60068-2-29: Bump, test Eb: | | | | |
| Salt Spray Test | Operating /no load 1. Salt Spray Concentration:5%; 2. Test Temperature:35°C; 3. Volume of spray:1~2ml/hour/80cm2; 4. PH:6.5~7.2; 5. Test Time:96hours 6. Tolerance: Salt Spray Concentration (±1%); Test Temperature: ± 2°C; | IEC 60068-2-29. Buildp, test ED. | | | | |
| Emission | 30-1000MHz 34-45dBuV/m | EN12895-2015 | | | | |
| mmunity | 20V/m /27-1000MHz AM; 3V/m /1-2GHz AM; 1V/m /2-2.7GHz AM EN12895-2015, | EN61000-4-3 | | | | |
| ESD | Direct: ±8KV; Air: ±15KV EN12895-2015, | EN61000-4-2 | | | | |

1 Specifications typical at Ta=+25°C, nominal input voltage and rated full load output current unless otherwise noted.

2 Specifications are subject to change without notice.



ELECTRICAL CURVES

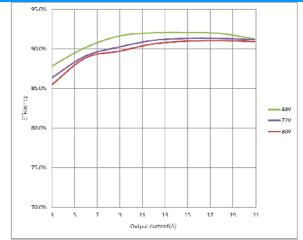


Figure 1: Efficiency vs. Output current @ Vin=48V,72V,80V

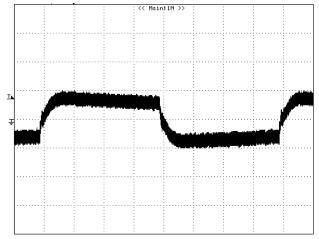


Figure 3: Dynamic response to load step 10.5A to 15.75A with 0.1A/uS slew rate at 72Vin CH1:VOUT, 100mV/div, 500uS/div

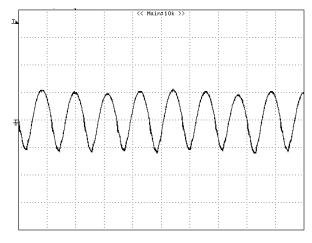


Figure 5: Output ripple & noise at 72 Vin, 21A lout CH1:VOUT, 20mV/div, 5uS/div

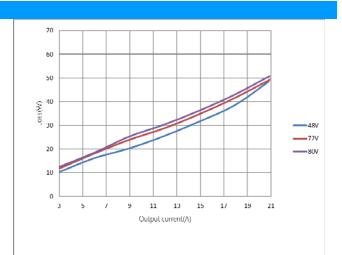


Figure 2: Loss vs. Output current @ Vin=48V,72V,80V

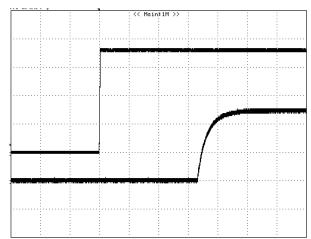


Figure 4: Vout start up with Vin on at 72Vin,21A lout, TOP:VIN, 20V/div, 200mS/div BOTTOM: VOUT, 10V/div, 200mS/div

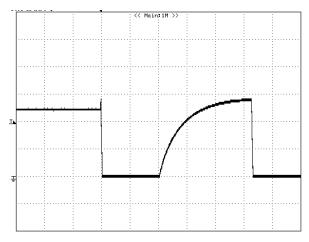


Figure 6: Output over voltage protection at 72Vin,21A lout CH1:VOUT, 10V/div, 100mS/div



ELECTRICAL CURVES (continous)

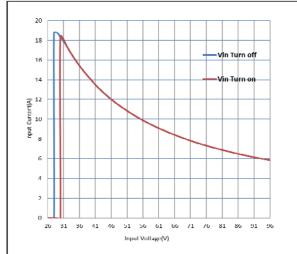


Figure 7: Input current vs. Input voltage @Full load

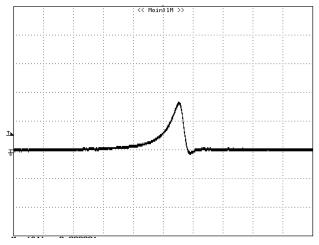


Figure 9: Inrush current @ Vin=72V CH1:lin, 2A/div, 200nS/div; Max current 3.4A,

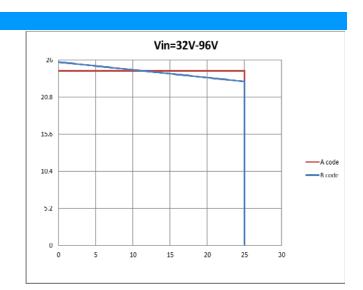


Figure 8: Output voltage vs. Output current OCL Performance



FEATURES DESCRIPTIONS

Output Over-Current Limit and Short Protection

The modules include internal output over-current limit (OCL) and short circuit protection (SCP) circuits, the OCL set point is lower than that of the SCP; The response of SCP circuit is much fast than that of the OCL circuit. The slowly increase of the output current will let module enter OCL protection when the current exceeds the OCL set point, while the fast increase of the output current will let module enter SCP when the current exceeds the SCP set point.

When the modules enter OCL protection, the output voltage will decrease while the output current is kept constant, the output voltage will soft start to set point when the overload condition is removed.

The module will enter hiccup mode when it triggers the SCP set point. The module will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is removed.

Output Over-Voltage Protection

The power module includes an internal output over-voltage protection(OVP) circuit, which monitors the voltage on the output terminals. If this voltage exceeds the OVP set point, the module will shut down, and then restart after a fixed delay time (hiccup mode), please refer to figure6 for detail.

Over-Temperature Protection

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the preset temperature threshold the module will shut down, and all components will not exceed their absolute maximum temperature ratings. The module will restart after the temperature is within specification.

Remote On/Off

B70SP24020A/B has Enable control function. This Enable PIN is designed on the primary side of converter, the converter will turn on when the Enable PIN connected to VIN+ or floating, and turn off when the Enable PIN connected to VIN-.

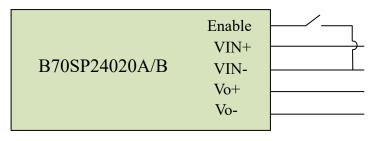


Figure 10: suggested Enable connection

Input Reverse Voltage Protection

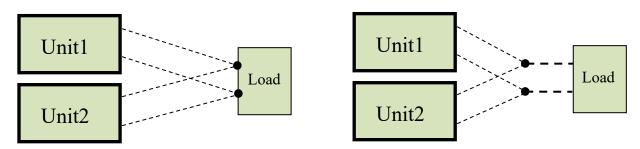
The B70SP24020 has input reverse voltage protection, the standoff voltage for the reverse protection shall be not less than -96V.

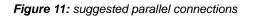


DESIGN CONSIDERATIONS

Parallel connection of multiple units (only for B code)

Two units parallel operation is verified, please contact Delta if more than two units need to be paralleled. While parallelling multiple units, the impedance of the cables from unit to junction point of each unit should be within $\pm 5\%$ of each other. Before all the parallel module finished start up, the total load current should be lower than the rated current of 1 module.



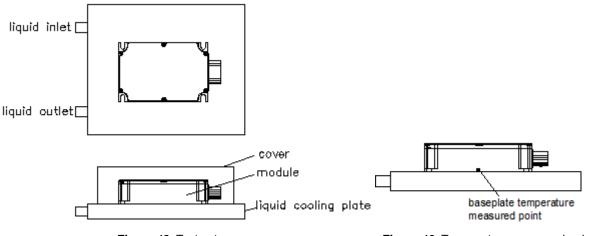


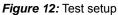


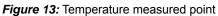
THERMAL CONSIDERATION

The thermal curve is based on the test setup shown as figure 12. The module is mounted on an liquid cooling plate and cooled by cooling liquid(It can also be air cooling with heatsink at client side).

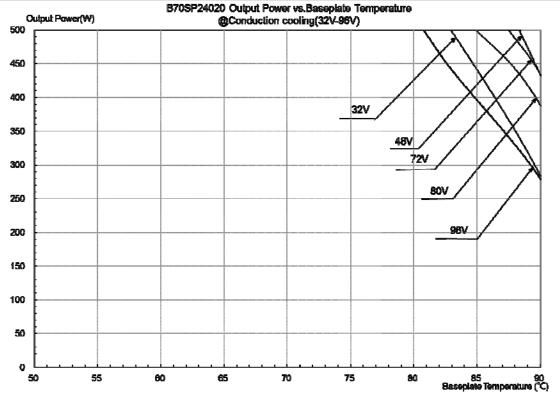
Figure13 shows the location to monitor the temperature of the module's baseplate. The baseplate temperature in thermal curve is a reference for customer to make thermal evaluation and make sure the module is operated under allowable temperature. (Thermal curves shown in Figure14 are based on different input voltage).

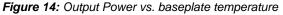






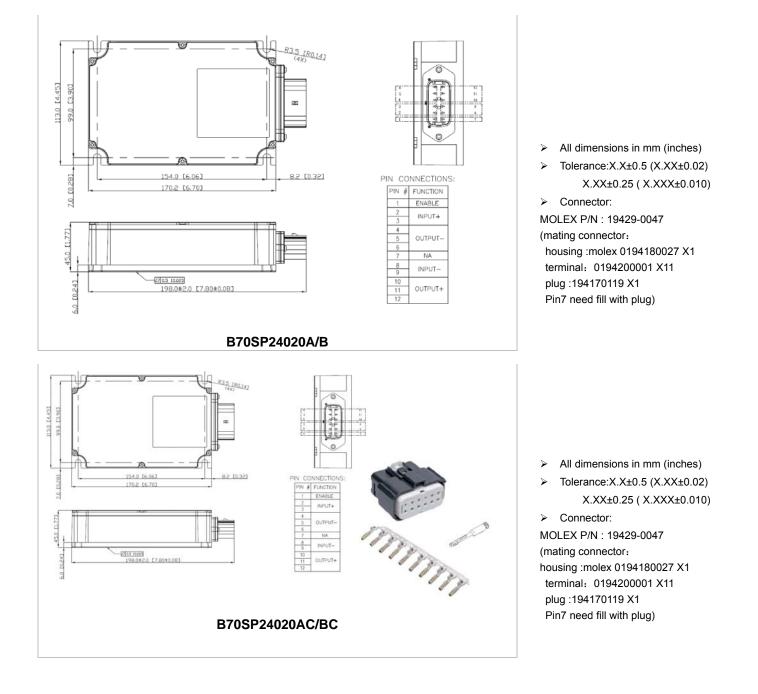








Mechanical Drawing



| PHYSICAL OUTLINE | | | | | | |
|------------------|--|--|--|--|--|--|
| Case Size | : 198x113.0x45.0mm (7.80"x4.45"x1.77") | | | | | |
| Case Material | : ADC12 | | | | | |



| Part Numbering System | | | | | | | | |
|-----------------------|------------------|-------------------------|-------------------|-------------------|-------------------|-------------|---------------------------|-----------------------|
| В | 70 | S | Р | 240 | 20 | A | | С |
| Form Factor | Input Voltage | Number of Outputs | Product Series | Output Voltage | Output Current | Option Code | | Option Fitting |
| B - Box | 70 - 32V~96V | S - Single | P - High | 240 - 24V | 20 - 21A | | | Connector Kit |
| | | | Power | | | А | Without parallel function | With mating connector |
| | | | | | | В | With parallel function | With mating connector |

| Recommended Part Number | | | | | | | | |
|-------------------------|---------|-----|------|-------------------------|-------|--|--|--|
| Input Voltage Range | Input | | Outp | EFF @72VIN 100% LOAD | | | | |
| B70SP24020A/AC/B/BC | 32V~96V | 19A | 24V | 21A | 91.5% | | | |

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