Grayhill

SERIES 65 Optical Encoder Interface



FEATURES

- Interfaces with all Grayhill and Most Standard Quadrature Optical Encoders
- Power Reduction of Up to 75-90% in Optical Encoder Use Through Power Management Feature
- User Selectable Output Modes: Magnitude/Direction, Up/Down, Standard Quadrature
- Simplified Microprocessor Interface Reduces Design Time
- Debounces Encoder Integral Pushbutton Switch
- Ideal for Battery Powered Applications that Include Optical Encoders



DESCRIPTION

The GH65C11-X is designed to receive input from standard quadrature optical encoders. The power management feature allows power to the encoder to be applied only during sampling intervals, thus conserving power (especially advantageous in battery powered systems). Sample rate is a nominal 4K per second allowing high speed quadrature input. The optical encoder interface can operate in 1 of 3 user-selectable output modes. These

modes are: magnitude and direction, up and down count, and standard quadrature. Debouncing of an integral pushbutton switch within the optical encoder can also be accomplished.

| lame | Type* | Description | | | | | |
|----------------|-------|---|-----------------|------------|----------------------------------|--|--|
| ИО, М1 | 1 | Mode selection input pins | | | | | |
| , DD | Р | 3–6 Vdc power source | | | | | |
| / DD RES | I | Reset pin, normally connected to V _{DD} | | SOIC/DIP | | SSOP | |
| , ss | Р | GND, 0v nominal power return | | □ 1 □ 2 | 18 □ ØBO/DN/DR 17 □ ØAO/UP/MG | M0 🗆 1 M1 🗆 2 | 20 🗖 ØBO/DN/DF 19 🗖 ØAO/UP/MG |
| ŠŠI, ØBI | I | Phase A and B quadrature input pins | V _{DD} | 3 | 16 🗆 RC | V _{DD} ∃3 | 18 🗆 RC |
| SWI | I | Switch input pin | | <u> </u> | 15 DNC | RES = 4 | 17 D NC |
| SWO | 0 | Debounced switch output pin | - | □ 5 □ 6 | 14 □ V _{ID} 13 □ PW | V _{ss} □ 5 V _{ss} □ 6 | 16 □ V _{DD} 15 □ V _{DD} |
| IC | 0 | No connect, this pin must be left unconnected | ØBI | 7 | 12 🗆 PW | ØAI □ 7 | 14 🗅 PW |
| W | 0 | Power source for encoder power management | SWI | 日8 | 11 PW 10 PW | ØBI □8 SWI □9 | 13 🗆 PW 12 🗖 PW |
| C | I/O | RC oscillator pin | 3000 | | 10 5 F W | SWO = 10 | 11 PW |
| BO/DN/DR | 0 | Phase B, down, direction, mode conditional output pin | | | | | |
| AO/UP/MG | 0 | Phase A, up, magnitude, mode conditional output pin | | | | | |

ORDERING INFORMATION

GH65C11-X-YY Temperature: -Packaging: C = Commercial (0° C to 70° C) PD = 18 lead 300 mil wide Plastic DIP $N = Industrial (-40^{\circ} C to 85^{\circ} C)$ SO = 18 lead 300 mil wide gull wing SOIC SS* = 20 lead SSOP

 * The SS package style is not available in the -40 $^{\circ}\text{C}$ to 85 $^{\circ}\text{C}$ temperature range.

Available from your local Grayhill Distributor. For prices and discounts, contact a local Sales Office, an authorized local Distributor or Grayhill.

For additional information about the use of the GH65 interface chips with optical encoders request Grayhill Application Note #719.

Mouser Electronics

Authorized Distributor

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Grayhill:

GH65C11-C-SO GH65C11-N-SO GH65C11-C-SS GH65C11-N-PD GH65C11-C-PD GH65C11-C-SS-1